



U74LVC1G34

CMOS IC

SINGLE BUFFER GATE

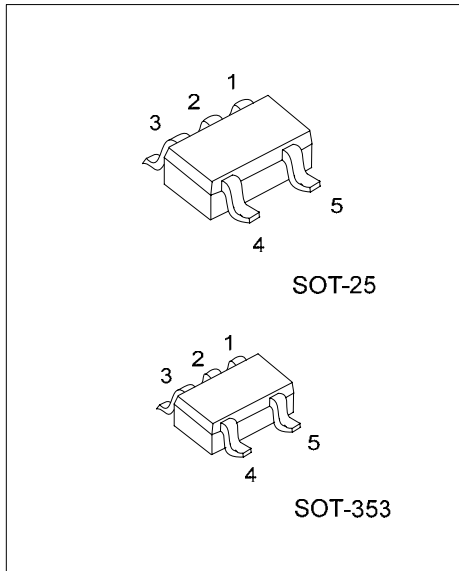
DESCRIPTION

The U74LVC1G34 is a single buffer, it provides the function $Y = A$.

This device has power-down protective circuit, preventing device destruction when it is powered down.

FEATURES

- * Operation voltage range: 1.65~5.5V
- * Low power dissipation: $I_{CC}=10\mu A(\text{Max})$
- * 24mA output drive ($V_{CC}=3.3V$)
- * High noise immunity
- * Power down protection



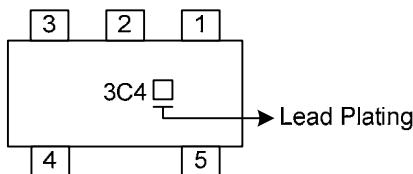
*Pb-free plating product number:
U74LVC1G34L

ORDERING INFORMATION

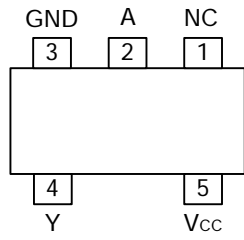
Ordering Number		Package	Packing
Normal	Lead Free Plating		
U74LVC1G34L-AF5-R	U74LVC1G34L -AF5-R	SOT-25	Tape Reel
U74LVC1G34L-AL5-R	U74LVC1G34L -AL5-R	SOT-353	Tape Reel

<p>U74LVC1G34L-AF5-R</p> <p>(1) Packing Type (2) Package Type (3) Lead Plating</p>	<p>(1) R: Tape Reel (2) AF5: SOT-25, AL5: SOT-353 (3) L: Lead Free Plating, Blank: Pb/Sn</p>
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MARKING



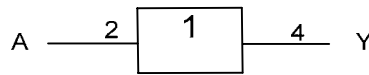
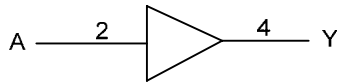
■ PIN CONFIGURATION



■ FUNCTION TABLE (each gate)

INPUT	OUTPUT
A	Y
H	H
L	L

■ LOGIC DIAGRAM (positive logic)



IEC logic symbol

■ ABSOLUTE MAXIMUM RATINGS (unless otherwise specified)(Note 1)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V _{CC}	-0.5~6.5	V
Input Voltage	V _{IN}	-0.5~6.5	V
Output Voltage(active mode)	V _{OUT}	-0.5~V _{CC} +0.5	V
Output Voltage(power-down mode)	V _{OUT}	-0.5~6.5	V
Input Clamp Current(V _{IN} <0)	I _{IK}	-50	mA
Output Clamp Current(V _{OUT} <0)	I _{OK}	-50	mA
Output Current	I _{OUT}	±50	mA
V _{CC} or GND Current	I _{CC}	±100	mA
Storage Temperature	T _{STG}	-65 ~ +150	

Note 1. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

2. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	V _{CC}	Operating	1.65		5.5	V
		Data retention only	1.5			
Input Voltage	V _{IN}		0		5.5	V
Output Voltage	V _{OUT}		0		V _{CC}	V
Input Transition Rise or Fall Rate	t _R , t _F	V _{CC} = 1.8V±0.15V, 2.5V±0.2V			20	ns/V
		V _{CC} = 3.3V±0.3V			10	ns/V
		V _{CC} = 5V±0.5V			5	ns/V
Operating Temperature	T _A		-40		85	

■ STATIC CHARACTERISTICS (T_A=25)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
High-Level Input Voltage	V _{IH}	V _{CC} = 1.65V~1.95V	0.65×V _{CC}			V
		V _{CC} = 2.3V~2.7V	1.7			
		V _{CC} = 2.7V~3.6V	2			
		V _{CC} = 4.5V~5.5V	0.7×V _{CC}			
Low-Level Input Voltage	V _{IL}	V _{CC} = 1.65V~1.95V			0.35×V _{CC}	V
		V _{CC} = 2.3V~2.7V			0.7	
		V _{CC} = 2.7V~3.6V			0.8	
		V _{CC} = 4.5V~5.5V			0.3×V _{CC}	
Output Voltage High-Level	V _{OH}	V _{CC} = 1.65V~5.5V, I _{OH} =-100μA	V _{CC} -0.1			V
		V _{CC} = 1.65V, I _{OH} =-4mA	1.2			
		V _{CC} = 2.3V, I _{OH} =-8mA	1.9			
		V _{CC} = 3V, I _{OH} =-16mA	2.4			
		V _{CC} = 3V, I _{OH} =-24mA	2.3			
Output Voltage Low-Level	V _{OL}	V _{CC} = 1.65V~5.5V, I _{OL} =100μA			0.1	V
		V _{CC} = 1.65V, I _{OL} =4mA			0.45	
		V _{CC} = 2.3V, I _{OL} =8mA			0.3	
		V _{CC} = 3V, I _{OL} =16mA			0.4	
		V _{CC} = 3V, I _{OL} =24mA			0.55	
		V _{CC} = 4.5V, I _{OL} =32mA			0.55	
Input Leakage Current	I _{I(LEAK)}	V _{CC} = 0~5.5V, V _{IN} =5.5V or GND			±1	μA

■ STATIC CHARACTERISTICS(Cont.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Leakage Current	$I_{O(LEAK)}$	$V_{CC}=0V, V_{IN}$ or $V_{OUT}=5.5V$			±10	μA
Quiescent Supply Current	I_q	$V_{CC}=1.65V\sim 5.5V, V_{IN}=5.5$ or GND, $I_{OUT}=0$			1	μA
Additional Quiescent Supply Current	ΔI_q	$V_{CC}=3V\sim 5.5V$, One input at $V_{CC}-0.6V$, other inputs at V_{CC} or GND			500	μA
Input Capacitance	C_{IN}	$V_{CC}=3.3V, V_{IN}=V_{CC}$ or GND		3.5		pF

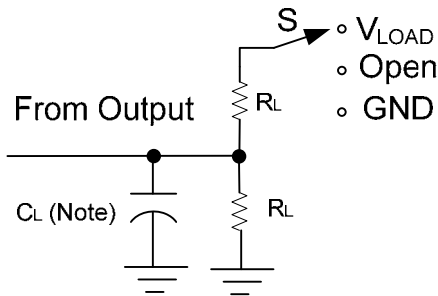
■ DYNAMIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Propagation Delay From Input (A or B) to Output(Y)	t_{PLH}/t_{PHL}	$V_{CC}=1.8V \pm 0.15V, C_L=15$ pF	2		9.9	ns
		$V_{CC}=2.5V \pm 0.2V, C_L=15$ pF	1.5		6	ns
		$V_{CC}=3.3V \pm 0.3V, C_L=15$ pF	1		3.5	ns
		$V_{CC}=5V \pm 0.5V, C_L=15$ pF	1		2.9	ns
		$V_{CC}=1.8V \pm 0.15V, C_L=30$ or 50 pF	3.2		8.6	ns
		$V_{CC}=2.5V \pm 0.2V, C_L=30$ or 50 pF	1.5		4.4	ns
		$V_{CC}=3.3V \pm 0.3V, C_L=30$ or 50 pF	1.5		4.1	ns
		$V_{CC}=5V \pm 0.5V, C_L=30$ or 50 pF	1		3.2	ns

■ OPERATING CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Power Dissipation Capacitance	Cpd	$V_{CC}=1.8V, f=10$ MHz		16		pF
		$V_{CC}=2.5V, f=10$ MHz		16		pF
		$V_{CC}=3.3V, f=10$ MHz		16		pF
		$V_{CC}=5V, f=10$ MHz		18		pF

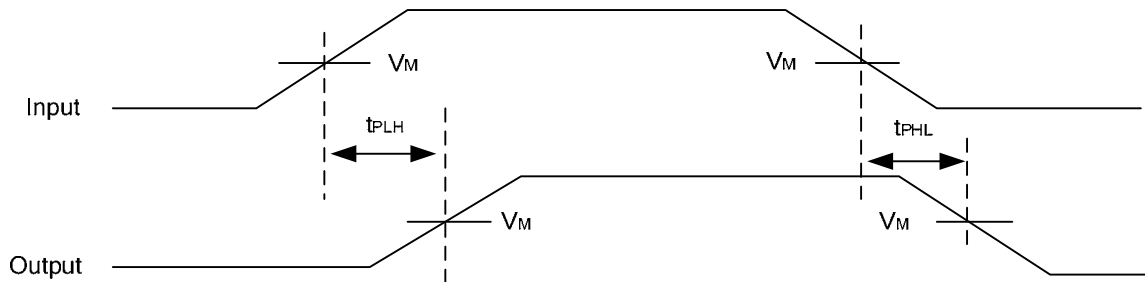
■ TEST CIRCUIT AND WAVEFORMS



TEST	S
t_{PLH}/t_{PHL}	Open
t_{PLZ}/t_{PZL}	V_{LOAD}
t_{PHZ}/t_{PZH}	GND

Note: C_L includes probe and jig capacitance.

V_{CC}	V_{IN}	t_R, t_F	V_M	V_{LOAD}	C_L	R_L	V
$1.8V \pm 0.15V$	V_{CC}	$\leq 2ns$	$V_{CC}/2$	$2 * V_{CC}$	15pF	1M	0.15V
$2.5V \pm 0.2V$	V_{CC}	$\leq 2ns$	$V_{CC}/2$	$2 * V_{CC}$	15pF	1M	0.15V
$3.3V \pm 0.3V$	3 V	$\leq 2.5ns$	1.5V	6V	15pF	1M	0.3V
$5V \pm 0.5V$	V_{CC}	$\leq 2.5ns$	$V_{CC}/2$	$2 * V_{CC}$	15pF	1M	0.3V



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