

## UTD413

Power MOSFET

P-CHANNEL  
ENHANCEMENT MODE

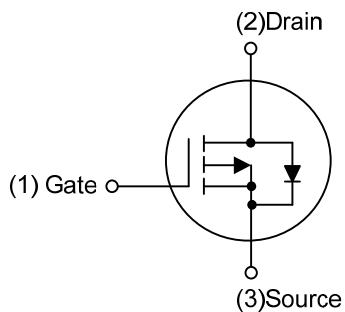
## ■ DESCRIPTION

The **UTD413** can provide excellent  $R_{DS(ON)}$  and low gate charge by using UTC's advanced trench technology. The **UTD413** is well suited for high current load applications with the excellent thermal resistance of the TO-252 package. Standard Product **UTD413** is Pb-free.

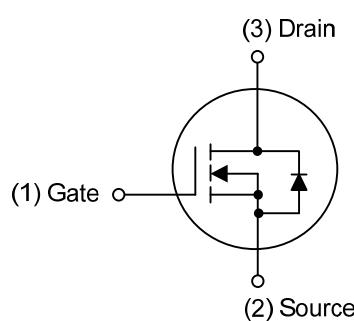
## ■ FEATURES

- \*  $R_{DS(ON)} \leq 45 \text{ m}\Omega @ V_{GS}=-10V, I_D = -12A$
- \*  $R_{DS(ON)} \leq 69 \text{ m}\Omega @ V_{GS}=-4.5V, I_D = -8.0A$
- \* Low capacitance
- \* Low gate charge
- \* Fast switching capability
- \* Avalanche energy specified

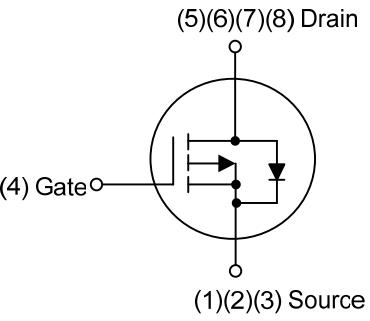
## ■ SYMBOL



SOT-223/TO-251/TO-252D



SOT-23



PDFN5x6

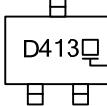
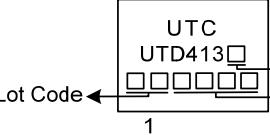
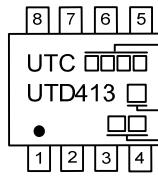
## ■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UTD413L-AA3-R	UTD413G-AA3-R	SOT-223	G	D	S	-	-	-	-	-	Tape Reel
UTD413L-AE3-R	UTD413G-AE3-R	SOT-23	G	S	D	-	-	-	-	-	Tape Reel
UTD413L-TN3-R	UTD413G-TN3-R	TO-252	G	D	S	-	-	-	-	-	Tape Reel
UTD413L-TND-R	UTD413G-TND-R	TO-252D	G	D	S	-	-	-	-	-	Tape Reel
UTD413L-S08-R	UTD413G-S08-R	SOP-8	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

UTD413G-AA3-R	(1)Packing Type  (2)Package Type  (3)Green Package	(1) R: Tape Reel  (2) AA3: SOT-223, AE3: SOT-23, TN3: TO-252, TND: TO-252D, S08: SOP-8  (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING

PACKAGE	MARKING
SOT-223	 <p>L: Lead Free G: Halogen Free Date Code 1</p>
SOT-23	 <p>L: Lead Free G: Halogen Free</p>
TO-252 TO-252D	 <p>Lot Code ← UTC UTD413 → L: Lead Free G: Halogen Free Date Code 1</p>
SOP-8	 <p>8 7 6 5 UTC      Date Code UDT413      L: Lead Free •      G: Halogen Free 1 2 3 4      Lot Code</p>

■ ABSOLUTE MAXIMUM RATINGS ( $T_c = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	$V_{DSS}$	-40	V
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V
Continuous Drain Current	$I_D$	-12	A
Pulsed Drain Current	$I_{DM}$	-30	A
Avalanche Energy	$E_{AS}$	37	mJ
Power Dissipation	SOT-223	$P_D$	2.4
	SOT-23		1.25
	TO-252/TO-252D		40
	SOP-8		1.7
Junction Temperature	$T_J$	+150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature

3.  $L = 0.1\text{mH}$ ,  $I_{AS} = -27\text{A}$ ,  $V_{DD} = -25\text{V}$ ,  $R_G = 25\Omega$ , Starting  $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	SOT-223	$\theta_{JA}$	83
	SOT-23		177
	TO-252/TO-252D		50
	SOP-8		90
Junction to Case	SOT-223	$\theta_{JC}$	52
	SOT-23		100
	TO-252/TO-252D		3.125
	SOP-8		73

Note: When surface mounted to an FR4 board using minimum recommended pad size. (Cu. Area 0.412 sq in), Steady State.

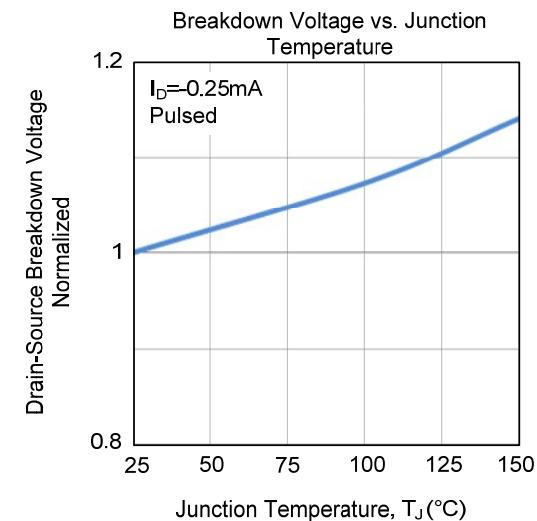
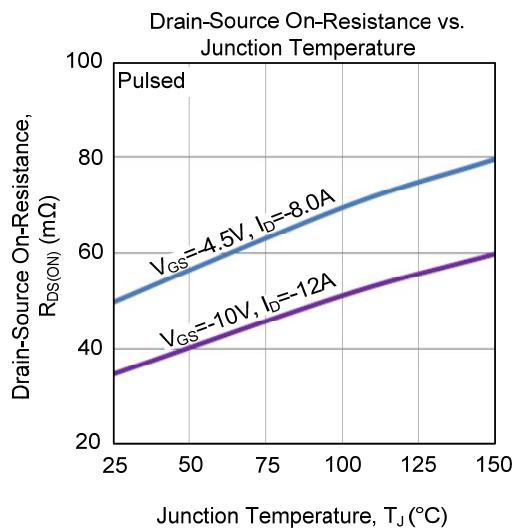
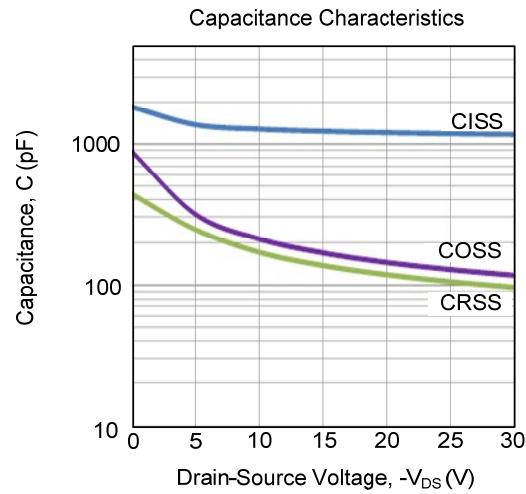
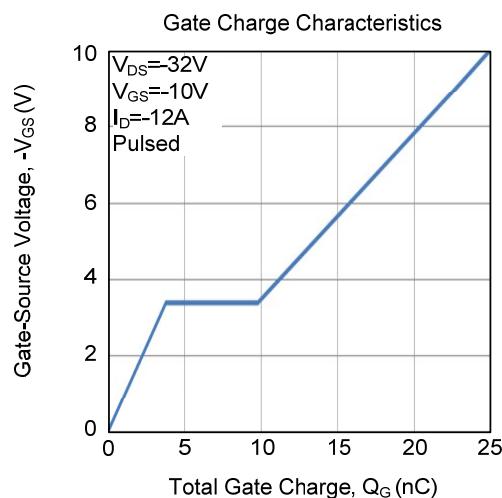
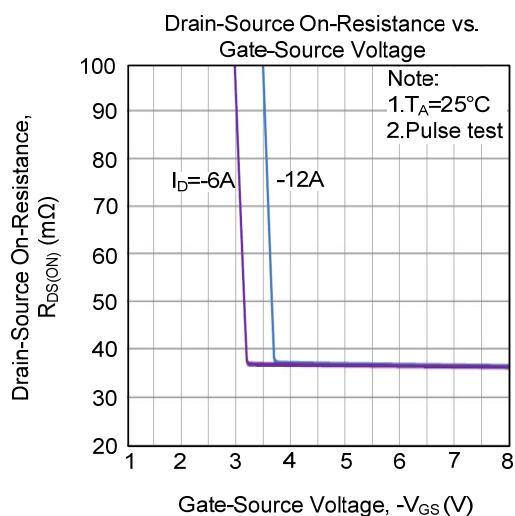
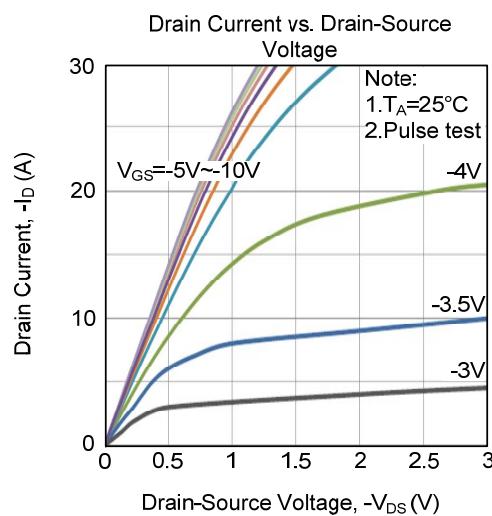
■ ELECTRICAL CHARACTERISTICS ( $T_J=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-10\text{mA}$	-40			V
Drain-Source Leakage Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-32\text{V}, V_{\text{GS}}=0\text{V}$		-1		$\mu\text{A}$
Gate-Source Leakage Current	$I_{\text{GSS}}$	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm 20\text{V}$			$\pm 100$	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{\text{GS}(\text{TH})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1.0	-1.9	-3.0	V
On State Drain Current	$I_{\text{D}(\text{ON})}$	$V_{\text{DS}}=-5\text{V}, V_{\text{GS}}=-10\text{V}$	-30			A
Static Drain-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-12\text{A}$		36	45	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-8.0\text{A}$		51	69	$\text{m}\Omega$
<b>DYNAMIC PARAMETERS</b>						
Input Capacitance	$C_{\text{ISS}}$	$V_{\text{DS}}=-20\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$		1200		pF
Output Capacitance	$C_{\text{OSS}}$			145		pF
Reverse Transfer Capacitance	$C_{\text{RSS}}$			118		pF
<b>SWITCHING PARAMETERS</b>						
Total Gate Charge	$Q_{\text{G}}$	$V_{\text{DS}}=-32\text{V}, V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-12\text{A}$		25		nC
Gate Source Charge	$Q_{\text{GS}}$			3.8		nC
Gate Drain Charge	$Q_{\text{GD}}$			6		nC
Turn-ON Delay Time	$t_{\text{D}(\text{ON})}$	$V_{\text{GS}}=-10\text{V}, V_{\text{DS}}=-20\text{V}, I_{\text{D}}=-12\text{A}, R_{\text{G}}=3.3\Omega$		7		ns
Turn-ON Rise Time	$t_{\text{R}}$			18		ns
Turn-OFF Delay Time	$t_{\text{D}(\text{OFF})}$			43		ns
Turn-OFF Fall-Time	$t_{\text{F}}$			24		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Maximum Body-Diode Continuous Current	$I_{\text{s}}$				-12	A
Diode Forward Voltage	$V_{\text{SD}}$	$I_{\text{s}}=-12\text{A}, V_{\text{GS}}=0\text{V}$		-1	-1.2	V
Body Diode Reverse Recovery Time	$t_{\text{rr}}$	$I_{\text{s}}=-12\text{A}, V_{\text{GS}}=0\text{V}, dI/dt=100\text{A}/\mu\text{s}$		56		ns
Body Diode Reverse Recovery Charge	$Q_{\text{rr}}$			48		nC

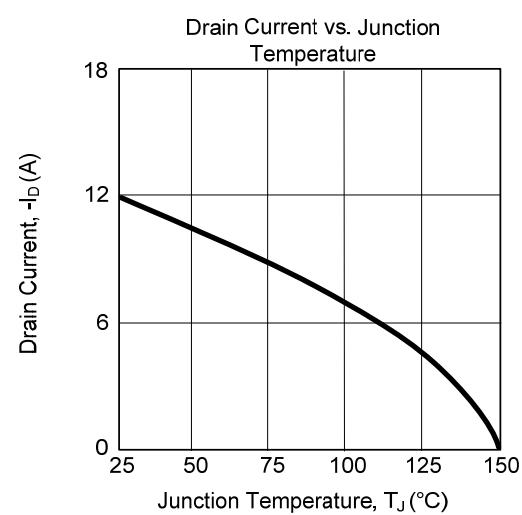
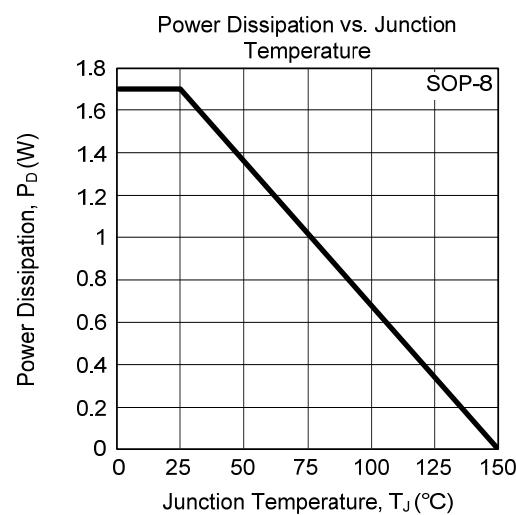
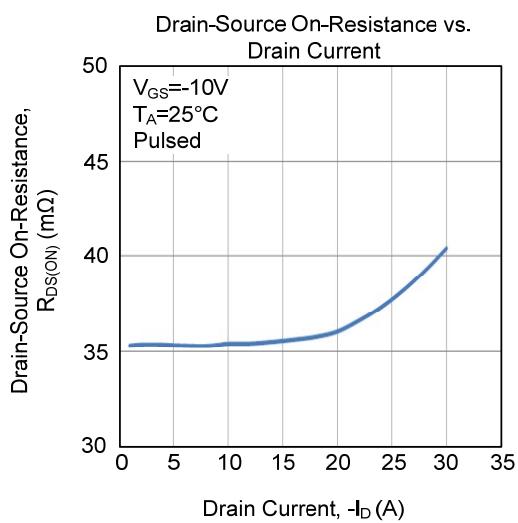
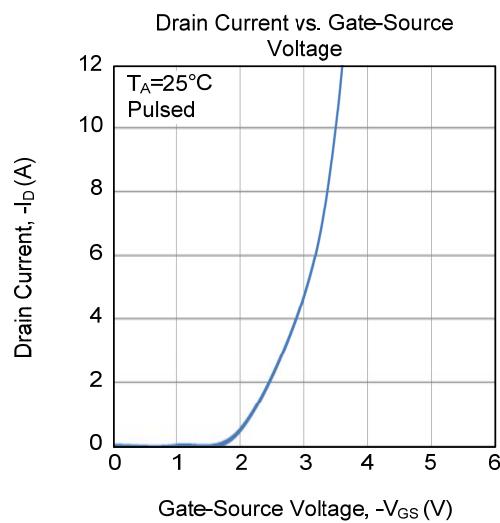
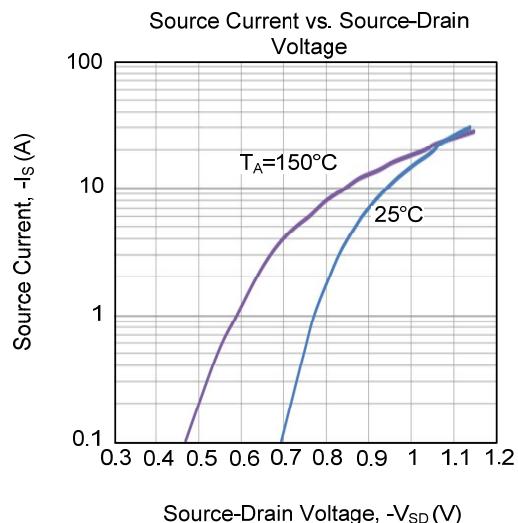
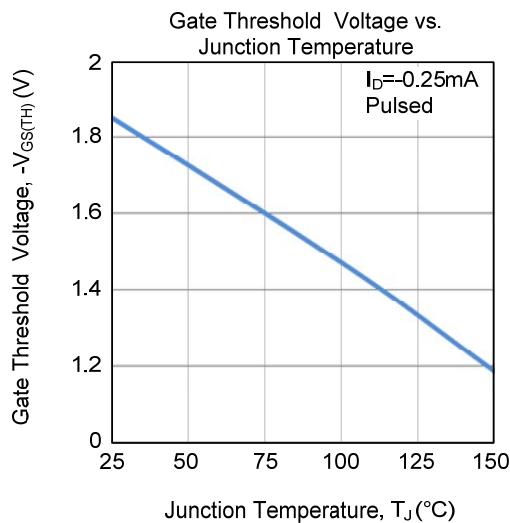
Notes: 1. Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$ .

2. Essentially independent of operating temperature.

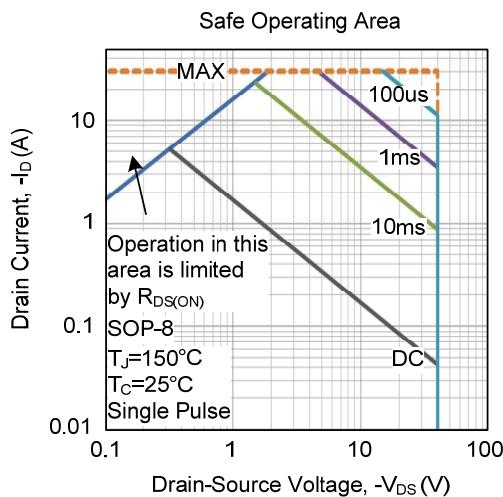
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



- TYPICAL CHARACTERISTICS (Cont.)



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