

UT3414

Power MOSFET

N-CHANNEL ENHANCEMENT MODE

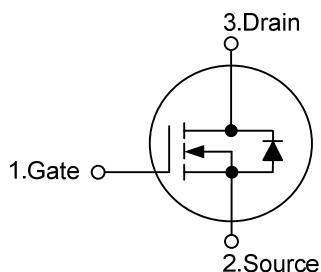
■ DESCRIPTION

The **UT3414** uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 1.8V. This device is suitable for use as a load switch or in PWM applications.

■ FEATURES

- * $R_{DS(ON)} \leq 50m\Omega$ @ $V_{GS}=4.5V$, $I_D=4.2A$
- * $R_{DS(ON)} \leq 63m\Omega$ @ $V_{GS}=2.5V$, $I_D=3.7A$
- * $R_{DS(ON)} \leq 87m\Omega$ @ $V_{GS}=1.8V$, $I_D=3.2A$
- * Low capacitance
- * Low gate charge
- * Fast switching capability
- * Avalanche energy specified

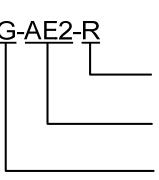
■ SYMBOL



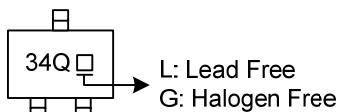
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UT3414L-AE2-R	UT3414G-AE2-R	SOT-23-3	G	S	D	Tape Reel
UT3414L-AE3-R	UT3414G-AE3-R	SOT-23	G	S	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

 UT3414G-AE2-R	(1)Packing Type (2)Package Type (3)Green Package	(1) R: Tape Reel (2) AE2: SOT-23-3, AE3: SOT-23 (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	20	V
Gate-Source Voltage		V_{GSS}	± 8	V
Continuous Drain Current		I_D	4.2	A
Pulsed Drain Current		I_{DM}	15	A
Power Dissipation	SOT-23-3	P_D	0.5	W
	SOT-23		0.6	W
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

■ THERMAL DATA

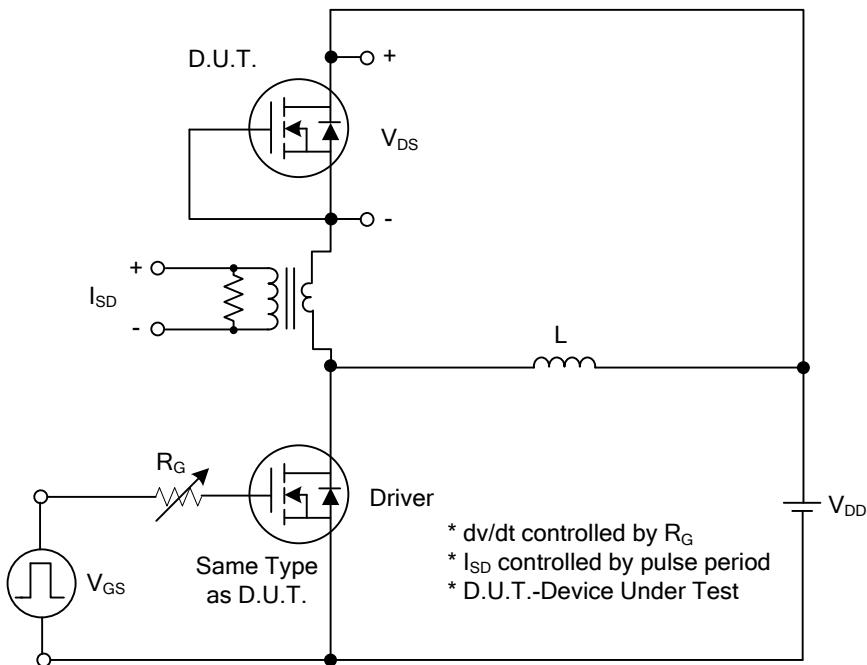
PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	SOT-23-3	θ_{JA}	250	$^\circ\text{C}/\text{W}$
	SOT-23		208	$^\circ\text{C}/\text{W}$

Note: Device mounted on FR-4 substrate P_C board, 2oz copper, with 1inch square copper plate.

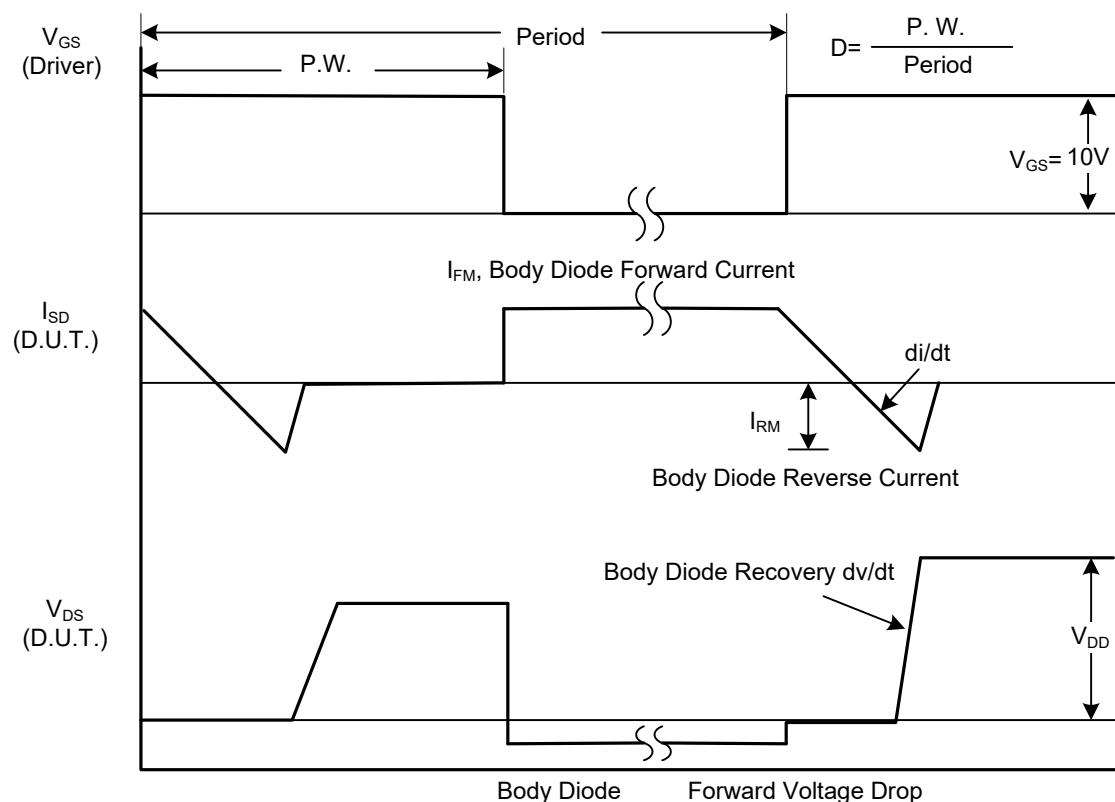
■ Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	20			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=16\text{V}$, $V_{GS}=0\text{V}$			1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{DS}=0\text{V}$, $V_{GS}=\pm 8\text{V}$			100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$	0.4	0.6	1.0	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=4.5\text{V}$, $I_D=4.2\text{A}$			50	$\text{m}\Omega$
		$V_{GS}=2.5\text{V}$, $I_D=3.7\text{A}$			63	
		$V_{GS}=1.8\text{V}$, $I_D=3.2\text{A}$			87	
DYNAMIC PARAMETERS						
Input Capacitance	C_{iss}	$V_{DS}=10\text{V}$, $V_{GS}=0\text{V}$, $f=1.0\text{MHz}$		256		pF
Output Capacitance	C_{oss}			72		pF
Reverse Transfer Capacitance	C_{rss}			58		pF
SWITCHING PARAMETERS						
Total Gate Charge	Q_G	$V_{DS}=10\text{V}$, $I_D=4.2\text{A}$, $V_{GS}=4.5\text{V}$		8		nC
Gate Source Charge	Q_{GS}			1		nC
Gate Drain Charge	Q_{GD}			1.6		nC
Turn ON Delay Time	$t_{D(ON)}$	$V_{DS}=10\text{V}$, $V_{GS}=5\text{V}$, $R_L=2.7\Omega$ $R_G=6\Omega$		4		ns
Turn ON Rise Time	t_R			16		ns
Turn OFF Delay Time	$t_{D(OFF)}$			16		ns
Turn OFF Fall-Time	t_F			21		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				2	A
Diode Forward Voltage	V_{SD}	$V_{GS}=0\text{V}$, $I_S=1.0\text{A}$		0.76	1	V

■ TEST CIRCUITS AND WAVEFORMS

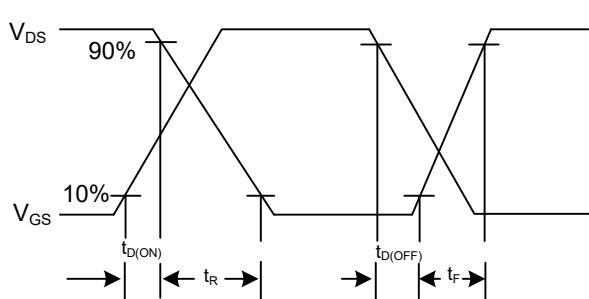
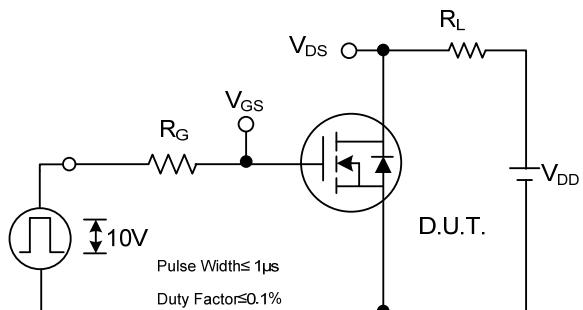


Peak Diode Recovery dv/dt Test Circuit



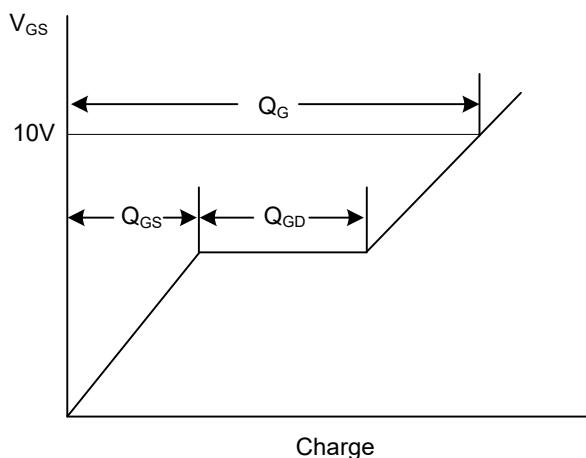
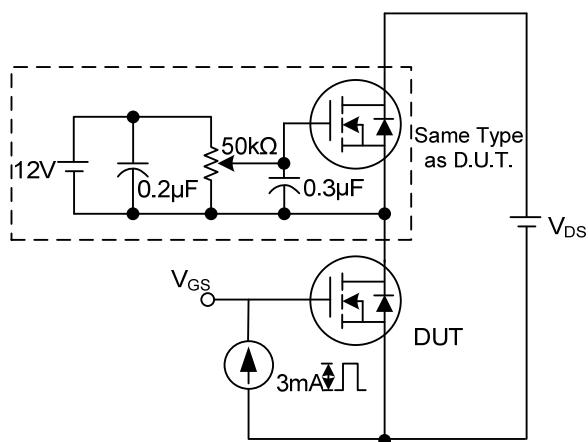
Peak Diode Recovery dv/dt Waveforms

■ TEST CIRCUITS AND WAVEFORMS



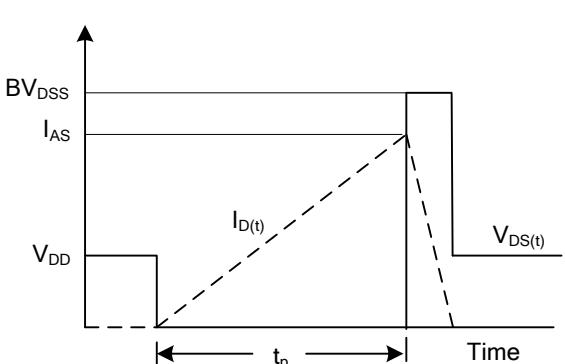
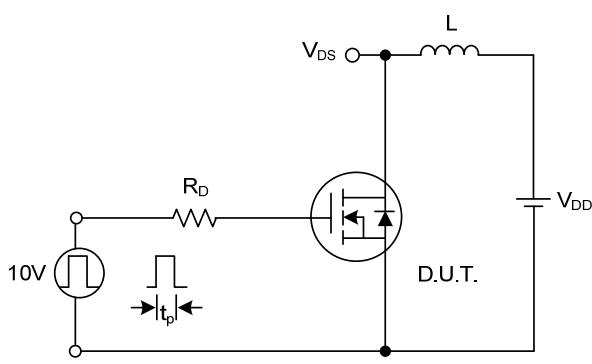
Switching Test Circuit

Switching Waveforms



Gate Charge Test Circuit

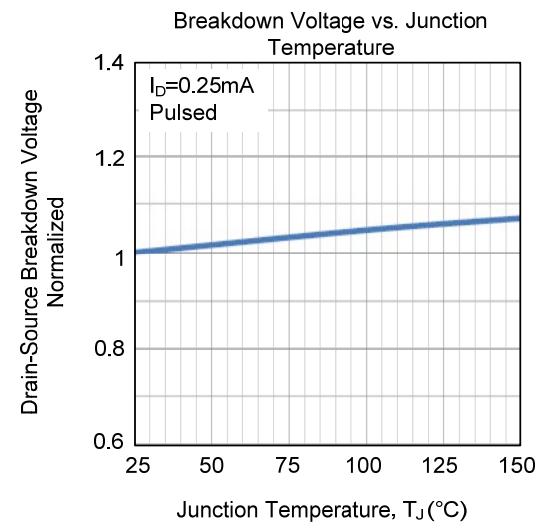
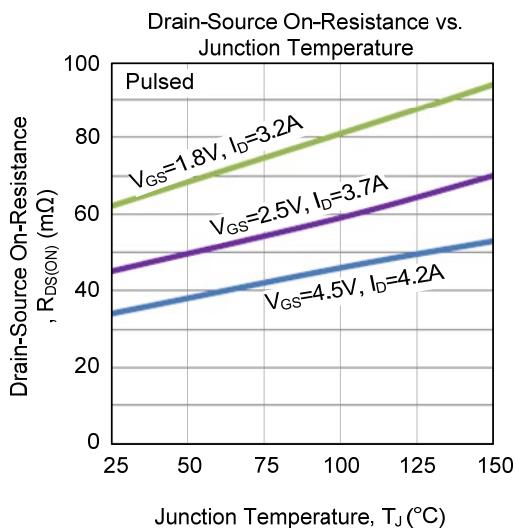
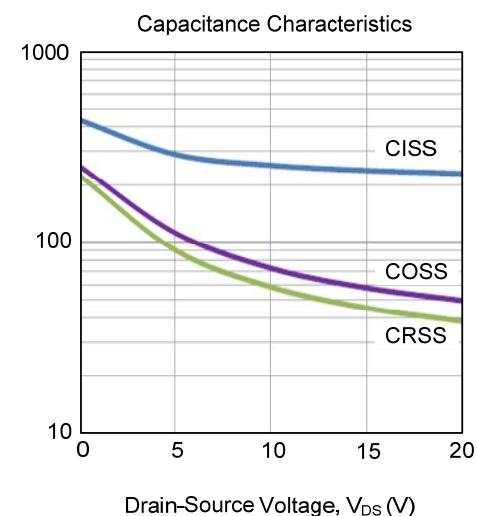
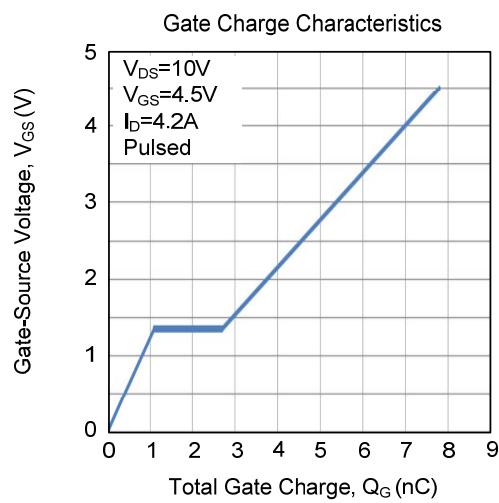
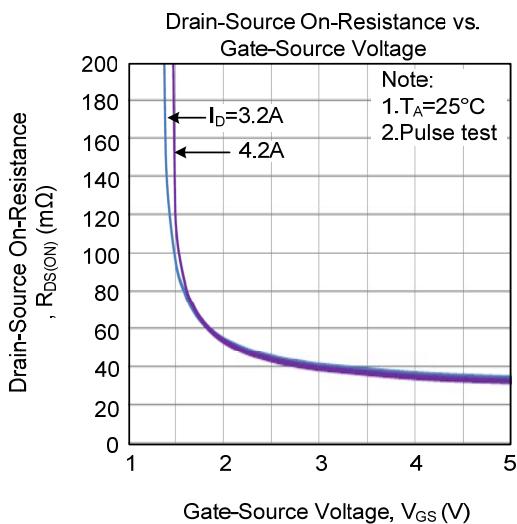
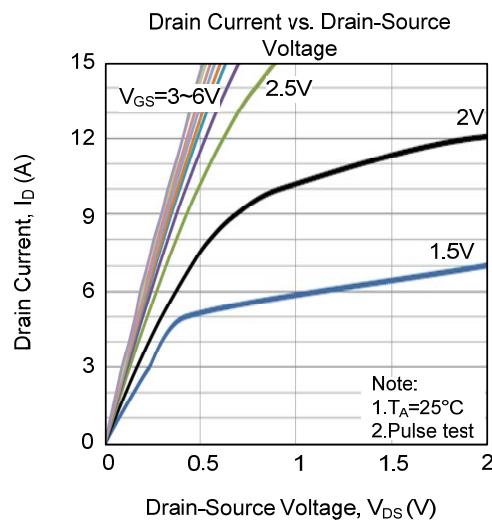
Gate Charge Waveform



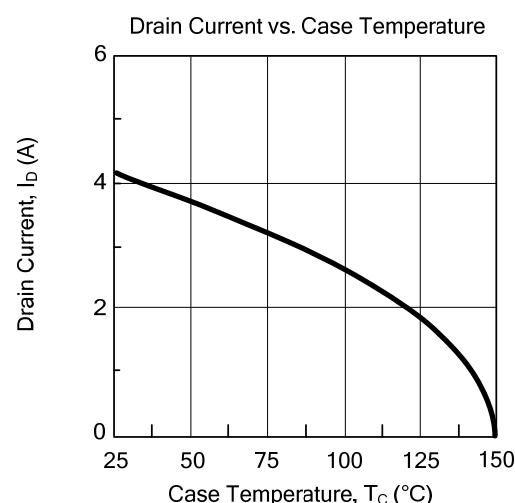
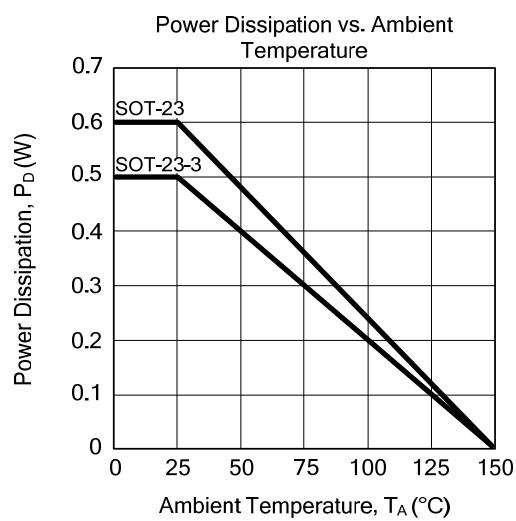
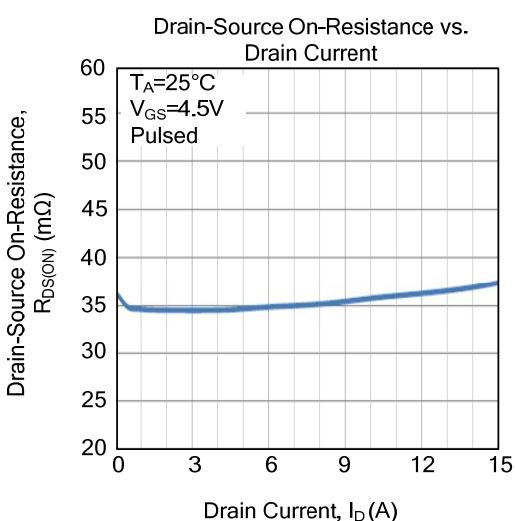
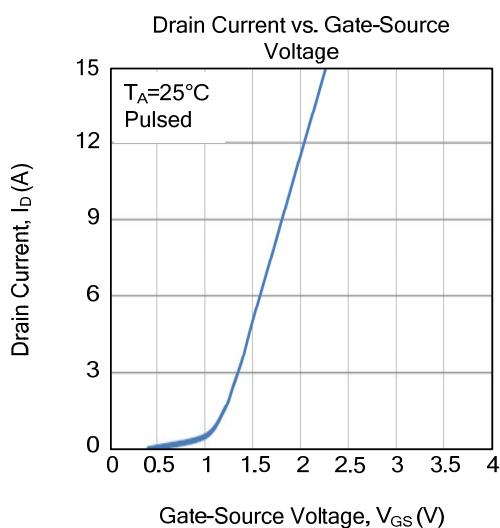
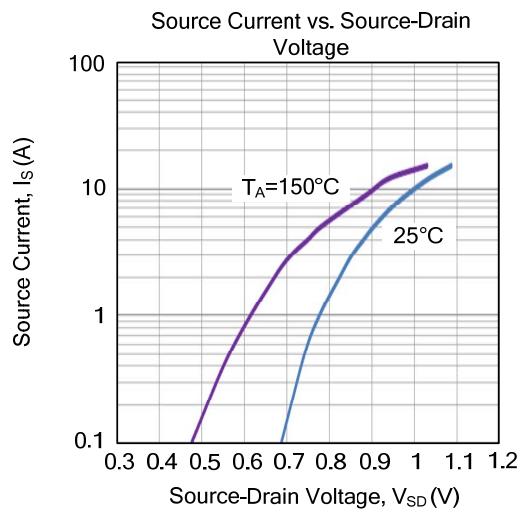
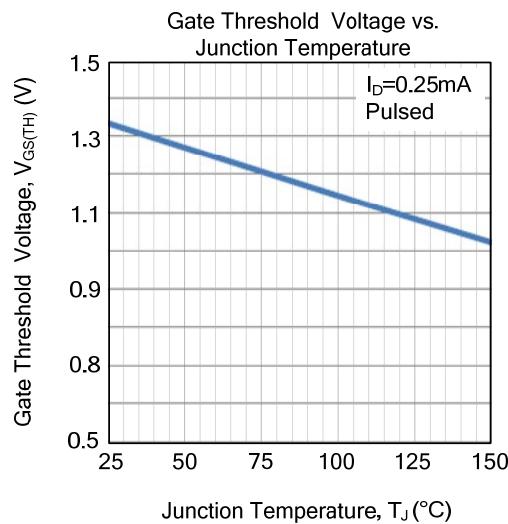
Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

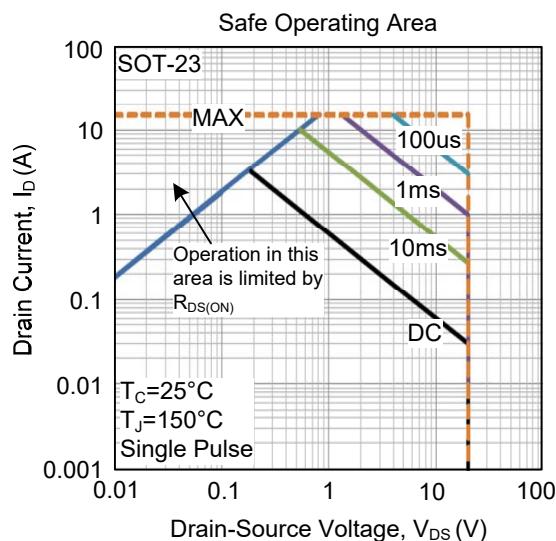
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



- TYPICAL CHARACTERISTICS (Cont.)



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