



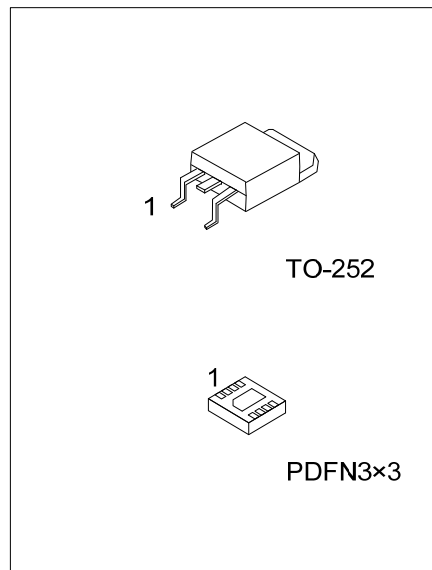
UTD408

Power MOSFET

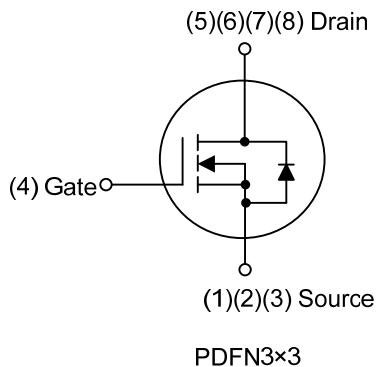
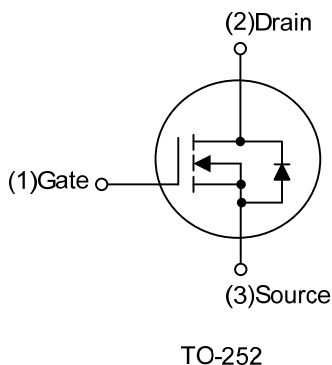
N-CHANNEL ENHANCEMENT MODE

■ FEATURES

- * $R_{DS(ON)} \leq 18\text{ m}\Omega$ @ $V_{GS} = 10\text{V}$, $I_D = 18\text{A}$
 $R_{DS(ON)} \leq 27\text{ m}\Omega$ @ $V_{GS} = 4.5\text{V}$, $I_D = 10\text{A}$
- * Low capacitance
- * Optimized gate charge
- * Fast switching capability
- * Avalanche energy specified



■ SYMBOL



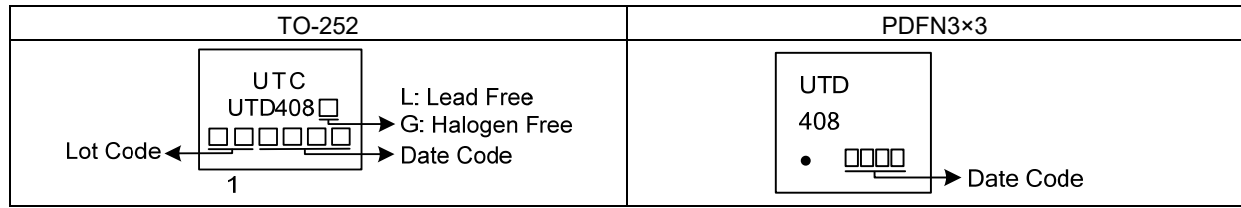
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UTD408L-TN3-R	UTD408G-TN3-R	TO-252	G	D	S	-	-	-	-	-	Tape Reel
UTD408L-P3030-R	UTD408G-P3030-R	PDFN3x3	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>UTD408G-TN3-R</p> <p>(1) Packing Type</p> <p>(2) Package Type</p> <p>(3) Green Package</p>	<p>(1) R: Tape Reel</p> <p>(2) TN3: TO-252, P3030: PDFN3x3</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V _{DSS}	30	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current (T _C =25°C)	I _D	18	A
Pulsed Drain Current (Note 2)	I _{DM}	40	A
Avalanche Energy, Single Pulse(Note 3)	E _{AS}	1.5	mJ
Peak Diode Recovery dv/dt (Note 4)	dv/dt	1.1	V/ns
Power Dissipation(T _C =25°C)	TO-252	32	W
	PDFN3×3	20	W
Junction Temperature	T _J	+150	°C
Strong Temperature	T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L = 0.1mH, I_{AS} = 5.5A, V_{DD} = 50V, R_G = 25 Ω, Starting T_J = 25°C

4. I_{SD} ≤ 18.0A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ _{JA}	TO-252	50	°C/W
		PDFN3×3	75	°C/W
Junction to Case	θ _{JC}	TO-252	3.9	°C/W
		PDFN3×3	6.25	°C/W

Notes: Device mounted on 1in 2 FR-4 board with 2oz. Copper, and the maximum temperature of 150°C may be used if the PCB or heat-sink allows it.

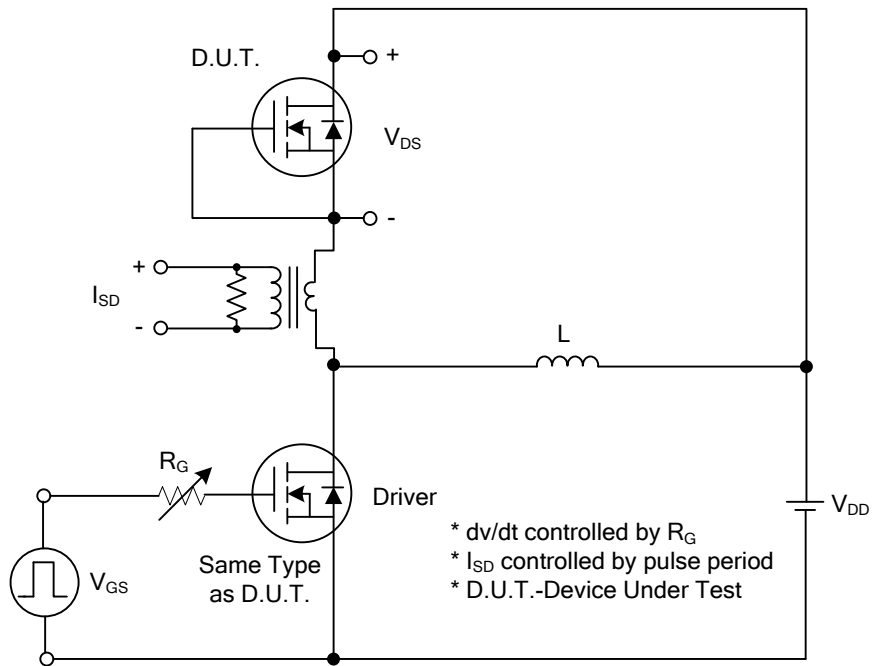
■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	30			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =24V, V _{GS} =0V			1	μA
Gate-Body Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	1.0	1.8	2.5	V
Static Drain-Source On-Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =18A			18	mΩ
		V _{GS} =4.5V, I _D =10A			27	mΩ
DYNAMIC PARAMETERS						
Input Capacitance	C _{ISS}	V _{DS} 15V, V _{GS} =0V, f=1MHz		580		pF
Output Capacitance	C _{OSS}			151		pF
Reverse Transfer Capacitance	C _{RSS}			132		pF
SWITCHING PARAMETERS						
Total Gate Charge	Q _G	V _{DD} =24V, V _{GS} =10V, I _D =18A (Note 1, 2)		25		nC
Gate Source Charge	Q _{GS}			4.3		nC
Gate Drain Charge	Q _{GD}			6.2		nC
Turn-ON Delay Time	t _{D(ON)}	V _{DD} =15V, V _{GS} =10V, I _D =18A, R _G =3Ω (Note 1, 2)		6		ns
Turn-ON Rise Time	t _R			15		ns
Turn-OFF Delay Time	t _{D(OFF)}			18		ns
Turn-OFF Fall-Time	t _F			21		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Continuous Drain-Source Diode Forward Current	I _S				18	A
Drain-Source Diode Forward Voltage	V _{SD}	I _S =18A, V _{GS} =0V			1.4	V
Body Diode Reverse Recovery Time	t _{rr}	I _F =18A, di/dt=100A/μs		300		ns
Body Diode Reverse Recovery Charge	Q _{rr}				737	

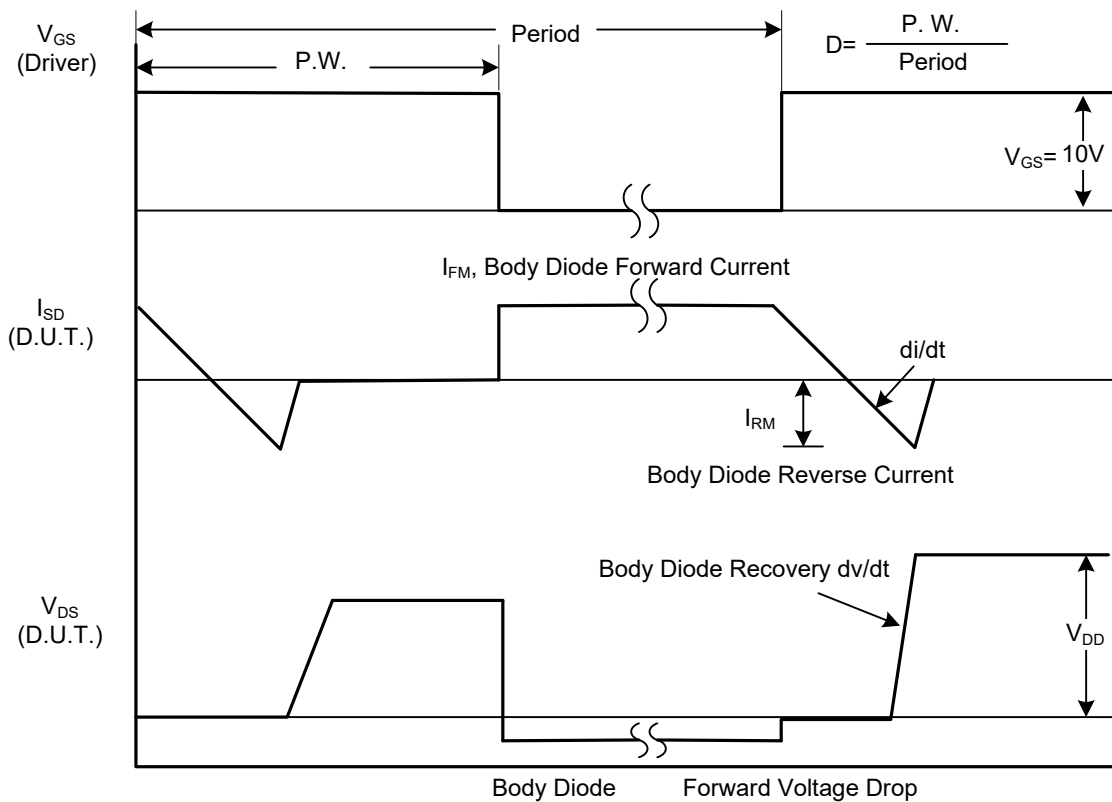
Notes: 1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Pulse width ≤ 300us, duty cycle ≤ 2%.

■ TEST CIRCUITS AND WAVEFORMS

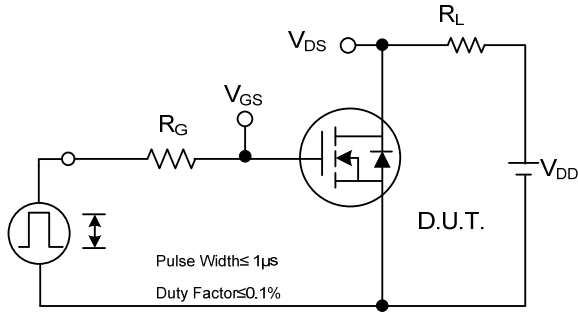


Peak Diode Recovery dv/dt Test Circuit

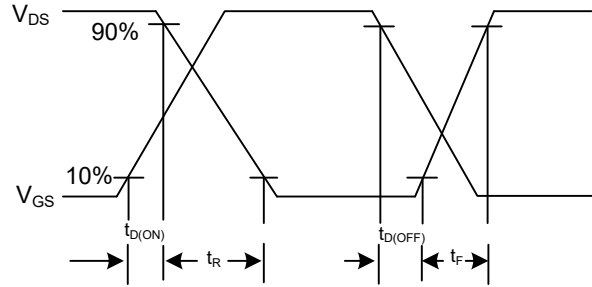


Peak Diode Recovery dv/dt Waveforms

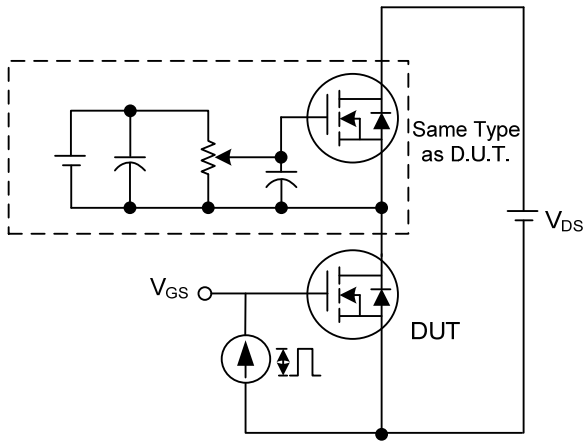
■ TEST CIRCUITS AND WAVEFORMS



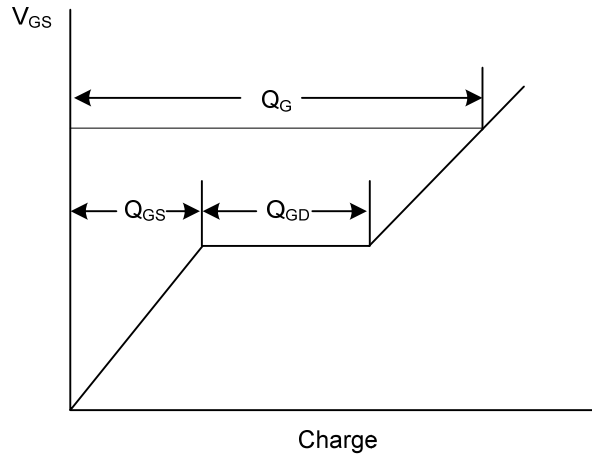
Switching Test Circuit



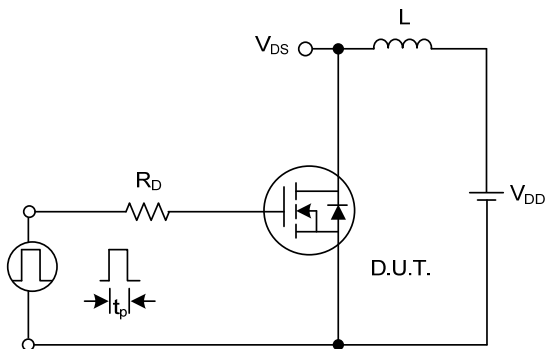
Switching Waveforms



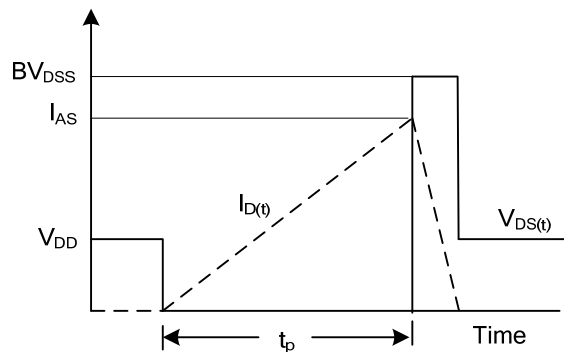
Gate Charge Test Circuit



Gate Charge Waveform

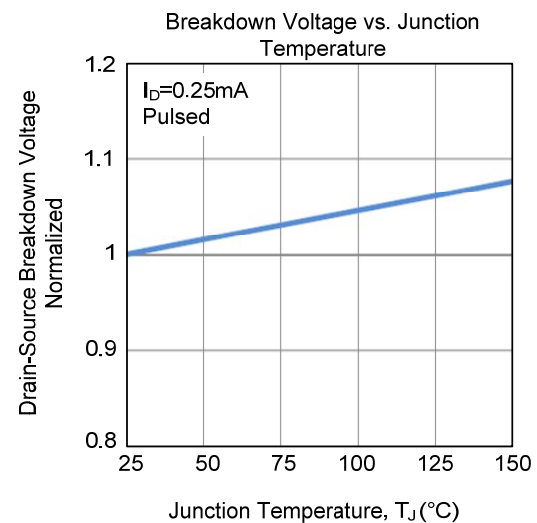
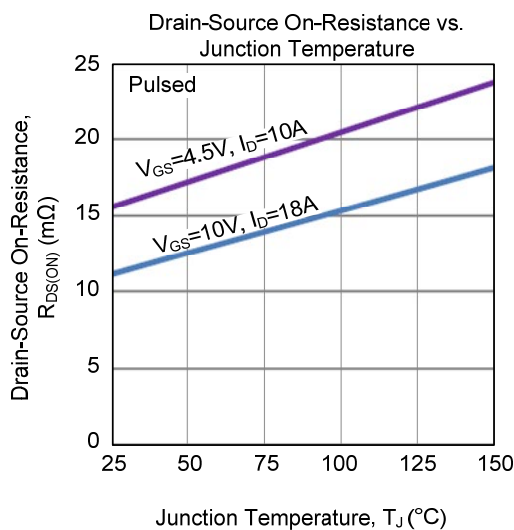
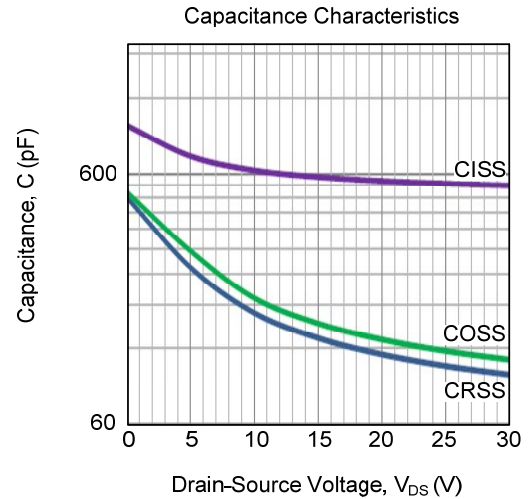
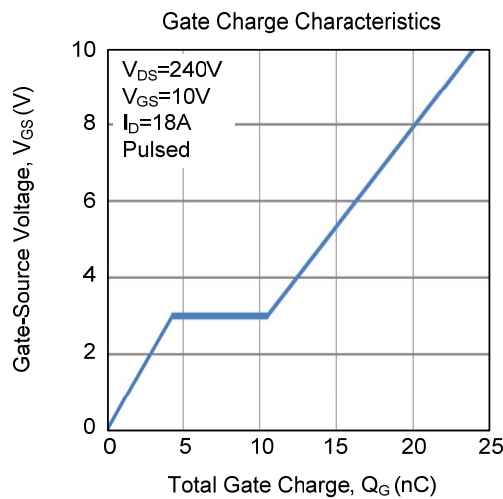
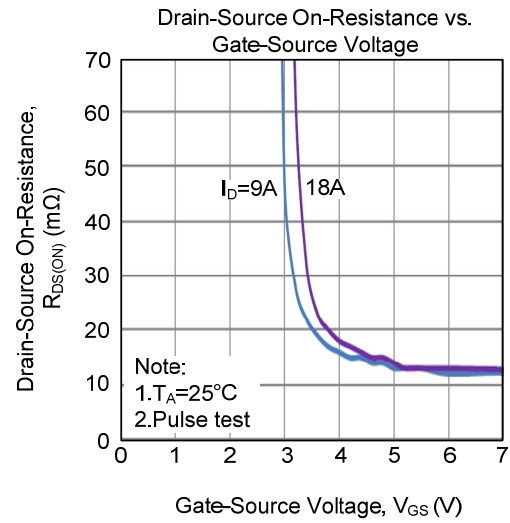
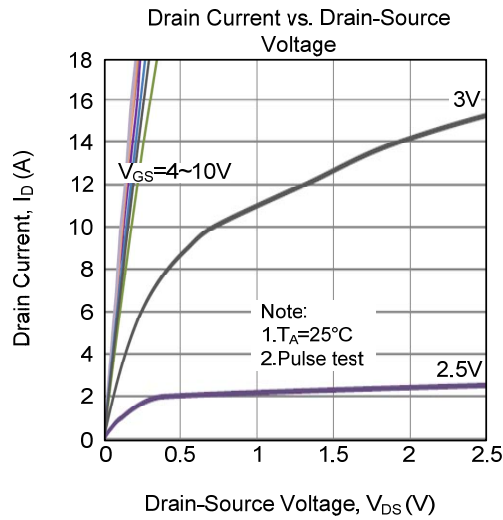


Unclamped Inductive Switching Test Circuit

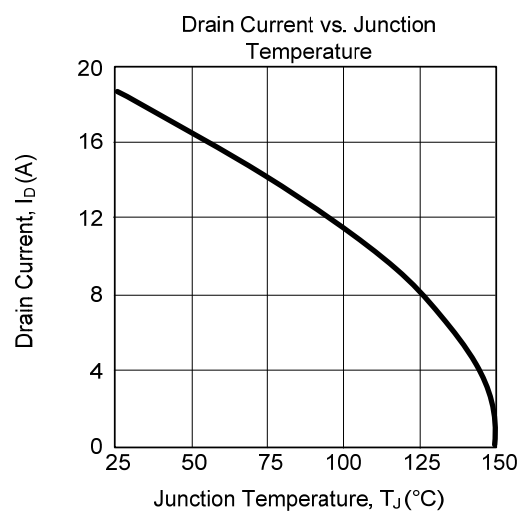
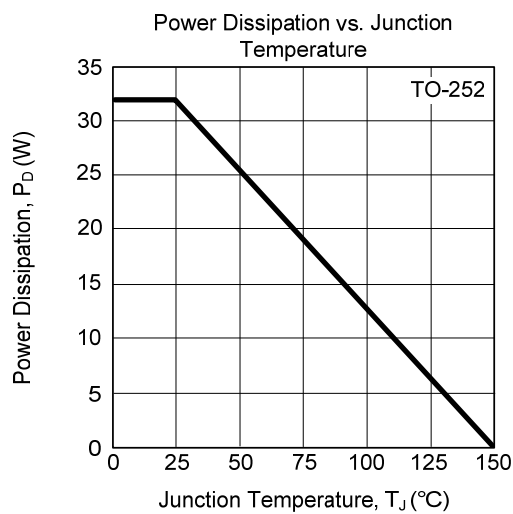
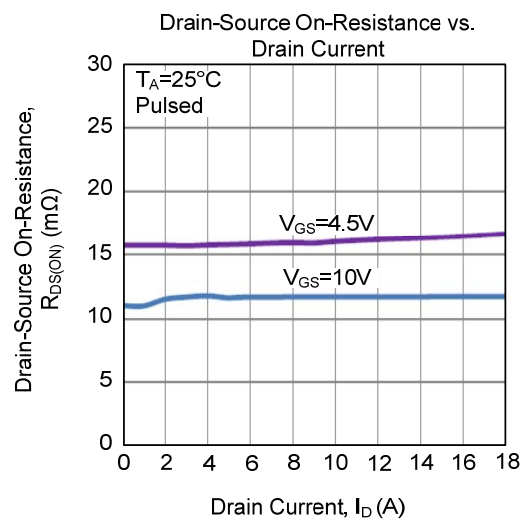
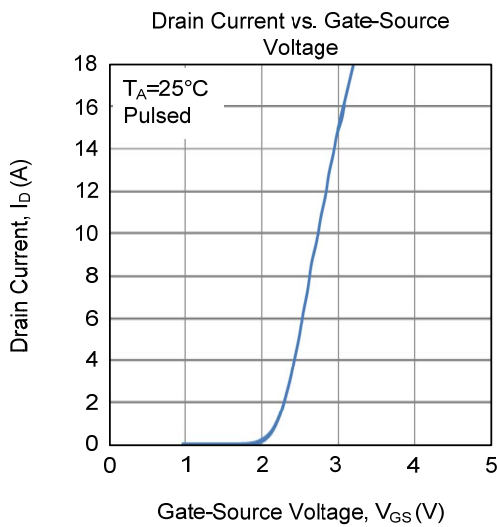
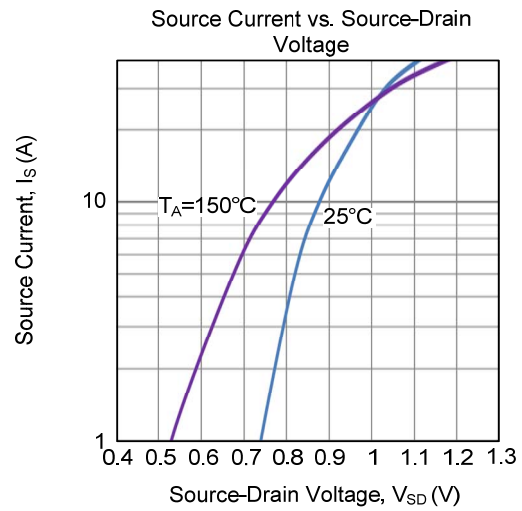
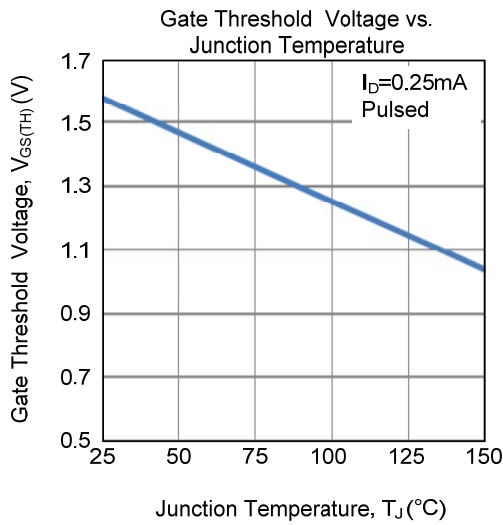


Unclamped Inductive Switching Waveforms

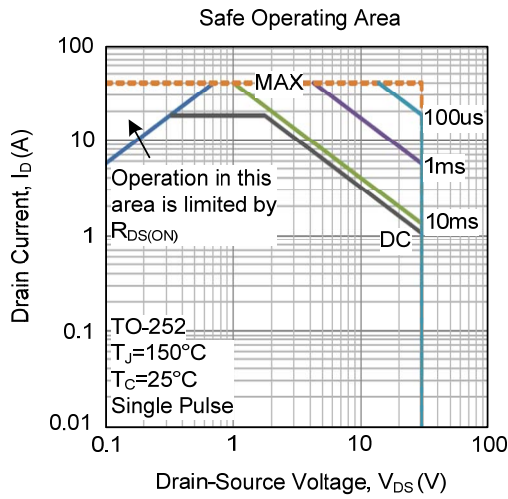
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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