



## UT3400

Power MOSFET

### 5.8A, 30V N-CHANNEL ENHANCEMENT MODE POWER MOSFET

#### DESCRIPTION

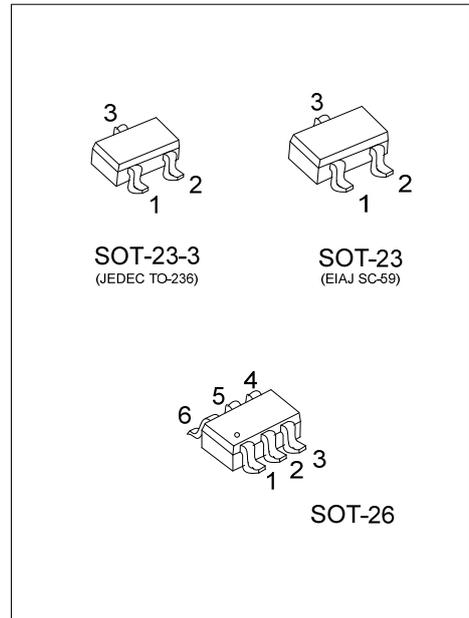
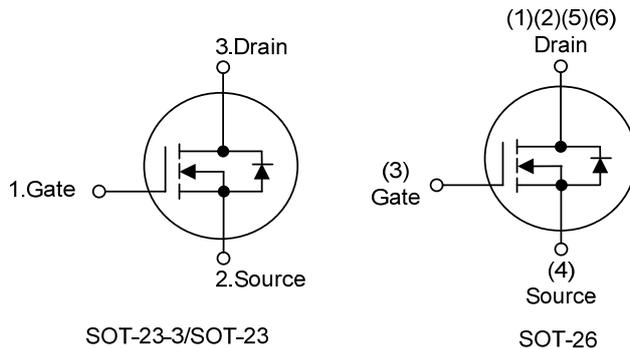
The UTC **UT3400** is an N-ch enhancement MOSFET providing the customers with perfect  $R_{DS(ON)}$  and low gate charge. This device can be operated with 2.5V low gate voltage.

The UTC **UT3400** is optimized for applications, such as a load switch or in PWM.

#### FEATURES

- \*  $R_{DS(ON)} \leq 28 \text{ m}\Omega @ V_{GS}=10\text{V}, I_D=5.8\text{A}$
- $R_{DS(ON)} \leq 33 \text{ m}\Omega @ V_{GS}=4.5\text{V}, I_D=5.0\text{A}$
- $R_{DS(ON)} \leq 52 \text{ m}\Omega @ V_{GS}=2.5\text{V}, I_D=4.0\text{A}$

#### SYMBOL



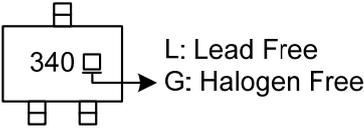
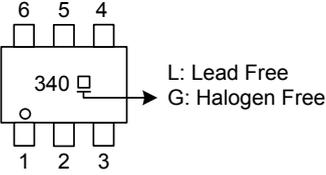
#### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment						Packing
Lead Free	Halogen Free		1	2	3	4	5	6	
UT3400L-AE2-R	UT3400G-AE2-R	SOT-23-3	G	S	D	-	-	-	Tape Reel
UT3400L-AE3-R	UT3400G-AE3-R	SOT-23	G	S	D	-	-	-	Tape Reel
UT3400L-AG6-R	UT3400G-AG6-R	SOT-26	D	D	G	S	D	D	Tape Reel

Note: Pin Assignment: G: Gate S: Source D: Drain

<p>UT3400G-AE2-R</p> <p>(1)Packing Type</p> <p>(2)Package Type</p> <p>(3)Green Package</p>	<p>(1) R: Tape Reel</p> <p>(2) AE2: UTC654G-AE2-R, AE3: SOT-23, AG6: SOT-26</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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## MARKING

SOT-23-3 / SOT-23	SOT-26
 <p>A schematic diagram of a MOSFET in an SOT-23-3 / SOT-23 package. The package is a small rectangle with three leads: one on the top, one on the right, and two on the bottom. The number '340' is printed on the top surface. An arrow points from the '340' to the text 'L: Lead Free' and 'G: Halogen Free'.</p>	 <p>A schematic diagram of a MOSFET in an SOT-26 package. The package is a larger rectangle with six leads: three on the top (labeled 6, 5, 4) and three on the bottom (labeled 1, 2, 3). The number '340' is printed on the top surface. An arrow points from the '340' to the text 'L: Lead Free' and 'G: Halogen Free'.</p>

■ ABSOLUTE MAXIMUM RATINGS ( $T_A=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DS}$	30	V
Gate-Source Voltage		$V_{GS}$	$\pm 12$	V
Continuous Drain Current		$I_D$	5.8	A
Pulsed Drain Current (Note 2)		$I_{DM}$	30	A
Power Dissipation	SOT-23-3	$P_D$	0.73	W
	SOT-23		1.25	W
	SOT-26		1.2	W
Junction Temperature		$T_J$	+150	$^\circ\text{C}$
Storage Temperature		$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

- Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.  
 2. Repetitive Rating: Pulse width limited by maximum junction temperature.  
 3. Pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 0.5\%$ .

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient (Note)	SOT-23-3	$\theta_{JA}$	170	$^\circ\text{C/W}$
	SOT-23		100	$^\circ\text{C/W}$
	SOT-26		104	$^\circ\text{C/W}$

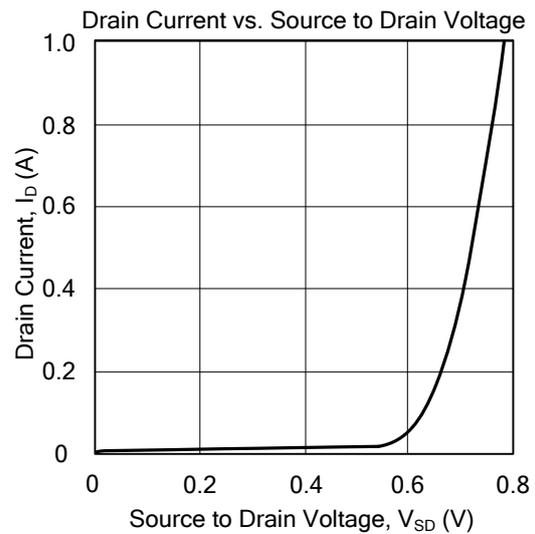
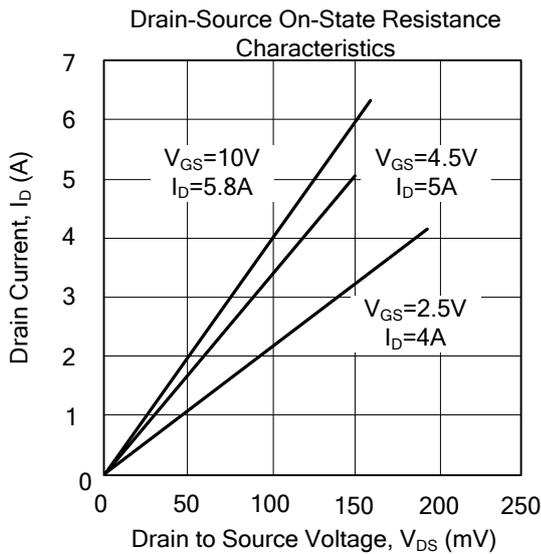
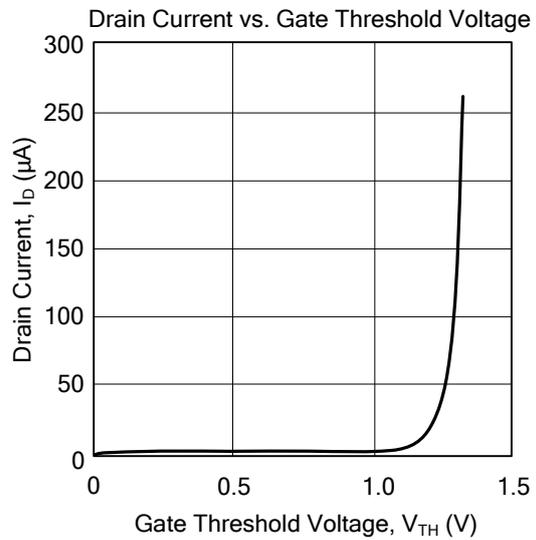
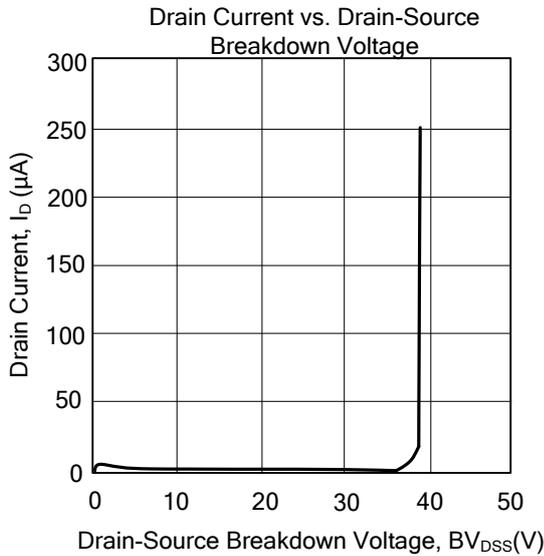
Note: Surface mounted on 1 in<sup>2</sup> copper pad of FR4 board with 2oz.

■ ELECTRICAL CHARACTERISTICS ( $T_J=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu\text{A}$	30			V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS}=24V, V_{GS}=0V$			1	$\mu\text{A}$
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 12V, V_{DS}=0V$			100	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	0.7	1.1	1.4	V
Drain to Source On-state Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=5.8A$		22.8	28	m $\Omega$
		$V_{GS}=4.5V, I_D=5A$		27.3	33	m $\Omega$
		$V_{GS}=2.5V, I_D=4A$		43.3	52	m $\Omega$
<b>DYNAMIC PARAMETERS</b>						
Input Capacitance	$C_{ISS}$	$V_{DS}=15V, V_{GS}=0V, f=1\text{MHz}$		550		pF
Output Capacitance	$C_{OSS}$			72		pF
Reverse Transfer Capacitance	$C_{RSS}$			57		pF
<b>SWITCHING PARAMETERS</b>						
Total Gate Charge	$Q_G$	$V_{DS}=15V, V_{GS}=4.5V, I_D=5.8A$ (Note 1, 2)		9		nC
Gate Source Charge	$Q_{GS}$			1.4		nC
Gate Drain Charge	$Q_{GD}$			3.4		nC
Turn-ON Delay Time	$t_{D(ON)}$	$V_{DS}=15V, V_{GS}=10V, I_D=5.8A$ $R_G=3\Omega$ (Note 1, 2)		5		ns
Turn-ON Rise Time	$t_R$			16		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			17		ns
Turn-OFF Fall-Time	$t_F$			24		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Drain-Source Diode Forward Voltage	$V_{SD}$	$I_S=1A, V_{GS}=0V$		0.71	1	V

- Notes: 1. Repetitive Rating: Pulse width limited by maximum junction temperature.  
 2. Pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 0.5\%$ .

## ■ TYPICAL CHARACTERISTICS



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