UNISONIC TECHNOLOGIES CO., LTD

UC34163

LINEAR INTEGRATED CIRCUIT

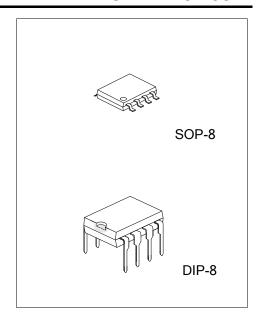
DC TO DC CONVERTER CONTROLLER

DESCRIPTION

The UTC UC34163 is a monolithic regulator subsystem, intended for use as DC-to-DC converter. This device contains a temperature compensated reference, 2 comparators, a duty-cycle control oscillator, driver and high current output switch.

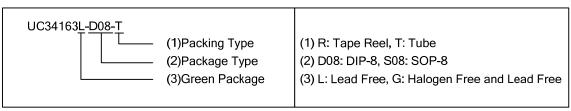
FEATURES

- * Maximum input voltage is 35V.
- * Low standby current.
- * Output switch current to 1.5A.
- * Frequency of operation from 100Hz ~ 100kHz.
- * Step-down switch regulators.

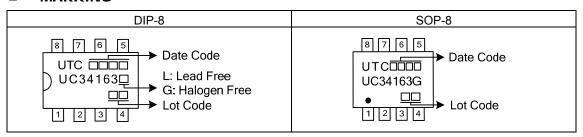


ORDERING INFORMATION

Ordering	Dealtone	Dealine	
Lead Free	Halogen Free	Package	Packing
UC34163L-D08-T	UC34163G-D08-T	DIP-8	Tube
-	UC34163G-S08-R	SOP-8	Tape Reel

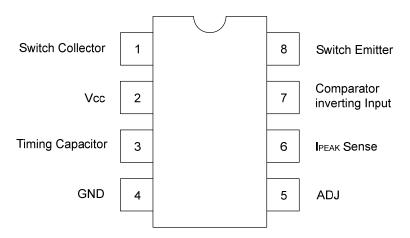


MARKING

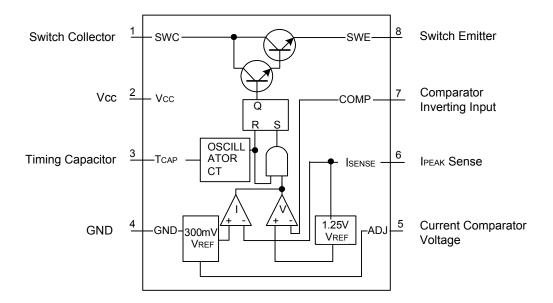


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■ PIN CONFIGURATION



■ BLOCK DIAGRAM



■ **ABSOLUTE MAXIMUM RATINGS** (T_A=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Supply Voltage		V _{CC}	35	V
Comparator Input Voltage Range		V _{IN(COMP)}	-0.3 ~ +35	V
Switch Collector Voltage		$V_{C(SW)}$	35	V
Switch Emitter Voltage		$V_{E(SW)}$	35	V
Switch Collector To Emitter Voltage		V _{CE(SW)}	35	V
Output Switch Current		l _{out}	1.5	Α
Davier Dissipation (Ta-25°C)	DIP-8	P _D	1250	mW
Power Dissipation (Ta=25°C)	SOP-8		625	mW
Operating Junction Temperature		TJ	+125	°C
Operating Temperature		T _{OPR}	-20 ~ +85	°C
Storage Temperature		T _{STG}	-40 ~ +150	°C

- Notes: 1. Absolute maximum ratings are those values beyond which the device which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
 - 2. The device is guaranteed to meet performance specification within 0°C~+70°C operating temperature range and assured by design from -20°C ~+85°C, characteristic and correlation with static process control.

■ THERMAL DATA

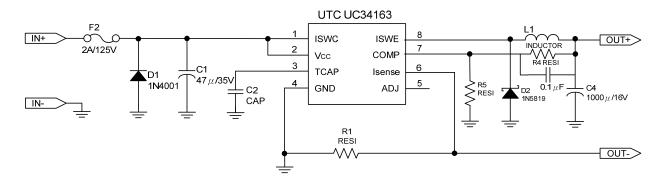
PARAMETER		SYMBOL	RATINGS	UNIT	
	DIP-8	θјс	100	°C/A/	
Junction to Case	SOP-8		160	°C/W	

■ **ELECTRICAL CHARACTERISTICS** (T_A=25°C, V_{CC} = 5.0V, T_A=0~70°C, unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
Oscillator							
Frequency (C _T =470pF, T _A =25°C)	Fosc		25	35	45	kHz	
Charging Current	I _{CHG}	V _{CC} =5 ~ 35V, T _A =25°C	20	30	40	μΑ	
Discharging Current	I _{DISCHG}	V _{CC} =5 ~ 35V, T _A =25°C	140	200	260	μΑ	
Oscillator Amplitude	V_{OSC}	T _A =25°C		0.8		V	
Discharge to Charge Current Ratio	K	T _A =25°C	5.2		8.0		
Output Switch							
Saturation Voltage 1(Note)	$V_{CE(SAT)}$	I _{SW} =1.0A			1.4	V	
Collector Off State Current (Note)	I _{C(OFF)}	V _{CE} =35.0V, T _A =25°C		0.01	100	μΑ	
ADJ							
Current limit Sense Voltage	V_{SENSE}		280	300	360	mV	
Comparator							
Threshold Voltage 1	V_{THD1}		1.21	1.24	1.29	V	
Threshold Voltage 2	V_{THD2}		280	300	360	mV	
Threshold Voltage Line Regulation	$\triangle V_{THD1}$	V _{CC} =5 ~ 35V		2.0	5.0	mV	
Input Bias Current	I _{BIAS}	V _{IN} =0V		50	400	nA	
Total Device							
Supply Current	I _{CC}	V_{CC} =5 ~ 35V, C_T =470pF V_6 =GND, V_7 > V_{THD1}		2.5	4.0	mA	

Note: Output switch tests are performed under pulsed conditions to minimize power dissipation.

■ TYPICAL APPLICATION CIRCUIT



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