



UD3H

Preliminary

DUAL TRANSISTOR

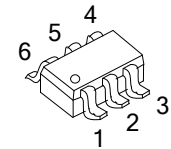
GENERAL PURPOSE (DUAL DIGITAL TRANSISTORS)

DESCRIPTION

The UTC **UD3H** is Dual Digital Transistors including a NPN transistor and a PNP transistor. The transistor elements are independent to eliminate interference.

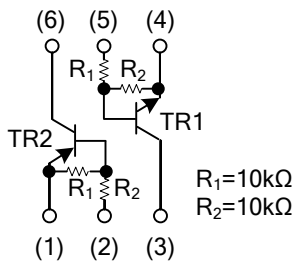
FEATURES

- * Transistor elements are independent, eliminating interference.
- * Mounting cost and area can be cut in half.



SOT-26

EQUIVALENT CIRCUIT



ORDERING INFORMATION

Ordering Number		Package	Pin Assignment						Packing
Lead Free	Halogen Free		1	2	3	4	5	6	
UD3HL-AG6-R	UD3HG-AG6-R	SOT-26	E2	B2	C1	E1	B1	C2	Tape Reel

UD3HL-AG3-R	(1)Packing Type	(1) R: Tape Reel
	(2)Package Type	(2) AG3: SOT-26
	(3)Halogen Free	(3) Halogen Free, L: Lead Free

■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V _{CC}	50	V
Input Voltage	V _{IN}	-10	V
		40	V
Output Current	I _O	50	mA
	I _{C(MAX.)}	100	mA
Power Dissipation (Note 2)	P _D	300	mW
Junction Temperature	T _J	150	°C
Storage Temperature	T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. 200mW per element must not be exceeded.

■ ELECTRICAL CHARACTERISTICS (T_A=25°C)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Voltage	V _{I(OFF)}	V _{CC} =5V, I _O =100μA			0.5	V
	V _{I(ON)}	V _O =0.3V, I _O =10mA	3			V
Output Voltage	V _{O(ON)}	I _O =10mA, I _I =0.5mA		0.1	0.3	V
Input Current	I _I	V _I =5V			0.88	mA
Output Current	I _{O(OFF)}	V _{CC} =50V, V _I =0V			0.5	μA
Emitter Cutoff Current	G _I	V _O =5V, I _O =5mA	30			
Transition Frequency (Note 1)	f _T	V _{CE} =10V, I _E =-5mA, f=100MHz		250		MHz
Input Resistance	R ₁		7	10	13	KΩ
Resistance Ratio	R ₂ / R ₁		0.8	1	1.2	

Note: 1. Transition frequency of the device

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