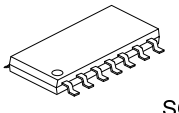
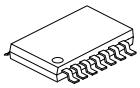
LV2464

CMOS IC

FAMILY OF LOW-POWER RAIL-TO-RAIL INPUT/OUTPUT OPERATIONAL AMPLIFIERS



SOP-14U



TSSOP-14U

■ DESCRIPTION

The UTC **LV2464** is a quad low-power rail-to-rail input/output op amplifier with low supply current (500uA/op) and low voltage (2.7-6V), that can be designed into a wide range of applications.

The UTC **LV2464** has a guaranteed 1.6V/μs slew rate and low supply current. Rail-to-rail output and high output current make the IC's ideal for buffering analog-to-digital converters. And the input common-mode voltage range including ground and V_{CC}. Besides, they are also able to driving large capacitive loads.

Good AC performance can be provided because of 6.4MHz of bandwidth and 1.6V/μs of slew rate. Furthermore, low input noise voltage (11nV/$\sqrt{Hz}$) and low input offset voltage (100μV) make good DC performance.

■ FEATURES

* Rail-to-Rail Input/Output
* ±48mA Output Drive Capability (V_{CC}=5V)
* Gain Bandwidth Product: 6.4MHz
* Supply Current: 500μA/channel
* Input Offset Voltage: 100μV
* Input Noise Voltage: 11nV/$\sqrt{Hz}$
* Universal Operational Amplifier
* Slew Rate: 1.6V/μs

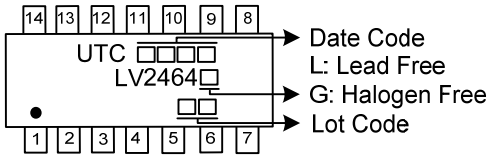
■ ORDERING INFORMATION

Ordering Number		Package	Packing
Lead Free	Halogen Free		
LV2464L-UEA-R	LV2464G-UEA-R	SOP-14U	Tape Reel
LV2464L-UEB-R	LV2464G-UEB-R	TSSOP-14U	Tape Reel

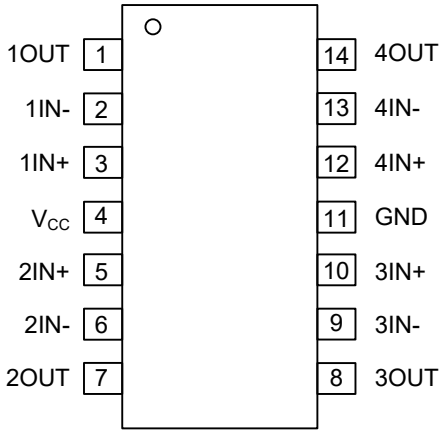
LV2464G-UEA-R
- (1)Packing Type
- (2)Package Type
- (3)Green Package

(1) R: Tape Reel
(2) UEA: SOP-14U, UEB: TSSOP-14U
(3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING



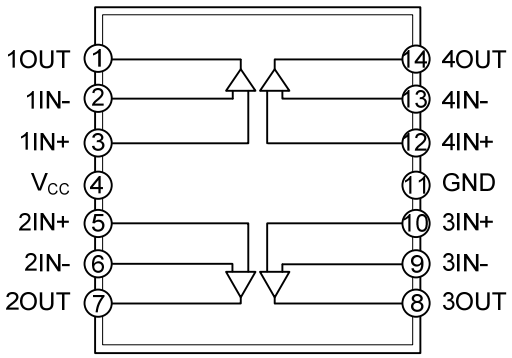
■ PIN CONFIGURATION



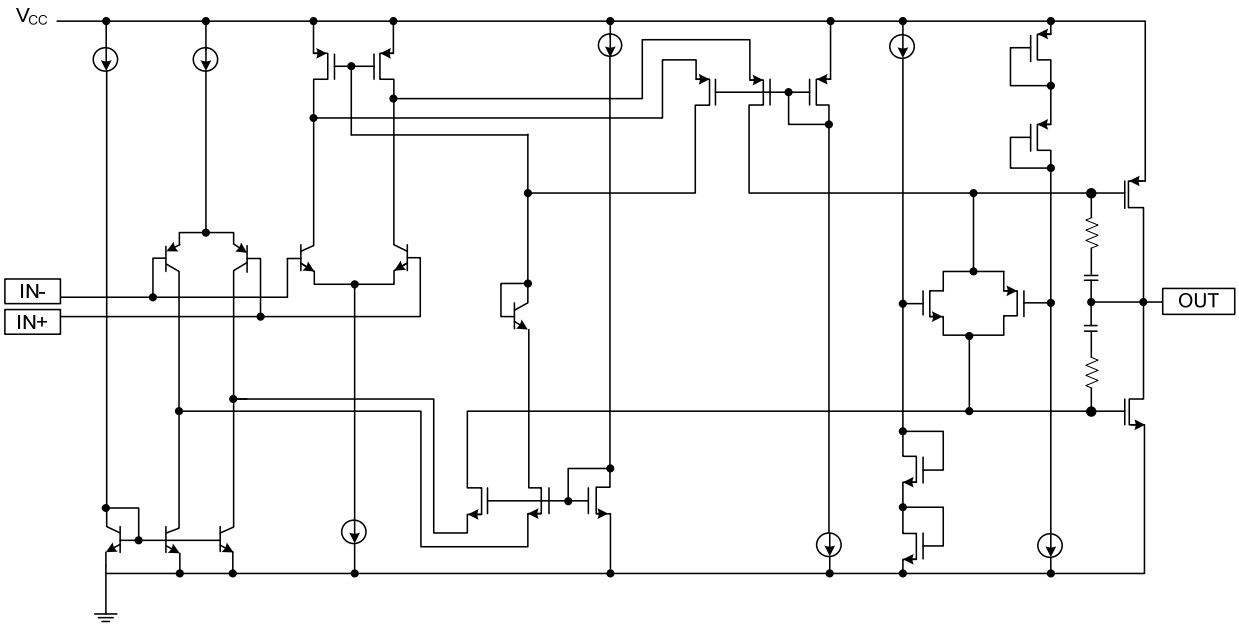
■ PIN DESCRIPTION

PIN NO.	PIN NAME	DESCRIPTION
1	1OUT	Output1
2	1IN-	Negative input1
3	1IN+	Positive input1
4	V_{CC}	Supply power
5	2IN+	Positive input2
6	2IN-	Negative input2
7	2OUT	Output2
8	3OUT	Output3
9	3IN-	Negative input3
10	3IN+	Positive input3
11	GND	Ground
12	4IN+	Positive input4
13	4IN-	Negative input4
14	4OUT	Output4

■ BLOCK DIAGRAM



■ INTERNAL SIMPLE CIRCUIT



■ ABSOLUTE MAXIMUM RATING (T_A=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Supply Voltage (Note 2)		V_{CC}	6	V
Differential Input Voltage		V_{ID}	-0.2 ~ V_{CC}+0.2	V
Output Current		I_O	±175	mA
Power Dissipation	SOP-14U	P_D	900	mW
	TSSOP-14U		600	mW
Operating Free-Air Temperature		T_A	-40 ~ +125	°C
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-60 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. All voltage values, except differential voltages, are with respect to GND.

■ RECOMMENDED OPWRAING CONDITIONS

PARAMETER		SYMBOL	RATINGS	UNIT
Supply Voltage	Single supply	V_{CC}	2.7 ~ 6	V
	Split supply		±1.35 ~ ±3	V
Common-Mode Input Voltage		V_{ICR}	0 ~ V_{CC}	V
Operating Free-Air Temperature		T_A	-40 ~ +125	°C

Note: Relative to voltage on the GND terminal of the device.

■ ELECTRICAL CHARACTERISTICS (T_A=25°C, unless otherwise specified)

V_{CC}=3V (T_A=25°C, unless otherwise specified)

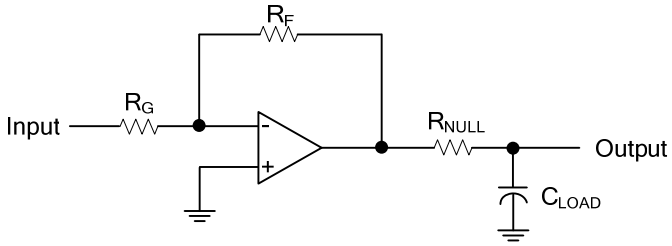
PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Input Offset Voltage	V_{IO}	V_{CC}=3V, V_{IC}=1.5V, V_O=1.5V, R_S=50Ω			500	2000	μV
Input Offset Current	I_{IO}	V_{CC}=3V, V_{IC}=1.5V, V_O=1.5V, R_S=50Ω				20	nA
Input Bias Current	I_{IB}					100	nA
High-Level Output Voltage	V_{OH}	I_{OH}=-2.5mA			2.9		V
		I_{OH}=-10mA			2.7		V
Low-Level Output Voltage	V_{OL}	V_{IC}=1.5V, I_{OL}=2.5mA			0.1		V
		V_{IC}=1.5V, I_{OL}=10mA			0.3		V
Short-Circuit Output Current	I_{OS}	Sourcing			50		mA
		Sinking			40		mA
Output Current	I_O	Measured 1V form rail			±40		mA
Large-Signal Differential Voltage Amplification	A_{VD}	R_L=10kΩ, $V_{O(PP)}$=1V		90	105		dB
Differential Input Resistance	$r_{I(D)}$	T_A=25°C			10^9		Ω
Common-Mode Input Capacitance	$C_{I(C)}$	f=10kHz, T_A=25°C			7		pF
Closed-Loop Output Impedance	Z_O	f=100kHz, A_V=10, T_A=25°C			33		Ω
Common-Mode Rejection Ratio	CMRR	V_{ICR}=0~3V, R_S=50Ω		66	80		dB
Supply Voltage Rejection Ratio ($\Delta V_{CC}/\Delta V_{IO}$)	k_{SVR}	V_{CC}=2.7~6V, V_{IC}= V_{CC}/2, No load		80	85		dB
		V_{CC}=3~5V, V_{IC}= V_{CC}/2, No load		85	95		dB
Supply Current(per channels)	I_{CC}	V_O=1.5V, No load			0.5	0.575	mA
OPWERAING CHARACTERISTICS							
Slew Rate at Unity Gain	SR	$V_{O(PP)}$=0.8V, R_L=10kΩ, C_L=160pF		0.9	1.6		V/μs
Equivalent Input Noise Voltage	V_N	f=100Hz			16		nV/$\sqrt{Hz}$
		f=1kHz			11		
Equivalent Input Noise Current	I_N	f=1kHz			0.13		pA/$\sqrt{Hz}$
Total Harmonic Distortion Plus Noise	THD+N	$V_{O(PP)}$=2V, R_L=10kΩ, f=1kHz	A_V=1		0.006		%
			A_V=10		0.02		%
			A_V=100		0.08		%
Gain-Bandwidth Product		f=10 kHz, C_L=160pF, R_L=10kΩ			5.2		MHz
Setting Time	t_S	$V_{(STEP)PP}$=2V, R_L=10kΩ, A_V=-1, C_L=10pF	0.1%		1.47		μs
			0.01%		1.78		μs
		$V_{(STEP)PP}$=2V, R_L=10kΩ, A_V=-1, C_L=56pF,	0.1%		1.77		μs
			0.01%		1.98		μs
Phase Margin at Unity Gain	φ_M	R_L=10kΩ, C_L=160pF			44		°
Gain Margin					7		dB

■ ELECTRICAL CHARACTERISTICS (T_A=25°C, unless otherwise specified)

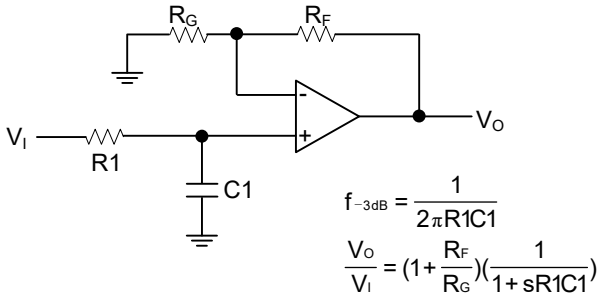
V_{CC}=5V (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Input Offset Voltage	V_{IO}	V_{CC}=5V, V_{IC}=2.5V, V_O=2.5V, R_S=50Ω			500	2000	μV
Input Offset Current	I_{IO}	V_{CC}=5V, V_{IC}=2.5V, V_O=2.5V, R_S=50Ω			7	20	nA
Input Bias Current	I_{IB}				45	100	nA
High-Level Output Voltage	V_{OH}	I_{OH}=-2.5mA			4.9		V
		I_{OH}=-10mA			4.8		V
Low-Level Output Voltage	V_{OL}	V_{IC}=2.5V, I_{OL}=2.5mA			0.1		V
		V_{IC}=2.5V, I_{OL}=10mA			0.2		V
Short-Circuit Output Current	I_{OS}	Sourcing			145		mA
		Sinking			100		mA
Output Current	I_O	Measured 1V form rail			±48		mA
Large-Signal Differential Voltage Amplification	A_{VD}	V_{IC}=2.5V, R_L=10kΩ, V_O=1~4V		92	109		dB
Differential Input Resistance	$r_{I(D)}$	T_A=25°C			10^9		Ω
Common-Mode Input Capacitance	$C_{I(C)}$	f=10kHz, T_A=25°C			7		pF
Closed-Loop Output Impedance	Z_O	f=100kHz, A_V=10, T_A=25°C			29		Ω
Common-Mode Rejection Ratio	CMRR	V_{ICR}=0~5V, R_S=50Ω		71	85		dB
Supply Voltage Rejection Ratio ($\Delta V_{CC}/\Delta V_{IO}$)	k_{SVR}	V_{CC}=2.7~6V, V_{IC}= V_{CC}/2, No load		80	85		dB
		V_{CC}=3~5V, V_{IC}= V_{CC}/2, No load		85	95		dB
Supply Current (per channels)	I_{CC}	V_O=2.5V, No load			0.55	0.65	mA
OPWERAING CHARACTERISTICS							
Slew Rate at Unity Gain	SR	$V_{O(PP)}$=2V, C_L=160pF, R_L=10 kΩ		0.9	1.6		V/μs
Equivalent Input Noise Voltage	V_N	f=100Hz			14		nV/$\sqrt{Hz}$
		f=1kHz			11		
Equivalent Input Noise Current	I_N	f=1kHz			0.13		pA/$\sqrt{Hz}$
Total Harmonic Distortion Plus Noise	THD+N	$V_{O(PP)}$=4V, R_L=10kΩ, f=1kHz	A_V=1		0.004		%
			A_V=10		0.01		%
			A_V=100		0.04		%
Gain-Bandwidth Product		f=10 kHz, C_L=160pF, R_L=10kΩ			6.4		MHz
Setting Time	t_S	$V_{(STEP)PP}$=2V, R_L=10kΩ A_V=-1, C_L=10pF	0.1%		1.53		μs
			0.01%		1.83		μs
		$V_{(STEP)PP}$=2V, R_L=10kΩ A_V=-1, C_L=56pF	0.1%		3.13		μs
			0.01%		3.33		μs
Phase Margin at Unity Gain	φ_M	R_L=10kΩ, C_L=160pF			45		°
Gain Margin					7		dB

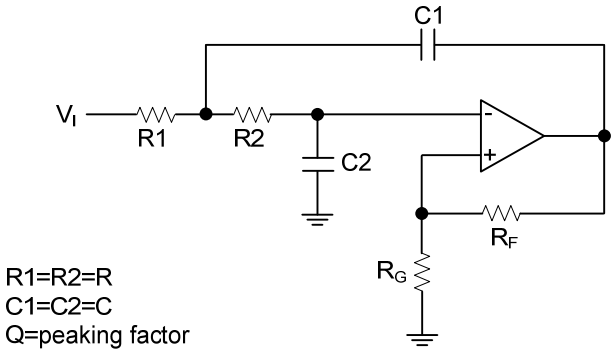
■ TYPICAL APPLICATION CIRCUIT



Driving A Capacitive Load



Single-Pole Low-Pass Filter



R1=R2=R
C1=C2=C
Q=peaking factor
(Butterworth Q=0.707)

$f_{-3dB} = \frac{1}{2\pi RC}$, $R_G = \frac{R_F}{(2-\frac{1}{Q})}$

2-Pole Low-Pass Sallen-Key Filter