

## UTT18P06

Power MOSFET

## -18.3A, -60V P-CHANNEL POWER MOSFET

### ■ DESCRIPTION

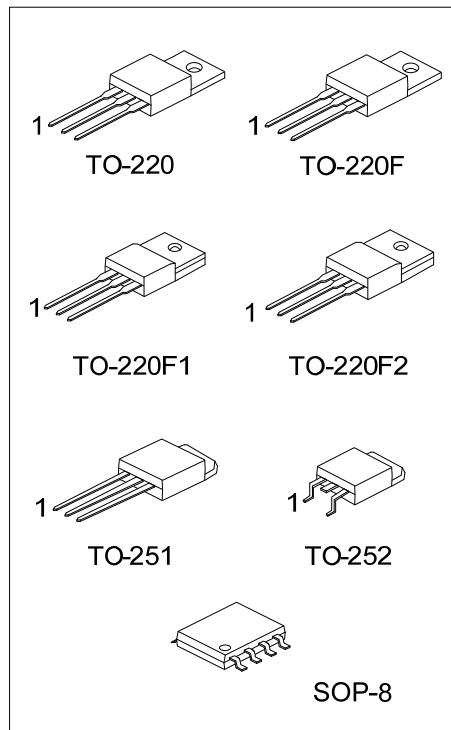
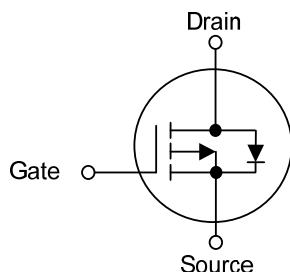
The UTC **UTT18P06** is a P-channel power MOSFET using UTC's advanced technology to provide the customers with high switching speed, cost-effectiveness and minimum on-state resistance. It can also withstand high energy in the avalanche.

### ■ FEATURES

\*  $R_{DS(ON)} \leq 70 \text{ m}\Omega$  @  $V_{GS} = -10\text{V}$ ,  $I_D = -18.3\text{A}$

\* High Switching Speed

### ■ SYMBOL



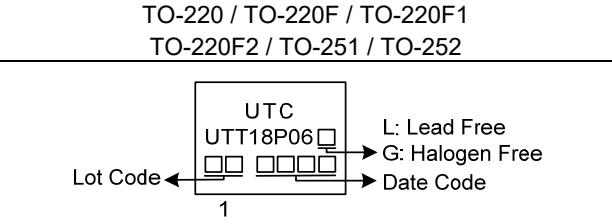
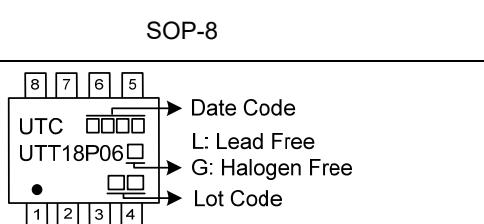
### ■ ORDERING INFORMATION

| Ordering Number | Lead Free | Halogen Free    | Package  | Pin Assignment |   |   |   |   |   |   |   | Packing   |
|-----------------|-----------|-----------------|----------|----------------|---|---|---|---|---|---|---|-----------|
|                 |           |                 |          | 1              | 2 | 3 | 4 | 5 | 6 | 7 | 8 |           |
| UTT18P06L-TA3-T |           | UTT18P06G-TA3-T | TO-220   | G              | D | S | - | - | - | - | - | Tube      |
| UTT18P06L-TF1-T |           | UTT18P06G-TF1-T | TO-220F1 | G              | D | S | - | - | - | - | - | Tube      |
| UTT18P06L-TF2-T |           | UTT18P06G-TF2-T | TO-220F2 | G              | D | S | - | - | - | - | - | Tube      |
| UTT18P06L-TF3-T |           | UTT18P06G-TF3-T | TO-220F  | G              | D | S | - | - | - | - | - | Tube      |
| UTT18P06L-TM3-T |           | UTT18P06G-TM3-T | TO-251   | G              | D | S | - | - | - | - | - | Tube      |
| UTT18P06L-TN3-R |           | UTT18P06G-TN3-R | TO-252   | G              | D | S | - | - | - | - | - | Tape Reel |
| UTT18P06L-S08-R |           | UTT18P06G-S08-R | SOP-8    | S              | S | S | G | D | D | D | D | Tape Reel |

Note: Pin Assignment: G: Gate D: Drain S: Source

|                             |  |
|-----------------------------|--|
| UTT18P06G-TA3-T<br><br><br> | (1) T: Tube, R: Tape Reel<br>(2) TA3: TO-220, TF3: TO-220F, TF1: TO-220F1,<br>TF2: TO-220F2, TM3: TO-251, TN3: TO-252<br>S08: SOP-8<br>(3) G: Halogen Free and Lead Free, L: Lead Free |
|-----------------------------|--|

**■ MARKING**

|   |  |
|---|--|
| TO-220 / TO-220F / TO-220F1<br>TO-220F2 / TO-251 / TO-252                         | SOP-8  |
|  |  |

■ ABSOLUTE MAXIMUM RATINGS ( $T_c=25^\circ\text{C}$ , unless otherwise specified)

| PARAMETER  |                        | SYMBOL    | RATINGS    | UNIT             |   |  |
|--|------------------------|-----------|------------|------------------|---|--|
| Drain-Source Voltage   |                        | $V_{DSS}$ | -60        | V                |   |  |
| Gate-Source Voltage  |                        | $V_{GSS}$ | $\pm 20$   | V                |   |  |
| Drain Current  | Continuous             | $I_D$     | -18.3      | A                |   |  |
|  | Pulsed                 | $I_{DM}$  | -36        | A                |   |  |
| Single Pulsed Avalanche Current ( $L=0.1\text{mH}$ )         |                        | $I_{AS}$  | -18.3      | A                |   |  |
| Single Pulsed Avalanche Energy ( $L=0.1\text{mH}$ ) (Note 3) |                        | $E_{AS}$  | 24.2       | mJ               |   |  |
| Peak Diode Recovery $dv/dt$ (Note 4)                         |                        | $dv/dt$   | 2.4        | V/nS             |   |  |
| Power Dissipation<br>(Note 4)                                | $T_c=25^\circ\text{C}$ | TO-220    | PD         | 80               | W |  |
|  |                        | TO-220F   |            |                  |   |  |
|  |                        | TO-220F1  |            | 32               | W |  |
|  |                        | TO-220F2  |            |                  |   |  |
|  |                        | TO-251    |            | 41               | W |  |
|  |                        | TO-252    |            | 3                | W |  |
| SOP-8  |                        | SOP-8     |            |                  |   |  |
| Junction Temperature   |                        | $T_J$     | +150       | $^\circ\text{C}$ |   |  |
| Storage Temperature  |                        | $T_{STG}$ | -55 ~ +150 | $^\circ\text{C}$ |   |  |

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. Duty cycle  $\leq 1\%$ .

4.  $I_{SD} \leq -18.0\text{A}$ ,  $dI/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$

■ THERMAL DATA

| PARAMETER           |                   | SYMBOL        | RATINGS     | UNIT               |
|---------------------|-------------------|---------------|-------------|--------------------|
| Junction to Ambient | TO-220/TO-220F    | $\theta_{JA}$ | 62.5        | $^\circ\text{C/W}$ |
|                     | TO-220F1/TO-220F2 |               | 110         | $^\circ\text{C/W}$ |
|                     | TO-251/TO-252     |               | 125 (Note)  | $^\circ\text{C/W}$ |
|                     | SOP-8             |               |             |                    |
| Junction to Case    | TO-220            | $\theta_{JC}$ | 1.56        | $^\circ\text{C/W}$ |
|                     | TO-220F/TO-220F1  |               | 3.9         | $^\circ\text{C/W}$ |
|                     | TO-220F2          |               | 3.05 (Note) | $^\circ\text{C/W}$ |
|                     | TO-251/TO-252     |               | 41.6 (Note) | $^\circ\text{C/W}$ |
|                     | SOP-8             |               |             |                    |

Note: Device mounted on FR-4 substrate  $P_C$  board, 2oz copper, with 1inch square copper plate.

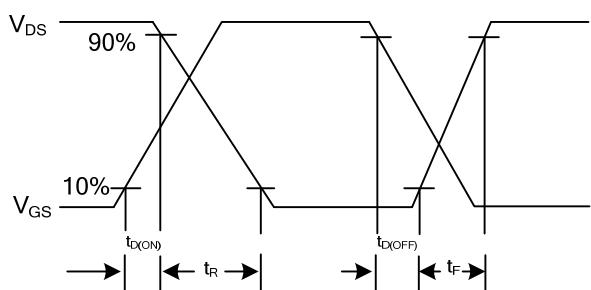
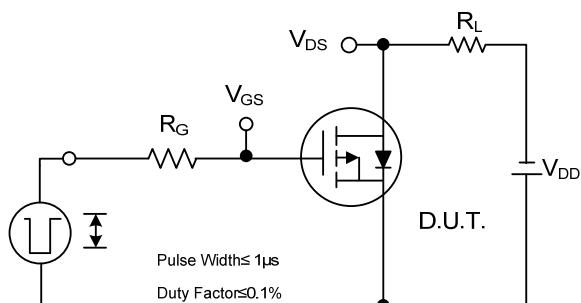
■ ELECTRICAL CHARACTERISTICS ( $T_J=25^\circ\text{C}$ , unless otherwise specified)

| PARAMETER  | SYMBOL                   | TEST CONDITIONS   | MIN  | TYP  | MAX   | UNIT             |
|--|--------------------------|---|------|------|-------|------------------|
| <b>OFF CHARACTERISTICS</b>   |                          |   |      |      |       |                  |
| Drain-Source Breakdown Voltage   | $\text{BV}_{\text{DSS}}$ | $I_D=-250\mu\text{A}, V_{GS}=0\text{V}$                                 | -60  |      |       | V                |
| Drain-Source Leakage Current   | $I_{\text{DSS}}$         | $V_{DS}=-60\text{V}, V_{GS}=0\text{V}$                                  |      |      | -1    | $\mu\text{A}$    |
| Gate-Source Leakage Current  | Forward                  | $V_{GS}=+20\text{V}, V_{DS}=0\text{V}$                                  |      |      | +100  | nA               |
|  | Reverse                  | $V_{GS}=-20\text{V}, V_{DS}=0\text{V}$                                  |      |      | -100  | nA               |
| <b>ON CHARACTERISTICS</b>  |                          |   |      |      |       |                  |
| Gate Threshold Voltage   | $V_{GS(\text{TH})}$      | $V_{DS}=V_{GS}, I_D=-250\mu\text{A}$                                    | -1.0 |      | -3.0  | V                |
| Static Drain-Source On-State Resistance  | $R_{DS(\text{ON})}$      | $V_{GS}=-10\text{V}, I_D=-18.3\text{A}$                                 |      | 55   | 70    | $\text{m}\Omega$ |
| On State Drain Current (Note 1)  | $I_{D(\text{ON})}$       | $V_{GS}=-10\text{V}, V_{DS}=-5.0\text{V}$                               | -30  |      |       | A                |
| <b>DYNAMIC PARAMETERS</b> (Note 2)   |                          |   |      |      |       |                  |
| Input Capacitance  | $C_{\text{ISS}}$         | $V_{GS}=0\text{V}, V_{DS}=-25\text{V}, f=1.0\text{MHz}$                 |      | 1415 |       | pF               |
| Output Capacitance   | $C_{\text{OSS}}$         |   |      | 100  |       | pF               |
| Reverse Transfer Capacitance   | $C_{\text{RSS}}$         |   |      | 81   |       | pF               |
| <b>SWITCHING PARAMETERS</b>  |                          |   |      |      |       |                  |
| Total Gate Charge  | $Q_G$                    | $V_{GS}=-10\text{V}, V_{DS}=-48\text{V}, I_D=-18.3\text{A}$ (Note 1, 2) |      | 33   |       | nC               |
| Gate to Source Charge  | $Q_{GS}$                 |   |      | 5    |       | nC               |
| Gate to Drain Charge   | $Q_{GD}$                 |   |      | 9    |       | nC               |
| Turn-ON Delay Time   | $t_{D(\text{ON})}$       | $V_{DD}=-30\text{V}, I_D=-18.3\text{A}, R_G=3\Omega$ (Note 1, 2)        |      | 6    |       | ns               |
| Rise Time  | $t_R$                    |   |      | 18   |       | ns               |
| Turn-OFF Delay Time  | $t_{D(\text{OFF})}$      |   |      | 32   |       | ns               |
| Fall-Time  | $t_F$                    |   |      | 20   |       | ns               |
| <b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b> ( $T_c=25^\circ\text{C}$ ) (Note 2) |                          |   |      |      |       |                  |
| Maximum Body-Diode Continuous Current  | $I_S$                    |   |      |      | -18.3 | A                |
| Maximum Body-Diode Pulsed Current  | $I_{SM}$                 |   |      |      | -36   | A                |
| Drain-Source Diode Forward Voltage   | $V_{SD}$                 | $I_F=-18.3\text{A}, V_{GS}=0\text{V}$ (Note 1)                          |      | -1.0 | -1.5  | V                |
| Body Diode Reverse Recovery Time   | $t_{rr}$                 | $I_F=-18.3\text{A}, dI_F/dt=100\text{A}/\mu\text{s}$                    |      | 57   |       | ns               |
| Reverse Recovery Charge  | $Q_{rr}$                 |   |      | 49   |       | nC               |

Notes: 1. Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$ .

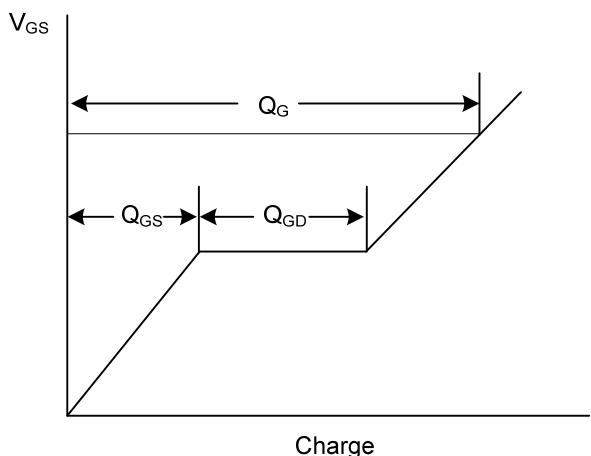
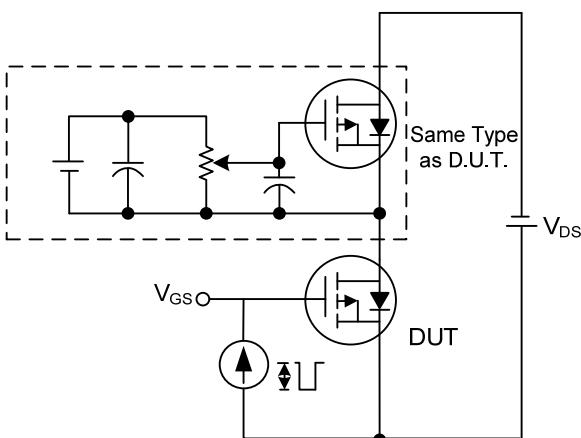
2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS



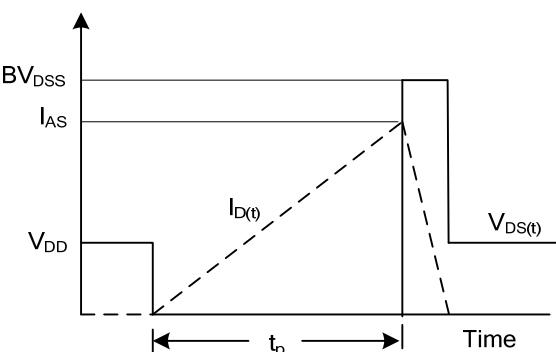
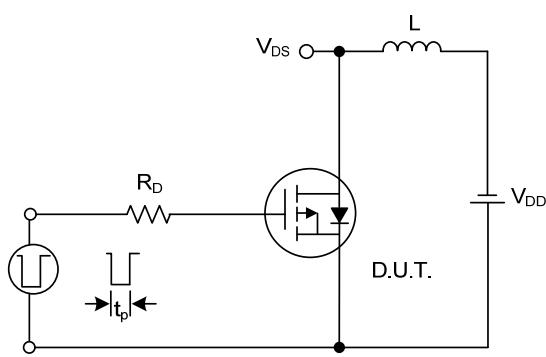
Switching Test Circuit

Switching Waveforms



Gate Charge Test Circuit

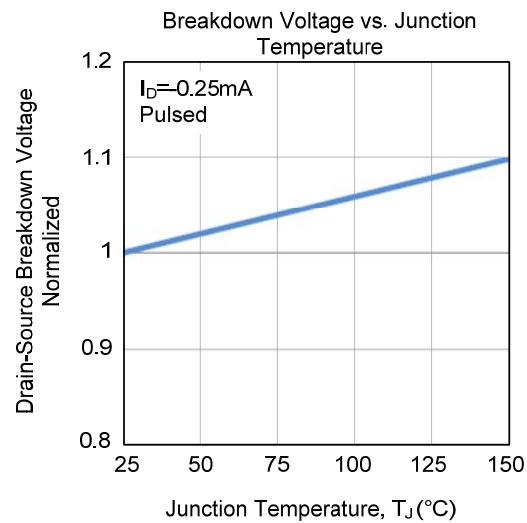
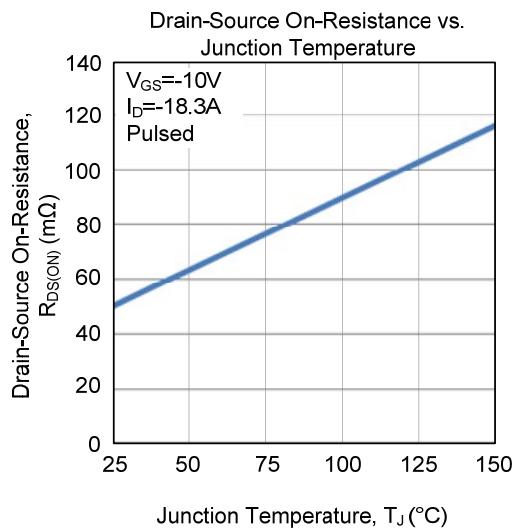
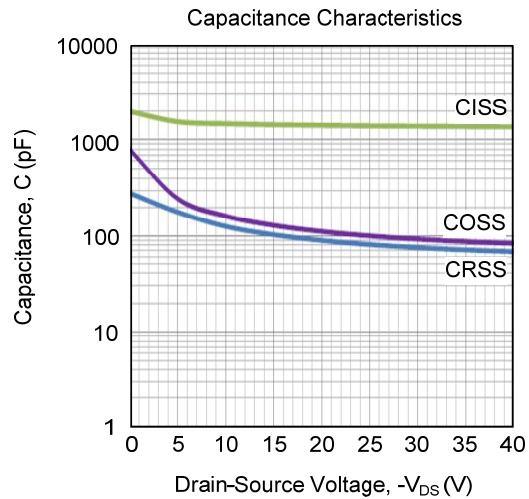
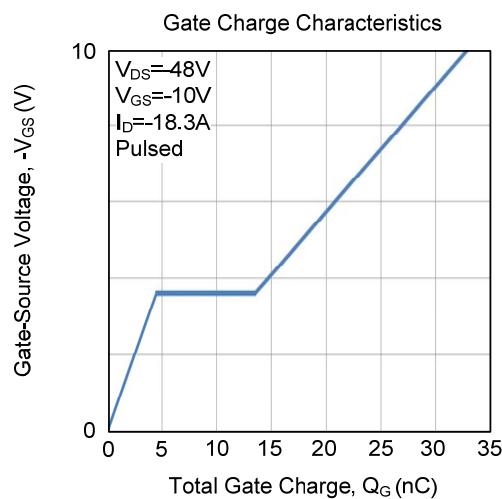
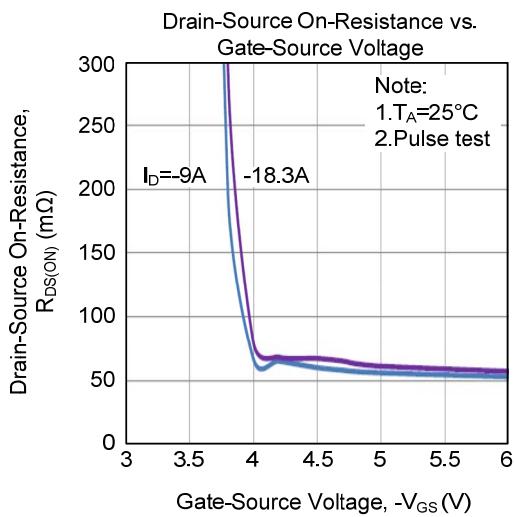
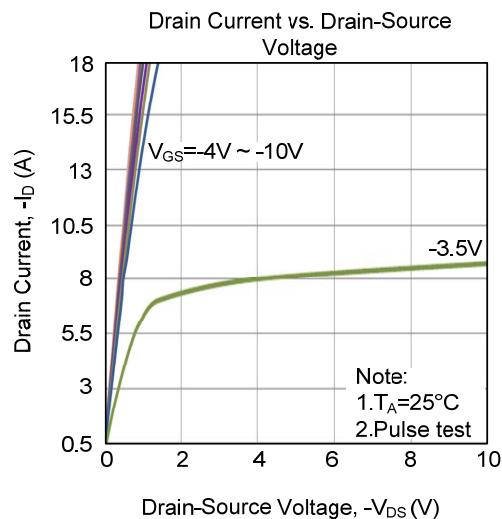
Gate Charge Waveform



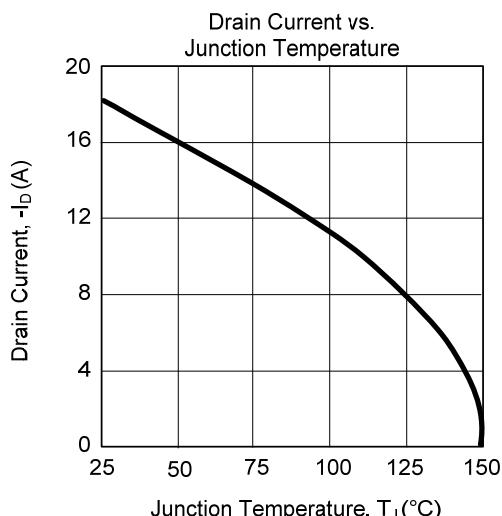
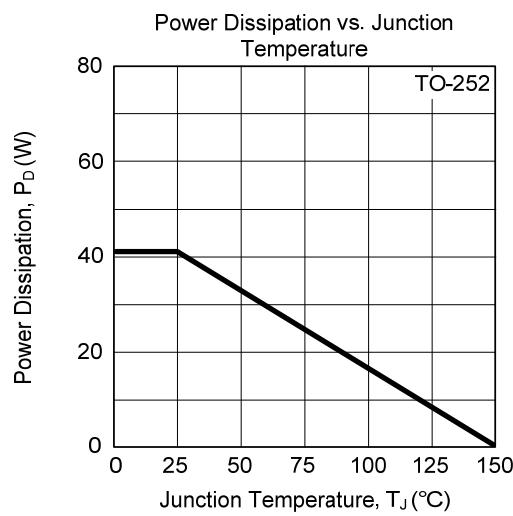
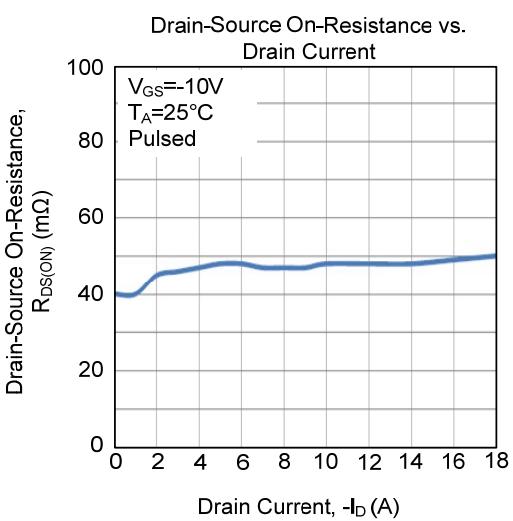
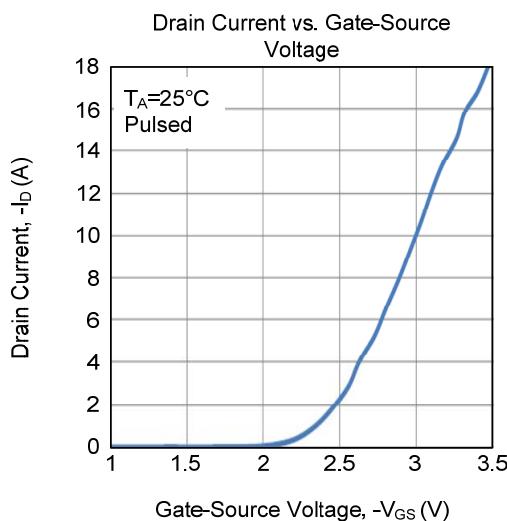
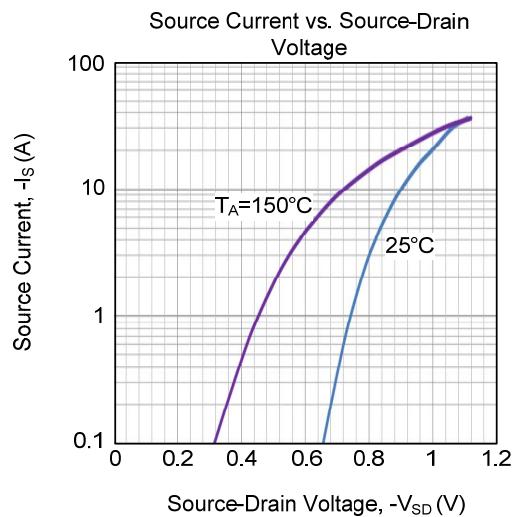
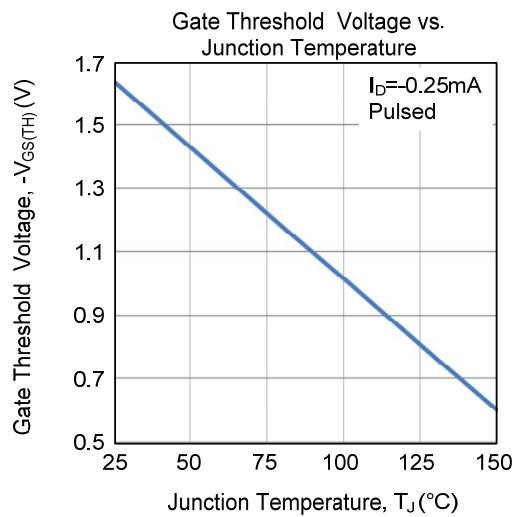
Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

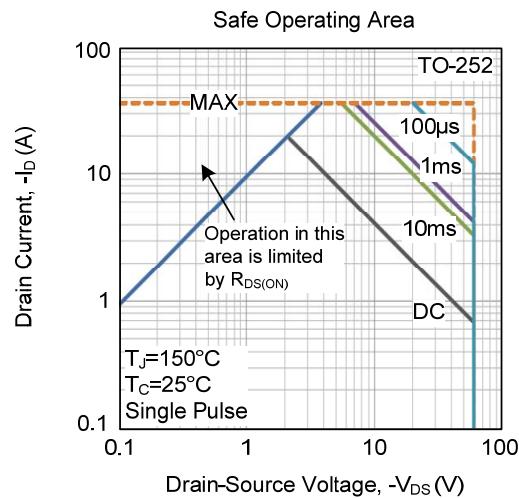
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



- TYPICAL CHARACTERISTICS (Cont.)



UTC assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all UTC products described or contained herein. UTC products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. UTC reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.