



UF7476

POWER MOSFET

N-CHANNEL POWER MOSFET

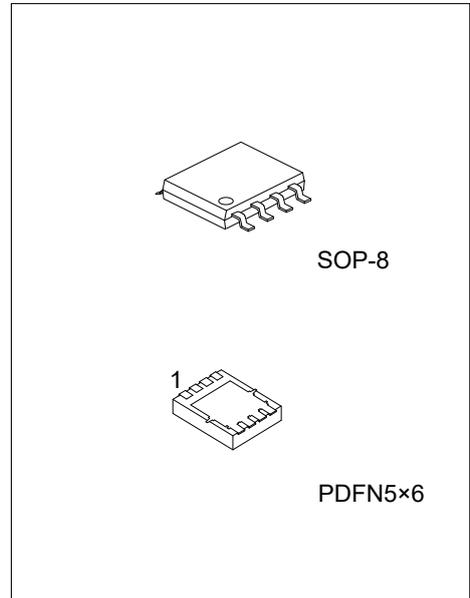
■ DESCRIPTION

The UTC **UF7476** is a N-channel Power MOSFET, it uses UTC's advanced technology to provide the customers with high switching speed and minimum on-state resistance.

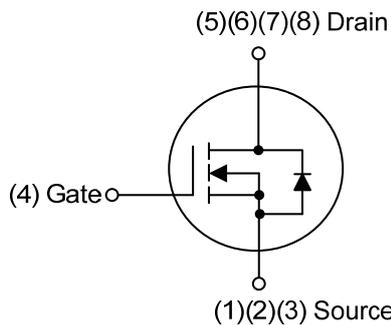
The UTC **UF7476** is suitable for various applications such as power management for Netcom, computing and portable applications, etc.

■ FEATURES

- * $R_{DS(ON)} \leq 8.0\ m\Omega$ @ $V_{GS}=4.5V$, $I_D=15A$
- $R_{DS(ON)} \leq 30\ m\Omega$ @ $V_{GS}=2.8V$, $I_D=12A$
- * Ultra-low gate impedance
- * High switching speed



■ SYMBOL



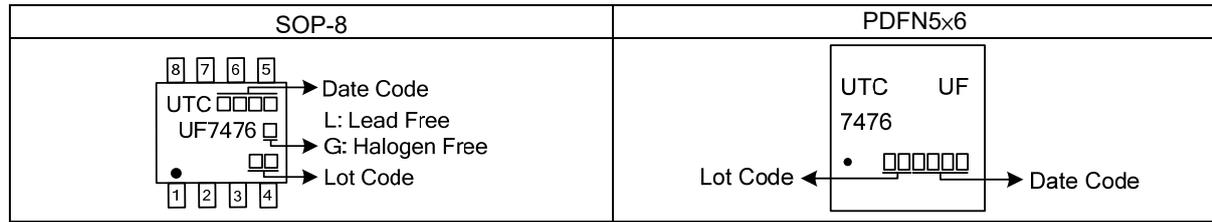
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UF7476L-S08-R	UF7476G-S08-R	SOP-8	S	S	S	G	D	D	D	D	Tape Reel
UF7476L-P5060-R	UF7476G-P5060-R	PDFN5x6	S	S	S	G	D	D	D	D	Tape Reel

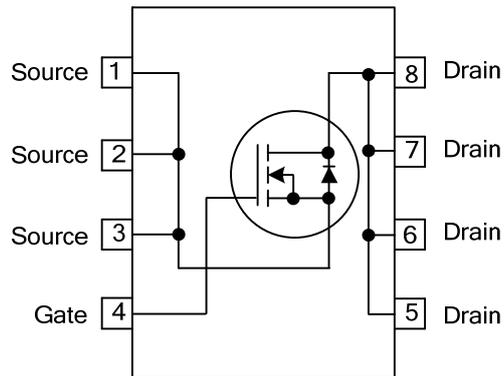
Note: Pin Assignment: G: Gate D: Drain S: Source

<p>UF7476G-S08-R</p> <ul style="list-style-type: none"> (1) Packing Type (2) Package Type (3) Green Package 	<ul style="list-style-type: none"> (1) R: Tape Reel (2) S08: SOP-8, P5060: PDFN5x6 (3) G: Halogen Free and Lead Free, L: Lead Free
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MARKING



PIN CONFIGURATION



■ ABSOLUTE MAXIMUM RATING

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		V_{DS}	12	V	
Gate-Source Voltage		V_{GS}	± 12	V	
Drain Current	Continuous	$T_A=25^\circ\text{C}$	15	A	
		$T_A=70^\circ\text{C}$	12	A	
	Pulsed (Note 1)		I_{DM}	120	A
Avalanche Energy (Note 3)		E_{AS}	71	mJ	
Power Dissipation (Note 4)	SOP-8	$T_A=25^\circ\text{C}$	P_D	2.5	W
	PDFN5x6			20	W
Junction Temperature		T_J	+150	$^\circ\text{C}$	
Storage Temperature Range		T_{STG}	-55 ~ +150	$^\circ\text{C}$	

- Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
 3. $L = 0.8\text{mH}$, $I_{AS} = 13.3\text{A}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$
 4. When mounted on 1 inch square copper board.

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient (Note 4)	SOP-8	θ_{JA}	75	$^\circ\text{C/W}$
	PDFN5x6		65	$^\circ\text{C/W}$

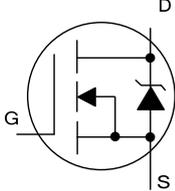
Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=250\mu\text{A}$, $V_{GS}=0\text{V}$	12			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=9.6\text{V}$, $V_{GS}=0\text{V}$			100	μA
Gate-Source Leakage Current	Forward Reverse	I_{GSS}	$V_{GS}=12\text{V}$, $V_{DS}=0\text{V}$ $V_{GS}=-12\text{V}$, $V_{DS}=0\text{V}$		200	nA
					-200	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$	0.6		1.9	V
Static Drain-Source On-State Resistance (Note)	$R_{DS(ON)}$	$V_{GS}=4.5\text{V}$, $I_D=15\text{A}$			8.0	m Ω
		$V_{GS}=2.8\text{V}$, $I_D=12\text{A}$			30	m Ω
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{GS}=0\text{V}$, $V_{DS}=6.0\text{V}$, $f=1.0\text{MHz}$		1750		pF
Output Capacitance	C_{OSS}			770		pF
Reverse Transfer Capacitance	C_{RSS}			730		pF
SWITCHING PARAMETERS						
Total Gate Charge	Q_G	$I_D=12\text{A}$, $V_{DS}=10\text{V}$, $V_{GS}=4.5\text{V}$		29		nC
Gate to Source Charge	Q_{GS}			3		nC
Gate to Drain ("Miller") Charge	Q_{GD}			13		nC
Turn-ON Delay Time	$t_{D(ON)}$	$V_{DD}=6.0\text{V}$, $V_{GS}=4.5\text{V}$, $I_D=12\text{A}$, $R_G=3\Omega$		12		ns
Rise Time	t_R			19		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			40		ns
Fall Time	t_F			36		ns

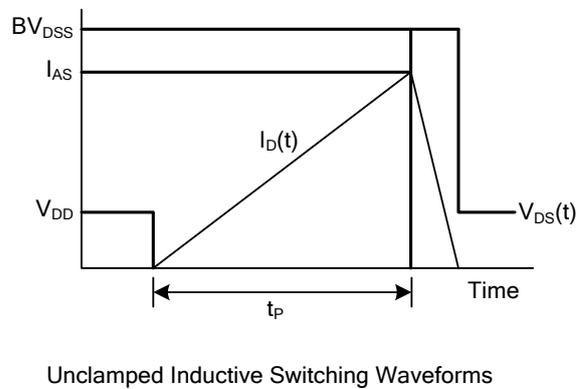
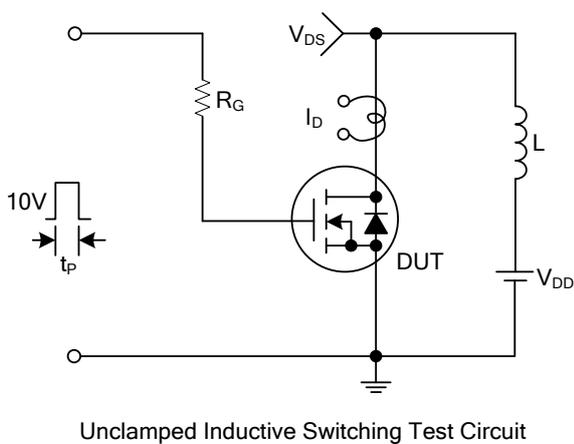
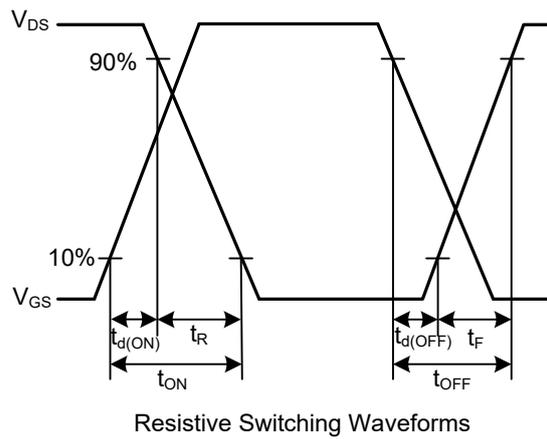
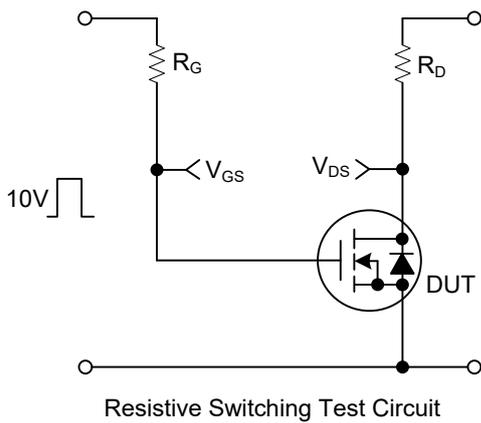
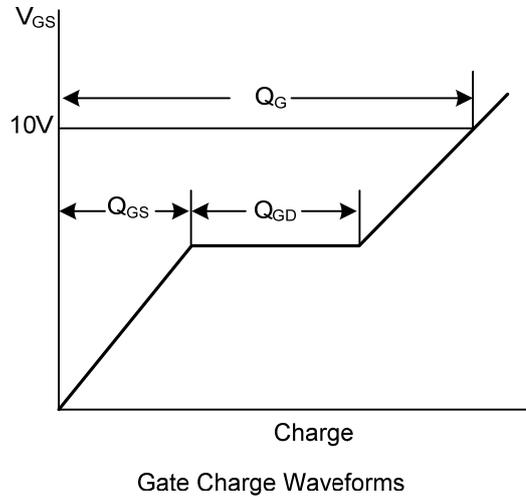
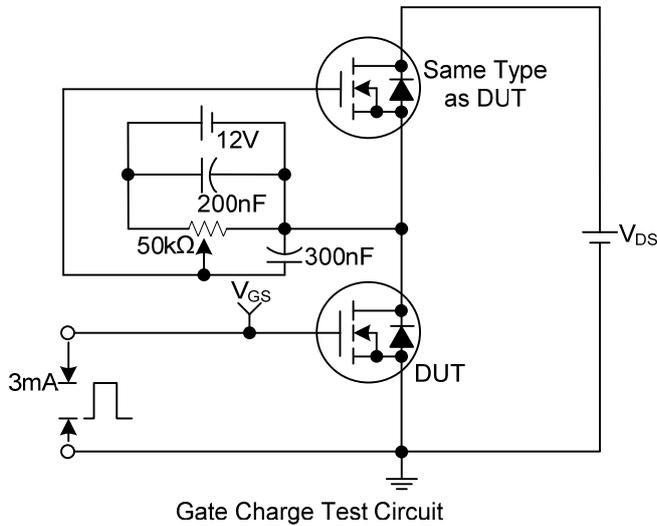
- Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.
 2. Essentially independent of operating temperature.

■ ELECTRICAL CHARACTERISTICS (Cont.)

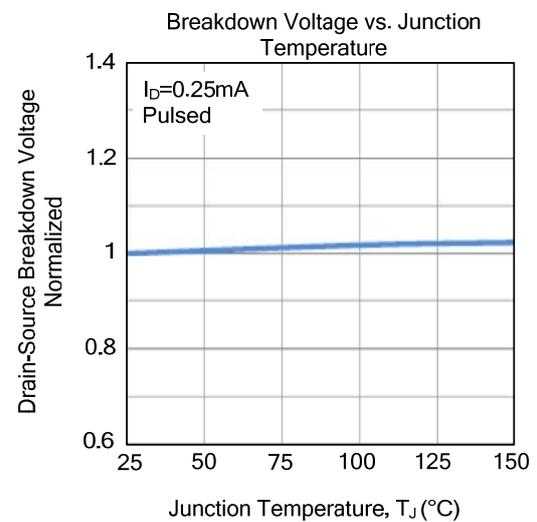
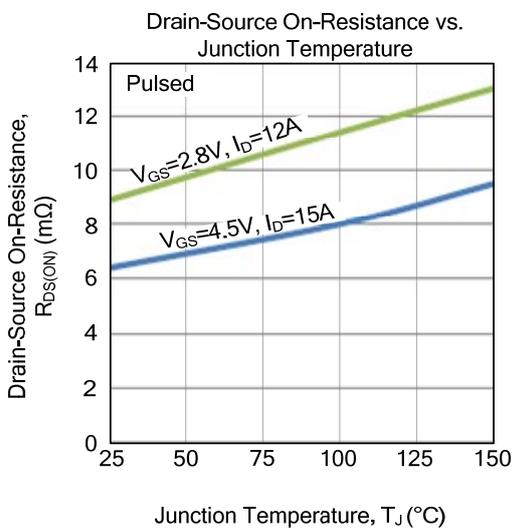
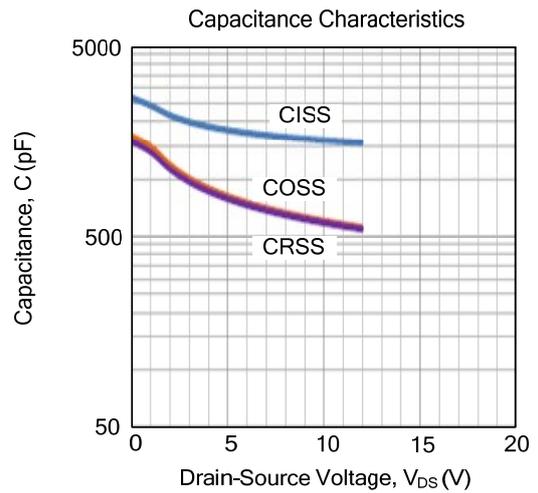
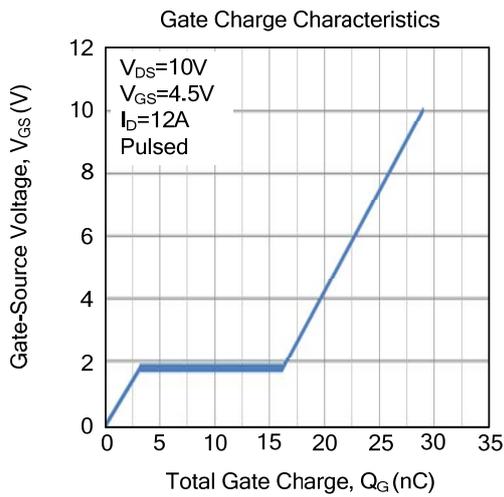
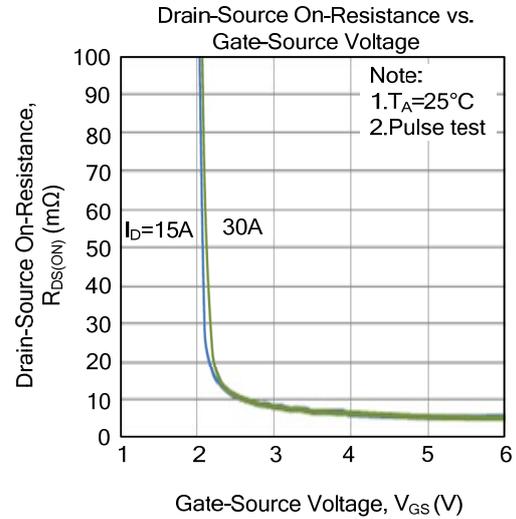
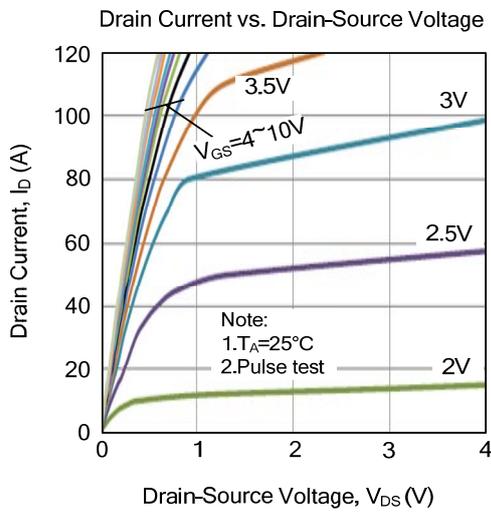
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body Diode Continuous Source Current	I_S	MOSFET symbol showing the integral reverse p-n junction diode.			2.5	A
Maximum Body Diode Pulsed Current (Note 1)	I_{SM}				120	A
Drain-Source Diode Forward Voltage (Note)	V_{SD}	$I_S=12A, V_{GS}=0V, T_J=25^{\circ}C$		0.87	1.2	V

Notes: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$.

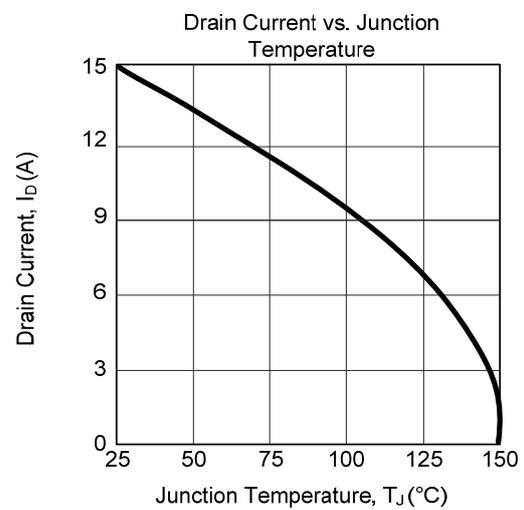
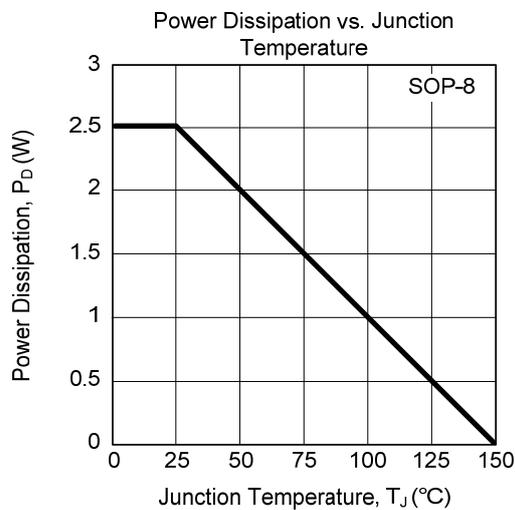
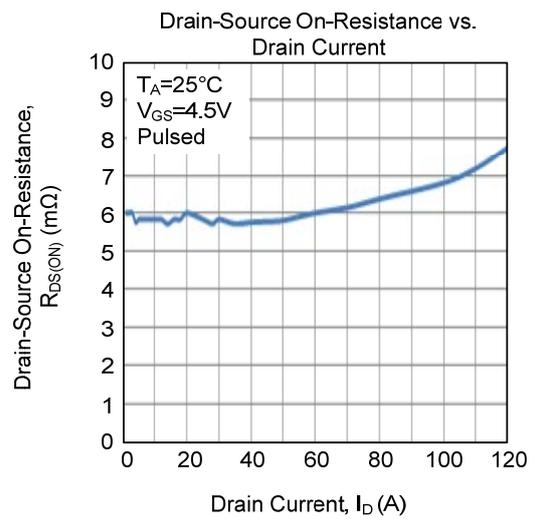
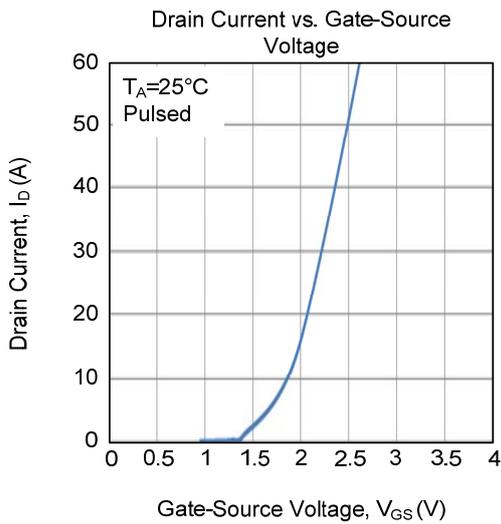
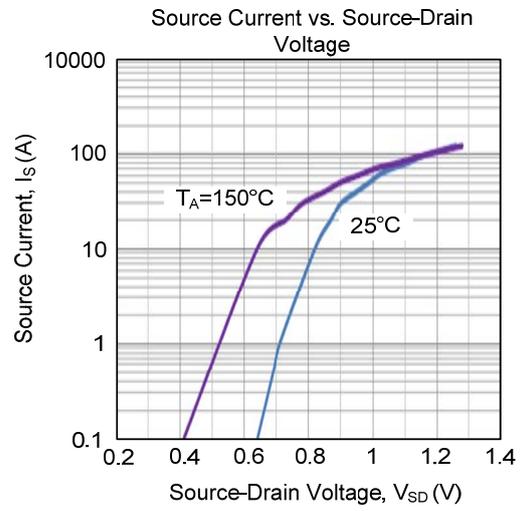
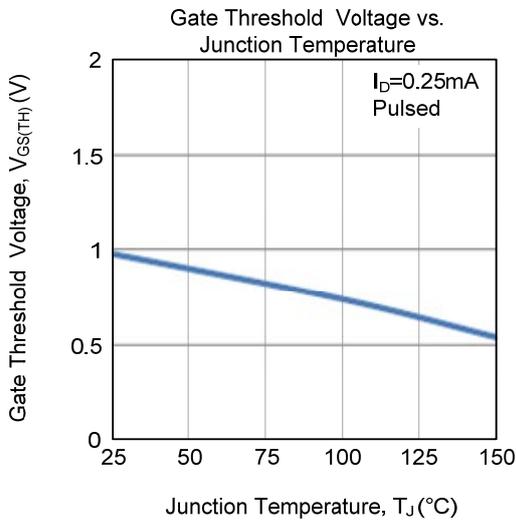
TEST CIRCUITS AND WAVEFORMS



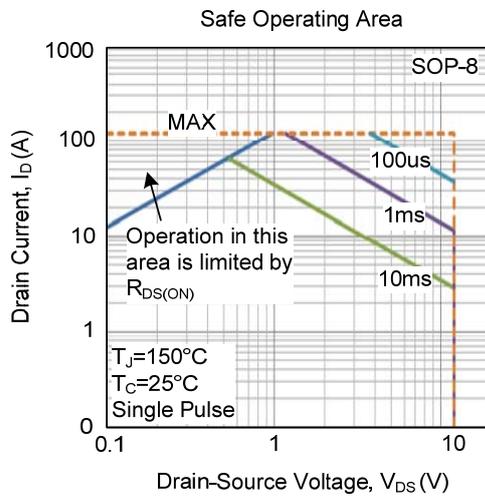
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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