

UNISONIC TECHNOLOGIES CO., LTD

BTA306A TRIAC

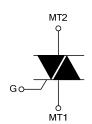
6A TRIACS

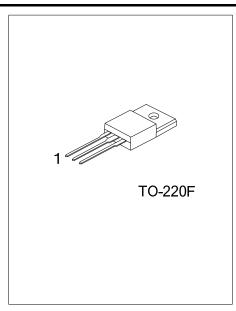
■ DESCRIPTION

The UTC **BTA306A** is a 6A triacs which can be operated in 3 quadrants only, it uses UTC's advanced technology to provide customers with high commutation performances, etc.

The UTC **BTA306A** is suitable for inductive load switching operations, also can be used in ON/OFF function applications such as induction motor starting circuits, heating regulation, static relays etc.

■ SYMBOL

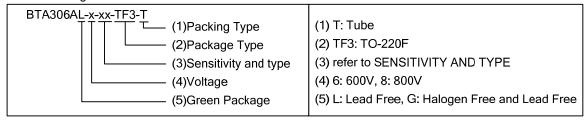




ORDERING INFORMATION

Ordering	Daakaga	Pin .	Assignr	Dooking			
Lead Free	Halogen Free	Package	1	2	3	Packing	
BTA306AL-x-xx-TF3-T	BTA306AG-x-xx-TF3-T	TO-220F	MT1	MT2	G	Tube	

Note: Pin Assignment: MT1: MT1 MT2: MT2 G: Gate

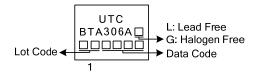


■ SENSITIVITY AND TYPE

	VOL ⁻	TAGE	CENCITIVITY	TYPF		
PART NUMBER	600V	800V	SENSITIVITY	ITPE		
BW	0	0	50mA	SNUBBERLESS		
CW	0	0	35mA	SNUBBERLESS		
SW	0	0	10mA	LOGIC LEVEL		
TW	0	0	5mA	LOGIC LEVEL		

⊚: Available

MARKING



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■ ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT		
RMS On-State Current (Full Sine Wave)	T _C =105°C		I _{T(RMS)}	6	Α
Non Repetitive Surge Peak On-State	F=50Hz	t=20ms	I _{TSM}	60	Α
Current (Full Cycle T _J initial=25°C)	F=60Hz	t=16.7ms	- 10W	63	Α
I ² t Value for Fusing	t _P =10ms		l ² t	21	A^2s
Critical Rate of Rise of On-State Current: I _G =2xI _{GT} , tr≤100ns	F=120Hz	T _J =125°C	dl/dt	50	A/µs
Peak Gate Current	t _P =20µs	T _J =125°C	I_{GM}	4	Α
Average Gate Power Dissipation	T _J =125°C	$P_{G(AV)}$	1	W	
Operating Junction Temperature			T_J	-40~+125	ç
Storage Junction Temperature		T_{STG}	-40~+150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL RESISTANCES

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	60	°C/W
Junction to Case (AC)	θ_{JC}	2.7	°C/W

■ ELECTRICAL CHARACTERISTICS (T_J= 25°C, unless otherwise specified)

FOR SNUBBERLESS AND LOGIC LEVEL (3 QUADRANTS)

PARAMETER	SYMBOL	TEST		TW		SW		CW			BW			UNIT		
PARAMETER	STIMBUL	CONDITION	ONS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNIT
Gate Trigger Current (Note 1)	I _{GT}	V _D =12V	1-11-111			5			10			35			50	mA
Gate Trigger Voltage	V_{GT}	R _L =30Ω	1-11-111			1.3			1.3			1.3			1.3	V
Gate Non-Trigger Voltage	V_{GD}	$V_D=V_{DRM}$, $R_L=3.3k\Omega$, $T_J=125^{\circ}C$	1-11-111	0.2			0.2			0.2			0.2			V
Holding Current (Note 2)	l _Η	I _T =100mA				10			15			35			50	mA
Latching Current	IL	I _G =1.2I _{GT}	I-III II			10 15			25 30			50 60			70 80	mA mA
Critical Rate of Rise of Off-State Voltage (Note 2)	dV/dt	V _D =67%V _{DF} Gate Open, T _J =125°C		20			40			400			1000			V/µs
Critical Rate of Rise of		(dV/dt)c=0.1 T _J =125°C	IV/μs	2.7			3.5									A/ms
Off-State Voltage at	(dl/dt)c	(dV/dt)c=10 T _J =125°C	V/μs,	1.2			2.4									A/ms
Commutation (Note 2)		Without Snu TJ= 125°C	ıbber							3.5			5.3			A/ms

Notes: 1. Minimum I_{GT} is guaranteed at 5% of I_{GT} max.

2. For both polarities of MT2 referenced to MT1.

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■ STATIC CHARACTERISTICS

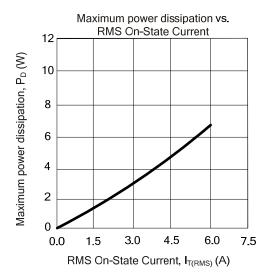
PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Peak On-State Voltage (Note 2)	V_{TM}	I _{TM} =8.5A, t _P =380µs	TJ=25°C			1.55	V
Threshold Voltage (Note 2)	V _{TO}	, , , ,	T _J =125°C			0.85	V
Dynamic Resistance (Note 2)	R_D		T _J =125°C			60	mΩ
Repetitive Peak Off-State Current	I _{DRM}	\ \ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\	TJ=25°C			5	μA
	I _{RRM}	V _{DRM} =V _{RRM}	T _J =125°C			1	mA

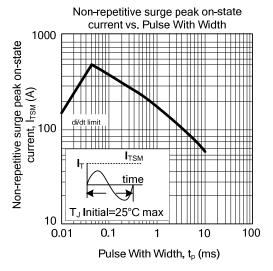
Notes: 1. Minimum I_{GT} is guaranteed at 5% of I_{GT} max.

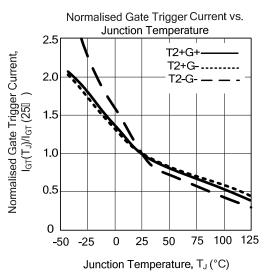
2. For both polarities of MT2 referenced to MT1.

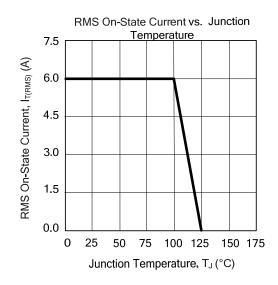
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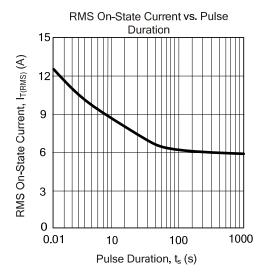
■ TYPICAL CHARACTERISTICS

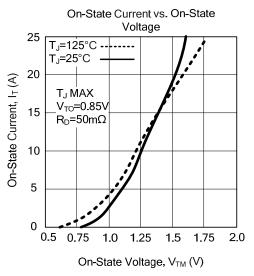




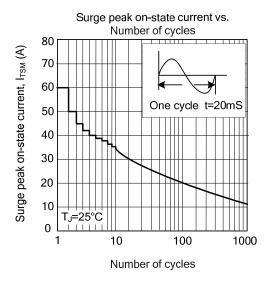








■ TYPICAL CHARACTERISTICS (Cont.)



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