

# UTC UNISONIC TECHNOLOGIES CO., LTD

**BTB316A TRIAC** 

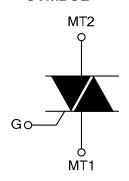
# **16A TRIACS**

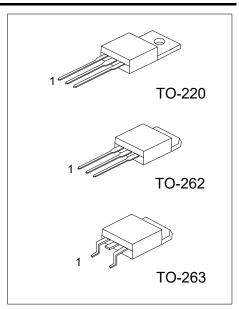
#### DESCRIPTION

The UTC BTB316A is a 16A triacs which can be operated in 3 quadrants, it uses UTC's advanced technology to provide customers with high commutation performances.

The UTC BTB316A is suitable for inductive load switching operations, also can be used in ON/OFF function applications such as induction motor starting circuits, heating regulation, static relays etc.

#### **SYMBOL**

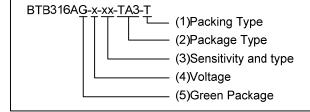




## **ORDERING INFORMATION**

Ordering Number		Daakaaa	Pin	Assignm	Daakina	
Lead Free	Halogen Free	Package	1	2	3	Packing
BTB316AL-x-xx-TA3-T	BTB316AG-x-xx-TA3-T	TO-220	MT1	MT2	G	Tube
BTB316AL-x-xx-T2Q-T	BTB316AG-x-xx-T2Q-T	TO-262	MT1	MT2	G	Tube
BTB316AL-x-xx-TQ2-T	BTB316AG-x-xx-TQ2-T	TO-263	MT1	MT2	G	Tube
BTB316AL-x-xx-TQ2-R	BTB316AG-x-xx-TQ2-R	TO-263	MT1	MT2	G	Tape Reel

Note: Pin Assignment: MT1: MT1 MT2: MT2 G: Gate



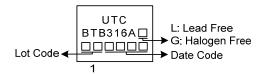
- (1) T: Tube, R: Tape Reel
- (2) TA3: TO-220, T2Q: TO-262, TQ2: TO-263
- (3) refer to SENSITIVITY AND TYPE
- (4) 6: 600V, 8: 800V, 10: 1000V
- (5) G: Halogen Free and Lead Free, L: Lead Free

### **SENSITIVITY AND TYPE**

DADT NUMBER		VOLTAGE		OFNOITIV/ITV	TYPE		
PART NUMBER	600V	800V	1000V	SENSITIVITY			
BW	0	0	0	50mA	SNUBBERLESS		
CW	0	0	0	35mA	SNUBBERLESS		
SW	0	0	0	10mA	LOGIC LEVEL		

⊚: Available

## **■** MARKING



#### ■ ABSOLUTE MAXIMUM RATINGS

PARAMETER			SYMBOL	RATINGS	UNIT
RMS On-State Current (Full Sine Wave) T <sub>C</sub> =86°C		I <sub>T(RMS)</sub>	16	Α	
Non Repetitive Surge Peak	F=50 Hz	t=20ms	,	160	Α
On-State Current (Full Cycle, T <sub>J</sub> initial=25°C)	F=60 Hz	t=16.7ms	I <sub>TSM</sub>	168	Α
I <sup>2</sup> t Value for Fusing	t <sub>P</sub> =10ms		l <sup>2</sup> t	144	$A^2s$
Critical Rate of Rise of On-State Current I <sub>G</sub> =2xI <sub>GT</sub> , tr≤100ns	F=120 Hz T <sub>J</sub> =125°C		dl/dt	50	A/µs
Non Repetitive Surge Peak Off-State Voltage	t <sub>P</sub> =10ms T <sub>J</sub> =25°C		V <sub>DSM</sub> /V <sub>RSM</sub>	V <sub>DRM</sub> /V <sub>RRM</sub> +100	V
Peak Gate Current	t <sub>P</sub> =20µs	T <sub>J</sub> =125°C	$I_{GM}$	4	Α
Average Gate Power Dissipation T <sub>J</sub> =125°C		$P_{G(AV)}$	1	W	
Operating Junction Temperature		$T_J$	-40 ~ +125	°C	
Storage Junction Temperature			T <sub>STG</sub>	-40 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### **■ THERMAL DATA**

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	$\theta_{JA}$	60	°C/W
Junction to Case (AC)	θις	1.2	°C/W

## ■ **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> =25°C unless otherwise specified)

## FOR SNUBBERLESS TYPE and LOGIC LEVEL TYPE (3 QUADRANTS)

			SW			CW			BW				
PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNIT
Gate Trigger Current (Note 1)	I <sub>GT</sub>	V <sub>D</sub> =12V, R <sub>L</sub> =33Ω	1-11-111			10			35			50	mA
Gate Trigger Voltage	V <sub>GT</sub>		1-11-111			1.3			1.3			1.3	V
Gate Non-Trigger Voltage	$V_{\sf GD}$	$V_D=V_{DRM}$ , $R_L=3.3k\Omega$ , $T_J=125^{\circ}C$	1-11-111	0.2			0.2			0.2			٧
Holding Current (Note 2)	lμ	I <sub>T</sub> =500mA				15			35			50	mA
Latching Current	lι	I <sub>G</sub> =1.2I <sub>GT</sub>	I-III			25			50			70	mA
Latering Current			II			30			60			80	mA
Critical Rate of Rise of Off-State Voltage (Note 2)	dV/dt	V <sub>D</sub> =67%V <sub>DRM</sub> , Gate Open, T <sub>J</sub> =125°C		40			500			1000			V/µs
Critical Rate of Rise of Off-State Voltage at Commutation(Note 2)	T <sub>.</sub> =125°C	(dV/dt)c=0.1V/μs, Τ <sub>J</sub> =125°C		8.5									A/ms
	(dl/dt)c	(dV/dt)c=10V/μs, T <sub>J</sub> =125°C		3.0									A/ms
	١	Without Snubber T <sub>J</sub> =125°C					8.5			14			A/ms

Notes: 1. Minimum  $I_{\text{GT}}$  is guaranteed at 5% of  $I_{\text{GT}}$  max.

2. For both polarities of MT2 referenced to MT1.

## ■ STATIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Peak On-State Voltage(Note 2)	V <sub>TM</sub>	I <sub>TM</sub> =22.5A, t <sub>p</sub> =380μs	TJ=25°C			1.55	V
Threshold Voltage(Note 2)	V <sub>TO</sub>		TJ=125°C			0.85	V
Dynamic Resistance(Note 2)	$R_D$		T <sub>J</sub> =125°C			25	mΩ
Repetitive Peak Off-State Current	I <sub>DRM</sub>	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	TJ=25°C			5	μA
	IRRM	V <sub>DRM</sub> =V <sub>RRM</sub>	T <sub>1</sub> =125°C			2	mA

Note: 1. Minimum  $I_{\text{GT}}$  is guaranteed at 5% of  $I_{\text{GT}}$  max.

<sup>2.</sup> For both polarities of MT2 referenced to MT1.

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