

UTC UNISONIC TECHNOLOGIES CO., LTD

SB1060 **Preliminary DIODE**

10A SCHOTTKY BARRIER RECTIFIER

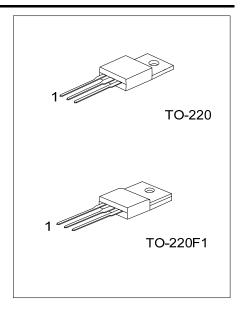
DESCRIPTION

The UTC SB1060 is a Schottky Rectifier with high current capacity and low forward voltage.

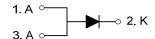
The UTC SB1060 is suitable for polarity protection ,low voltage and high frequency inverters and free wheeling applications.

FEATURES

- * Guard Ring Transient Protection
- * High surge Current Capability
- * High Current Capability
- * Low Forward Voltage
- * For use in low voltage, high frequency inverters



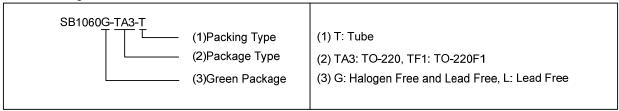
SYMBOL



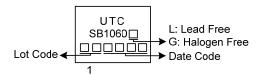
ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			Dacking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
SB1060L-TA3-T	SB1060G-TA3-T	TO-220	Α	K	Α	Tube	
SB1060L-TF1-T	SB1060G-TF1-T	TO-220F1	Α	K	Α	Tube	

Note: Pin Assignment: A: Anode K: Cathode



MARKING



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■ ABSOLUTE MAXIMUM RATINGS (T_A =25°C unless otherwise specified.)

PARAMETER	SYMBOL	RATINGS	UNIT
Working Peak Reverse Voltage	V_{RWM}	60	V
Repetitive Peak Reverse Voltage	V_{RRM}	60	V
RMS Reverse Voltage	$V_{R(RMS)}$	42	V
DC Blocking Voltage	V_R	60	V
Average Rectified Output Current	Io	10	Α
Non-repetitive Peak Forward Surge Current 8.3 ms Single Half-Sine-Wave	I _{FSM}	150	Α
Operating Temperature	TJ	-65 ~ +150	°C
Storage Temperature	T _{STG}	-65 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	60	°C/W
Junction to Case	$\theta_{ m JC}$	3.0	°C/W

■ ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	$V_{(BR)R}$	I _R =0.50mA	60			V
Forward Voltage Drop	I V _{EM}	I _F =10A, T _J =25°C			0.75	V
		I _F =10A, T _J =100°C			0.70	V
Leakage Current (Note 1)	PМ	V _R =60V, T _J =25°C			200	μA
		V _R =60V, T _J =100°C			50	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.

^{2.} These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

^{2.} Thermal resistance junction to case mounted on heatsink.

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