

U74AHC21

CMOS IC

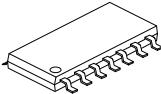
DUAL 4-INPUT AND GATES

■ DESCRIPTION

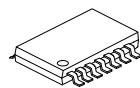
The **U74AHC21** contains two independent 4-input AND gates. They perform the Boolean function $Y=A \bullet B \bullet C \bullet D$ or $Y=\overline{\overline{A}} + \overline{\overline{B}} + \overline{\overline{C}} + \overline{\overline{D}}$ in positive logic.

■ FEATURES

- * Operation voltage range: 2 ~ 5.5V
- * Low power dissipation: $I_{CC}=2\mu A$ (Max.)
- * $\pm 8mA$ output drive at 5V



SOP-14U

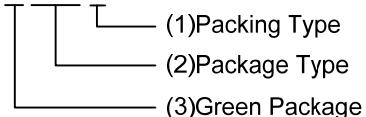


TSSOP-14U

■ ORDERING INFORMATION

Ordering Number		Package	Packing
Lead Free	Halogen Free		
U74AHC21L-UEA-R	U74AHC21G-UEA-R	SOP-14U	Tape Reel
U74AHC21L-UEB-R	U74AHC21G-UEB-R	TSSOP-14U	Tape Reel

U74AHC21G-UEA-R



(1)Packing Type

(2)Package Type

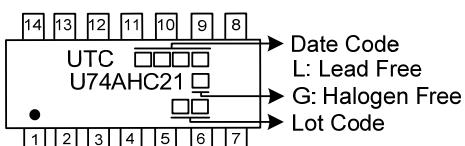
(3)Green Package

(1) R: Tape Reel

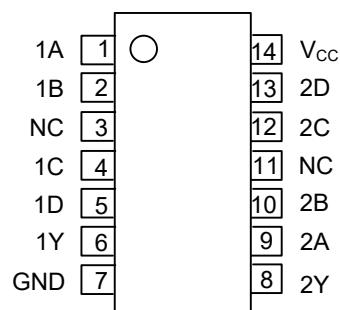
(2) UEA: SOP-14U, UEB: TSSOP-14U

(3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING



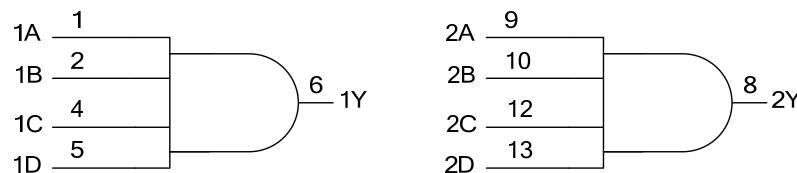
■ PIN CONFIGURATION



■ FUNCTION TABLE (each gate)

INPUT(A)	INPUT(B)	INPUT(C)	INPUT(D)	OUTPUT(Y)
H	H	H	H	H
L	X	X	X	L
X	L	X	X	L
X	X	L	X	L
X	X	X	L	L

■ LOGIC DIAGRAM (positive logic)



■ ABSOLUTE MAXIMUM RATING ($T_A=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V_{CC}	-0.5 ~ 7	V
Input Clamp Current ($V_I < 0$)	I_{IK}	-20	mA
Output Clamp Current ($V_O < 0$ or $V_O > V_{CC}$)	I_{OK}	± 20	mA
Output Current	I_{OUT}	± 25	mA
V_{CC} or GND Current	I_{CC}	± 50	mA
Storage Temperature	T_{STG}	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ RECOMMENDED OPERATING CONDITIONS ($T_A=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	V_{CC}		2		5.5	V
Input Voltage	V_{IN}		0		V_{CC}	V
Output Voltage	V_{OUT}		0		V_{CC}	V
Input Transition Rise or Fall Rate $\Delta t/\Delta V$	$\Delta t/\Delta V$	$V_{CC}=3V \pm 0.3V$			100	ns
		$V_{CC}=5V \pm 0.5V$			20	ns
Operating Temperature	T_A		-40		+125	°C

■ STATIC CHARACTERISTICS ($T_A=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
High-Level Input Voltage	V_{IH}	$V_{CC}=2V$	1.5			V
		$V_{CC}=3V$	2.1			V
		$V_{CC}=5.5V$	3.85			V
Low-Level Input Voltage	V_{IL}	$V_{CC}=2V$			0.5	V
		$V_{CC}=3V$			0.9	V
		$V_{CC}=5.5V$			1.65	V
High-Level Output Voltage	V_{OH}	$V_{CC}=2V, I_{OH}=50\mu A$	1.9			V
		$V_{CC}=3V, I_{OH}=50\mu A$	2.9			V
		$V_{CC}=4.5V, I_{OH}=50\mu A$	4.4			V
		$V_{CC}=3V, I_{OH}=4mA$	2.58			V
		$V_{CC}=4.5V, I_{OH}=8mA$	3.94			V
Low-Level Output Voltage	V_{OL}	$V_{CC}=2V, I_{OL}=50\mu A$			0.1	V
		$V_{CC}=3V, I_{OL}=50\mu A$			0.1	V
		$V_{CC}=4.5V, I_{OL}=50\mu A$			0.1	V
		$V_{CC}=3V, I_{OL}=4mA$			0.36	V
		$V_{CC}=4.5V, I_{OL}=8mA$			0.36	V
Input Leakage Current	$I_{(LEAK)}$	$V_{CC}=6V, V_{IN}=V_{CC}$ or GND			± 0.1	uA
Quiescent Supply Current	I_{CC}	$V_{CC}=5.5V, V_{IN}=V_{CC}$ or GND, $I_{OUT}=0$			2	μA
Input Capacitance	C_{IN}	$V_{CC}=5V$			4	pF

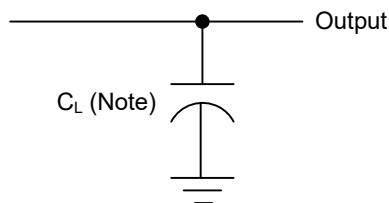
■ DYNAMIC CHARACTERISTICS (Input: $t_R=t_F=3\text{ ns}$, $T_A=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT	
Propagation delay from Input (A, B, C, D) to output (Y)	t_{PLH}	$V_{CC}=3.3 \pm 0.3V$	$C_L=15\text{ pF}$		6.2	8.8	ns
			$C_L=50\text{ pF}$		8.7	12.3	ns
	t_{PHL}	$V_{CC}=5 \pm 0.5V$	$C_L=15\text{ pF}$		4.5	5.9	ns
			$C_L=50\text{ pF}$		5.8	7.9	ns

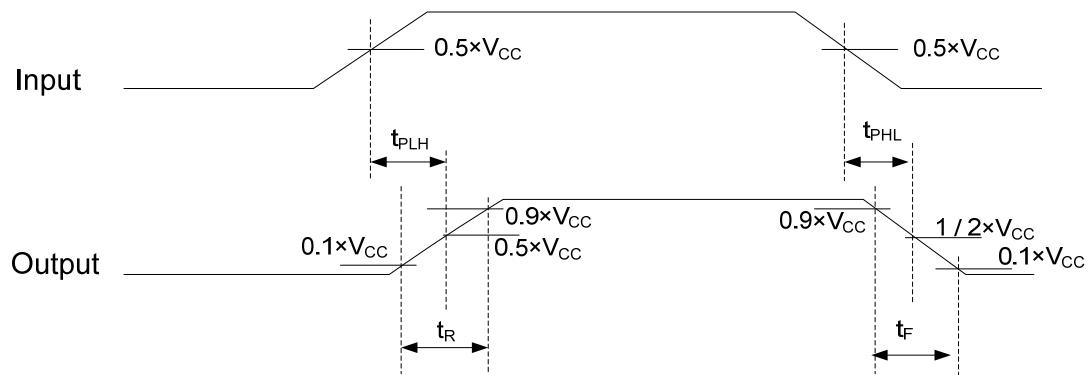
■ OPERATING CHARACTERISTICS ($T_A=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Power Dissipation Capacitance	C_{PD}	No load		18		pF

■ TEST CIRCUIT AND WAVEFORMS



Note : C_L includes probe and jig capacitance.



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