DTC144V

Advance

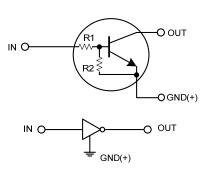
## NPN SILICON TRANSISTOR

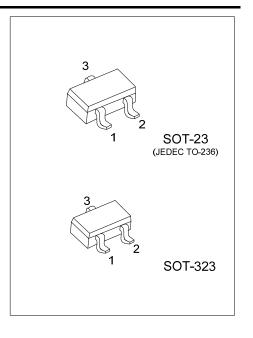
# NPN DIGITAL TRANSISTOR (BUILT- IN BIAS RESISTORS)

#### **■ FEATURES**

- \* Built-in bias resistors that implies easy ON/OFF applications.
- \* The bias resistors are thin-film resistors with complete isolation to allow negative input.

#### **■ EQUIVALENT CIRCUIT**

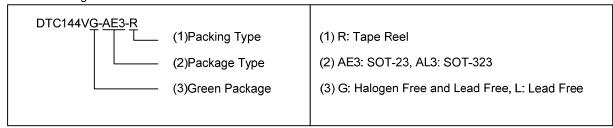




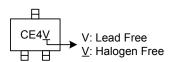
#### **■ ORDERING INFORMATION**

Ordering Number		Dookogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
DTC144VL-AE3-R	DTC144VG-AE3-R	SOT-23	В	E	С	Tape Reel	
DTC144VL-AL3-R	DTC144VG-AL3-R	SOT-323	В	E	С	Tape Reel	

Note: Pin Assignment: B: Base E: Emitter C: Collector



#### MARKING



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## ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub>= 25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT	
Supply Voltage	$V_{CC}$	50	V	
Input Voltage	V <sub>IN</sub>	-10 ~ +40	V	
Output Current	I <sub>OUT</sub>	100	mA	
Output Current	I <sub>OUT(MAX)</sub>	100	mA	
Power Dissipation	$P_D$	200	mW	
Junction Temperature	TJ	+150	°C	
Storage Temperature	T <sub>STG</sub>	-55 ~ <b>+</b> 150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

### ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub>= 25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Voltage	$V_{IN(OFF)}$	V <sub>CC</sub> = 5V, I <sub>OUT</sub> =100μA			0.5	V
	$V_{IN(ON)}$	V <sub>OUT</sub> = 0.3V, I <sub>OUT</sub> = 20mA	3			V
Output Voltage	$V_{OUT(ON)}$	I <sub>OUT</sub> /I <sub>IN</sub> = 10mA / 0.5mA		0.1	0.3	V
Input Current	I <sub>IN</sub>	V <sub>IN</sub> = 5V			0.18	mA
Output Current	I <sub>OUT(OFF)</sub>	V <sub>CC</sub> = 50V , V <sub>IN</sub> =0V			0.5	μΑ
DC Current Gain	$h_{FE}$	V <sub>OUT</sub> = 5V, I <sub>OUT</sub> = 5mA	33			
Input Resistance	R1		32.9	47	61.1	kΩ
Resistance Ratio	R2			10		kΩ
Transition Frequency	$f_T$	$V_{CE}$ = 10V, $I_{E}$ = -5mA, f=100MHz		250		MHz

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