

5NM70-U2

Power MOSFET

5.4A, 700V N-CHANNEL SUPER-JUNCTION MOSFET

■ DESCRIPTION

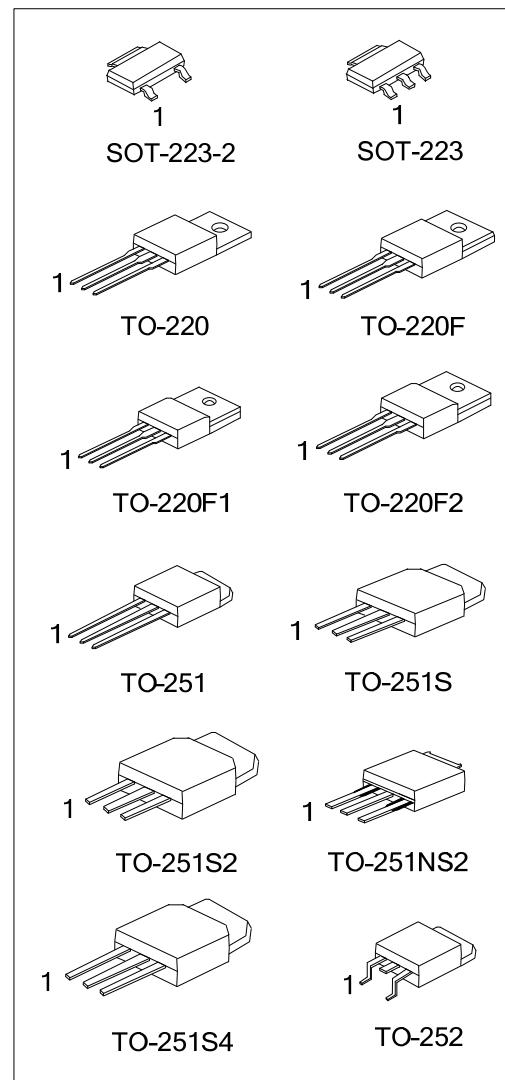
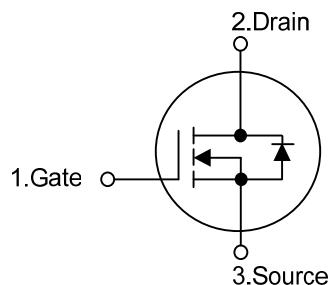
The UTC **5NM70-U2** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics.

This power MOSFET is usually used in high speed switching applications at power supplies, PWM motor controls, high efficient AC to DC converters and bridge circuits.

■ FEATURES

- * $R_{DS(ON)} \leq 1.5 \Omega$ @ $V_{GS} = 10V$, $I_D = 2.5A$
- * Fast Switching Capability
- * Improved dv/dt Capability, High Ruggedness

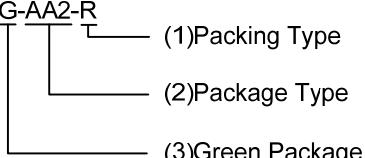
■ SYMBOL



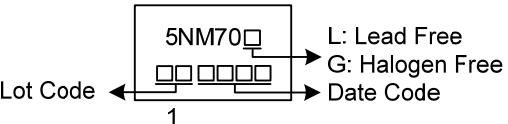
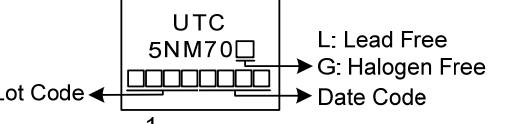
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
5NM70L-AA2-R	5NM70G-AA2-R	SOT-223-2	G	D	S	Tape Reel
5NM70L-AA3-R	5NM70G-AA3-R	SOT-223	G	D	S	Tape Reel
5NM70L-TA3-T	5NM70G-TA3-T	TO-220	G	D	S	Tube
5NM70L-TF1-T	5NM70G-TF1-T	TO-220F1	G	D	S	Tube
5NM70L-TF2-T	5NM70G-TF2-T	TO-220F2	G	D	S	Tube
5NM70L-TF3-T	5NM70G-TF3-T	TO-220F	G	D	S	Tube
5NM70L-TM3-T	5NM70G-TM3-T	TO-251	G	D	S	Tube
5NM70L-TMS-T	5NM70G-TMS-T	TO-251S	G	D	S	Tube
5NM70L-TMS2-T	5NM70G-TMS2-T	TO-251S2	G	D	S	Tube
5NM70L-TMS4-T	5NM70G-TMS4-T	TO-251S4	G	D	S	Tube
5NM70L-TMN2-T	5NM70G-TMN2-T	TO-251NS2	G	D	S	Tube
5NM70L-TN3-R	5NM70G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

 5NM70G-AA2-R	(1) T: Tube, R: Tape Reel (2) AA2: SOT-223-2, AA3: SOT-223, TA3: TO-220, TF1: TO-220F1, TF2: TO-220F2, TF3: TO-220F, TM3: TO-251, TMS: TO-251S, TMS2: TO-251S2, TMS4: TO-251S4, TN3: TO-252, TMN2: TO-251NS2 (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING

SOT-223-2 / SOT-223	TO-220 / TO-220F / TO-220F1 / TO-220F2 TO-251 / TO-251S / TO-251S2 / TO-251S4 TO-252 / TO-251NS2
 Lot Code ← 1 → Date Code L: Lead Free G: Halogen Free	 Lot Code ← 1 → Date Code L: Lead Free G: Halogen Free

■ ABSOLUTE MAXIMUM RATINGS ($T_c = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	700	V
Gate-Source Voltage		V_{GSS}	± 30	V
Drain Current	Continuous	$T_c=25^\circ\text{C}$	I_D	5.4
		$T_c=100^\circ\text{C}$		3.4
	Pulsed (Note 2)	I_{DM}	20	A
Avalanche Current (Note 2)		I_{AR}	1.2	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	104	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.0	V/ns
Power Dissipation	SOT-223-2/SOT-223	P_D	5	W
	TO-220		108	W
	TO-220F/TO-220F1		36	W
	TO-220F2			
	TO-251/TO-251S			
	TO-251S2/TO-251S4		54	W
Junction Temperature	TO-251NS2/TO-252			
Junction Temperature	T_J		+150	$^\circ\text{C}$
Storage Temperature	T_{STG}		-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature.

3. $L=144\text{mH}$, $I_{AS}=1.2\text{A}$, $V_{DD}=50\text{V}$, $R_G=25\Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 5.0\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	SOT-223-2/SOT-223	θ_{JA}	160	$^\circ\text{C/W}$
	TO-220F/TO-220F		62.5	$^\circ\text{C/W}$
	TO-220F1/TO-220F2			
	TO-251/TO-251S		110	$^\circ\text{C/W}$
	TO-251S2/TO-251S4			
Junction to Case	TO-251NS2/TO-252	θ_{JC}		
	SOT-223-2/SOT-223		25	$^\circ\text{C/W}$
	TO-220		1.16	$^\circ\text{C/W}$
	TO-220F/TO-220F1		3.47	$^\circ\text{C/W}$
	TO-220F2			
	TO-251/TO-251S			
	TO-251S2/TO-251S4		2.3	$^\circ\text{C/W}$
	TO-251NS2/TO-252			

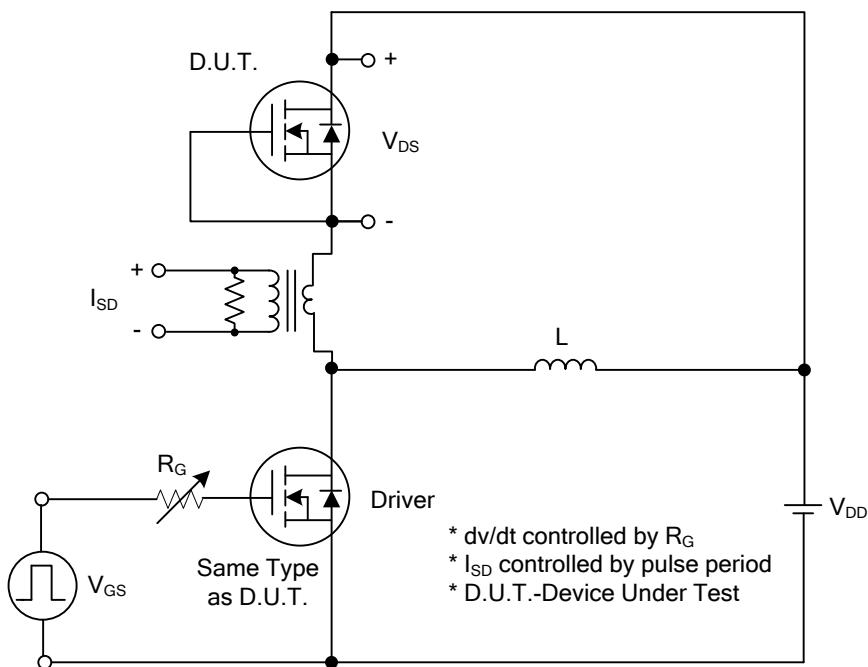
■ ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	700			V
Drain-Source Leakage Current	I_{DSS}	$\text{V}_{\text{DS}}=700\text{V}, \text{V}_{\text{GS}}=0\text{V}$		1		μA
Gate-Source Leakage Current	Forward	$\text{V}_{\text{GS}}=30\text{V}, \text{V}_{\text{DS}}=0\text{V}$		100		nA
	Reverse	$\text{V}_{\text{GS}}=-30\text{V}, \text{V}_{\text{DS}}=0\text{V}$		-100		
ON CHARACTERISTICS						
Gate Threshold Voltage	$\text{V}_{\text{GS(TH)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	2.5		4.5	V
Static Drain-Source On-State Resistance	$\text{R}_{\text{DS(ON)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=2.5\text{A}$			1.5	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{ISS}	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=25\text{V}, f=1.0\text{MHz}$		280		pF
Output Capacitance	C_{OSS}			135		pF
Reverse Transfer Capacitance	C_{RSS}			18		pF
Gate Resistance	R_G	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=0\text{V}, f=1.0\text{MHz}$		2.3		Ω
SWITCHING CHARACTERISTICS						
Total Gate Charge (Note 1)	Q_G	$\text{V}_{\text{DS}}=50\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{I}_D=1.3\text{A}$, (Note 1, 2)		15		nC
Gate to Source Charge	Q_{GS}			5		nC
Gate to Drain Charge	Q_{GD}			4		nC
Turn-ON Delay Time (Note 1)	$t_{\text{D(ON)}}$	$\text{V}_{\text{DD}}=30\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{I}_D=0.5\text{A}$, $\text{R}_G=25\Omega$ (Note 1, 2)		5		ns
Rise Time	t_R			10		ns
Turn-OFF Delay Time	$t_{\text{D(OFF)}}$			50		ns
Fall-Time	t_F			60		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				5	A
Maximum Body-Diode Pulsed Current	I_{SM}				20	A
Drain-Source Diode Forward Voltage (Note 1)	V_{SD}	$\text{I}_S=5.0\text{A}, \text{V}_{\text{GS}}=0\text{V}$			1.4	V
Body Diode Reverse Recovery Time (Note 1)	t_{rr}	$\text{I}_S=5.0\text{A}, \text{V}_{\text{GS}}=0\text{V}$, $d\text{I}_F/dt=100\text{A}/\mu\text{s}$		290		ns
Body Diode Reverse Recovery Charge	Q_{rr}			2.5		μC

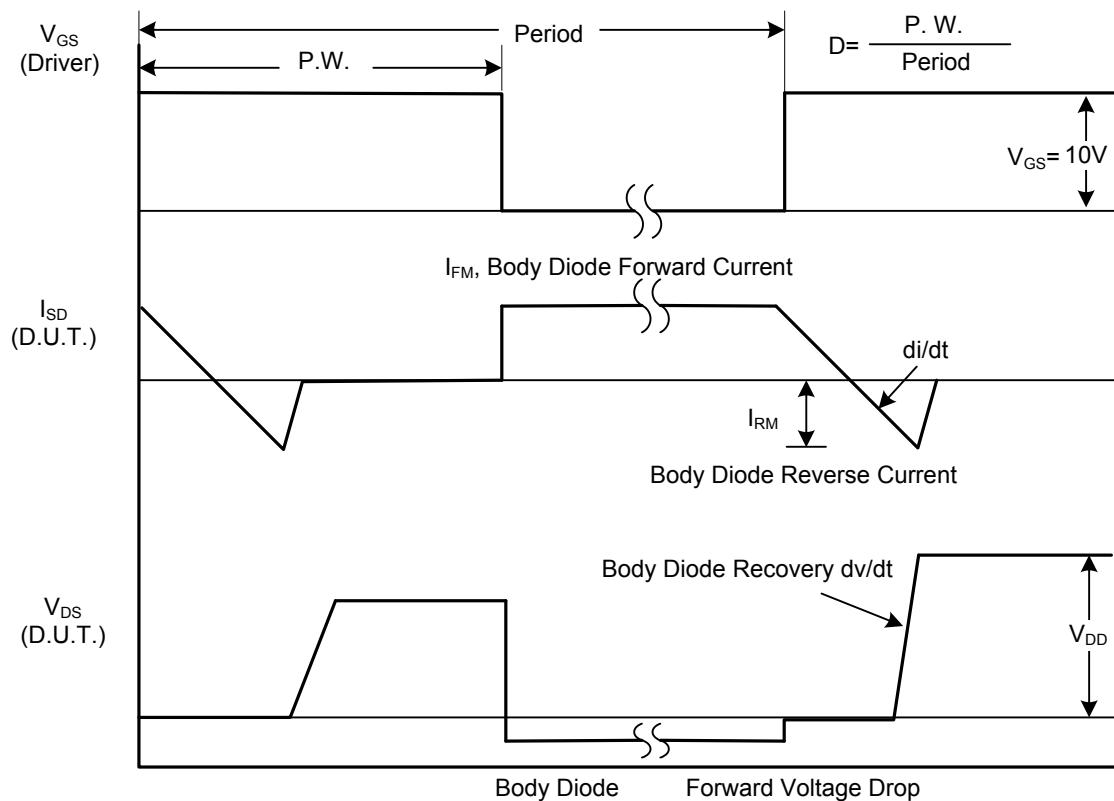
Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

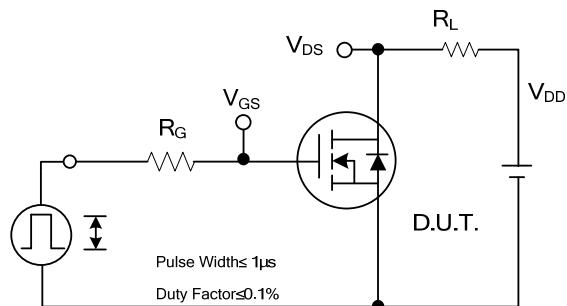


Peak Diode Recovery dv/dt Test Circuit

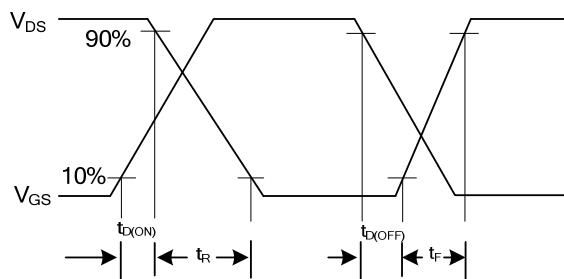


Peak Diode Recovery dv/dt Waveforms

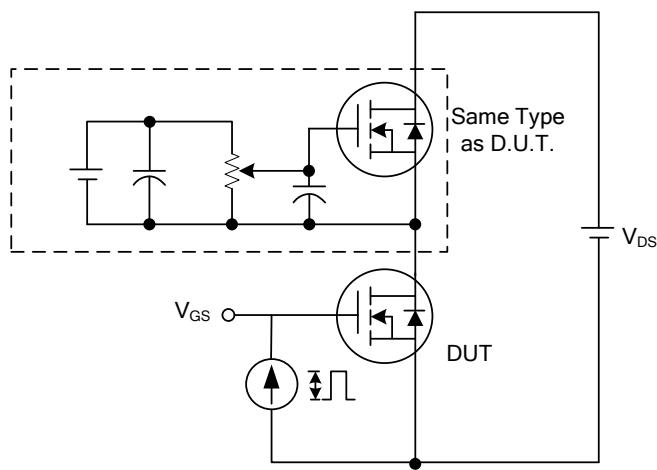
■ TEST CIRCUITS AND WAVEFORMS



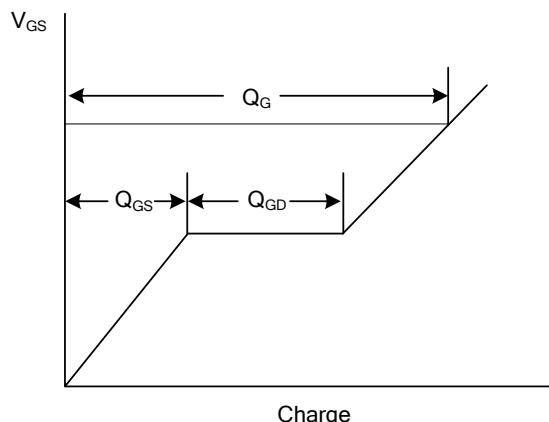
Switching Test Circuit



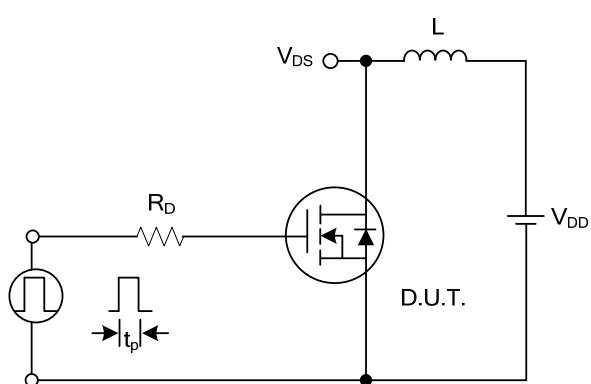
Switching Waveforms



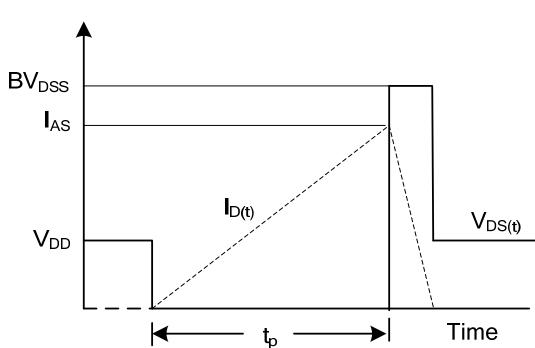
Gate Charge Test Circuit



Gate Charge Waveform

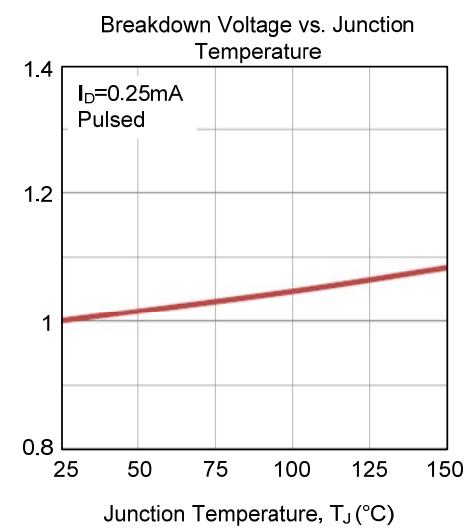
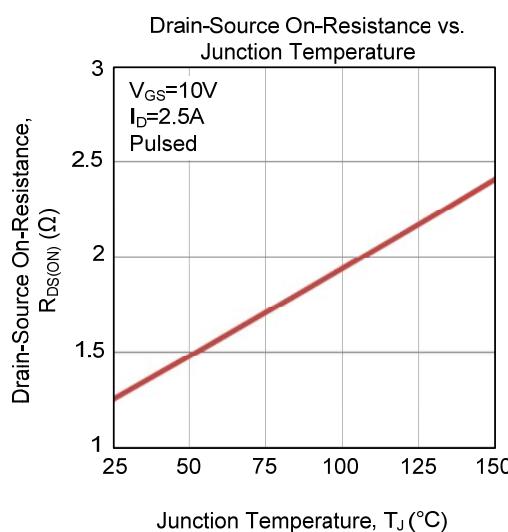
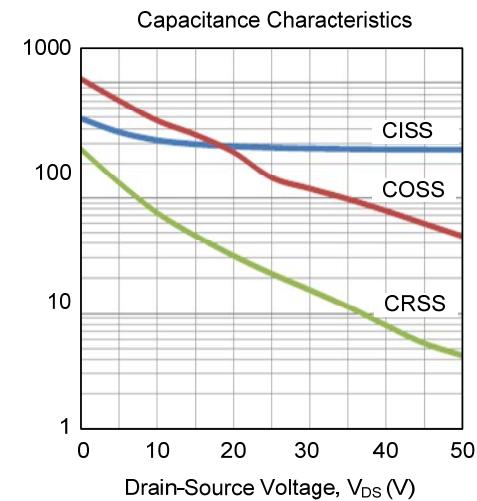
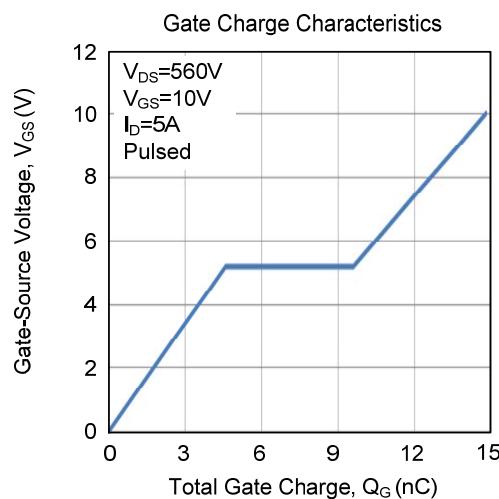
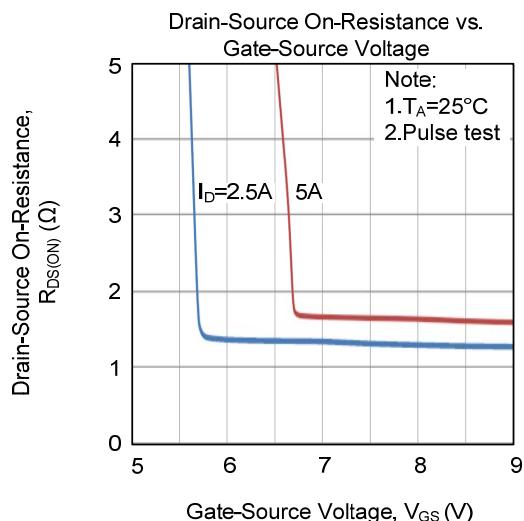
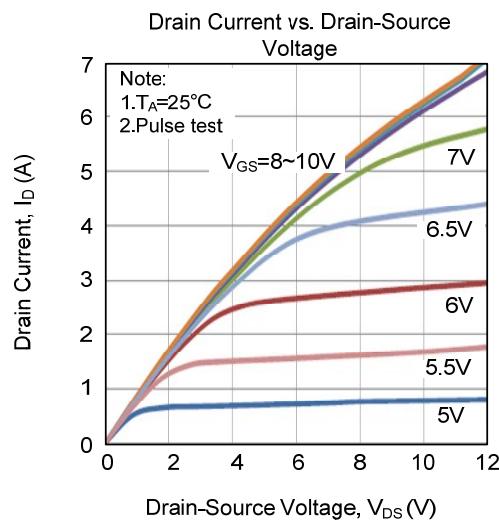


Unclamped Inductive Switching Test Circuit

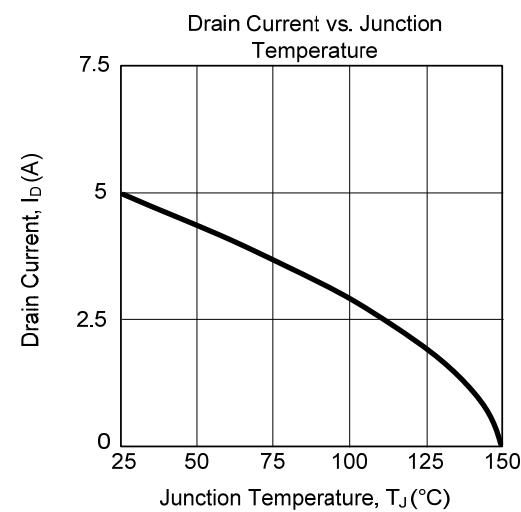
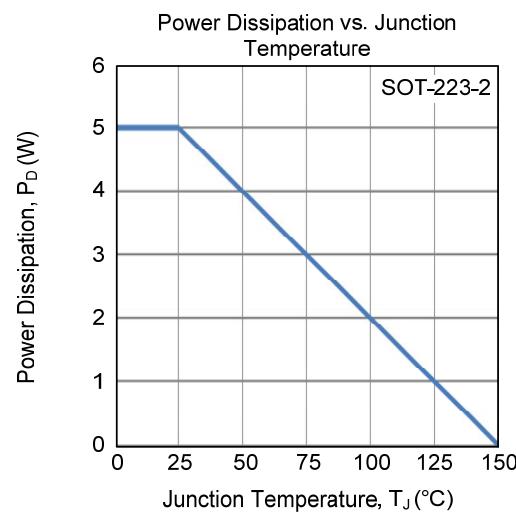
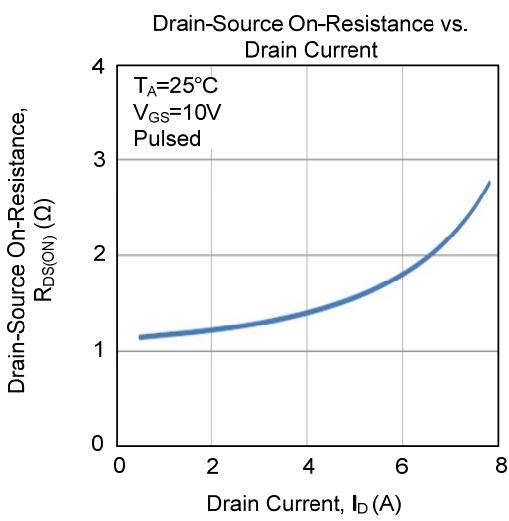
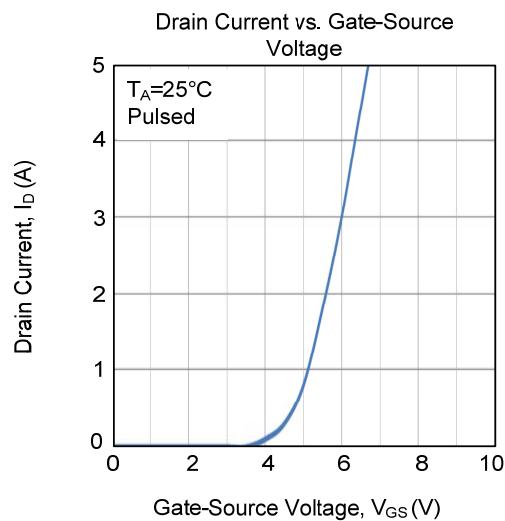
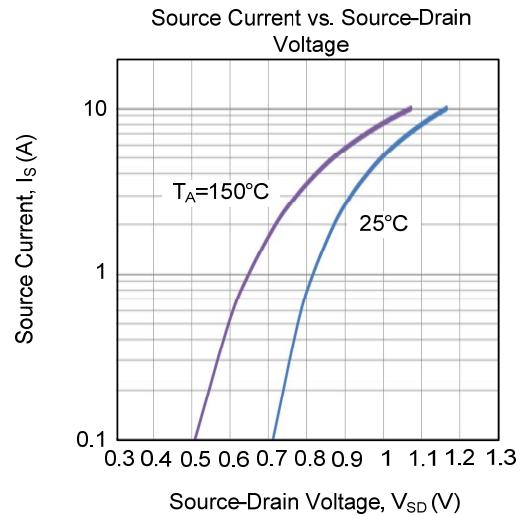
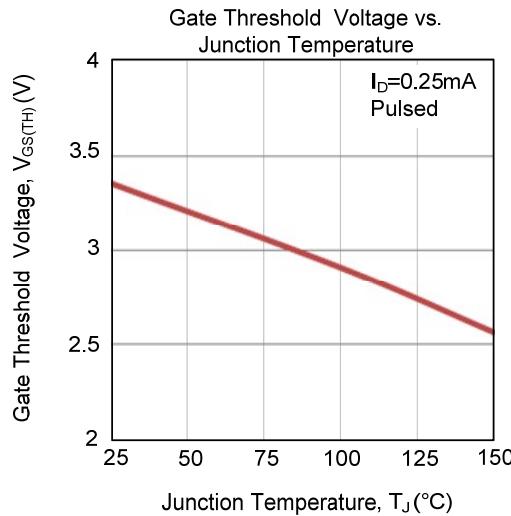


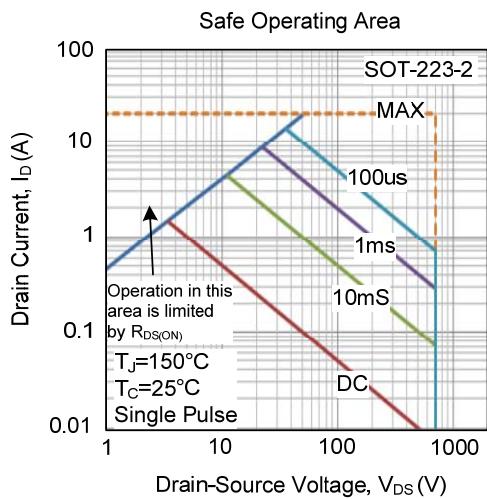
Unclamped Inductive Switching Waveforms

■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)

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