

MGBR15V50

Preliminary

DIODE

MOS GATED BARRIER RECTIFIER

DESCRIPTION

The UTC MGBR15V50 is a surface mount mos gated barrier rectifier, it uses UTC's advanced technology to provide customers with low forward voltage drop and high current capability, etc.

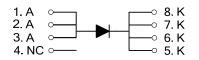
The UTC MGBR15V50 suitable for free wheeling, high frequency inverters, polarity protection, and low voltage.

FEATURES

* Very low forward voltage drop

- * High current capability
- * High surge capability
- * High efficiency

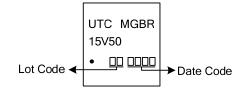
SYMBOL

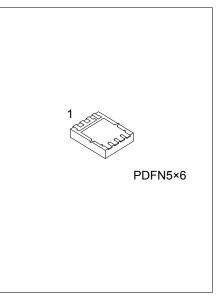


	Ordering Number		Dookogo	Pin Assignment							Decking		
	Lead Free	Halogen Free	Package	1	2	3	4	5	6	7	8	Packing	
	MGBR15V50L-P5060-R MGBR15V50G-P5060-R		PDFN5×6	А	А	Α	NC	Κ	Κ	K	K	Tape Reel	
Note: Pin Assignment: A: Anode K: Common Cathode NC: No Comment													

(2) P5060: PDFN5×6 (3) Green Package (3) G: Halogen Free and Lead Free, L: Lead Free

MARKING





Preliminary

■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

PARAMETER	SYMBOL	RATINGS	UNIT
DC Blocking Voltage	V _{RM}	50	V
Working Peak Reverse Voltage	V _{RWM}	50	V
Peak Repetitive Reverse Voltage	V _{RRM}	50	V
Average Rectified Output Current T _C =125°C	Ι _Ο	15	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	200	А
Operating Junction Temperature	ΤJ	-65 ~ +150	°C
Storage Temperature	T _{STG}	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS (PER LEG)

PARAMETER	SYMBOL	RATINGS	UNIT
Typical Thermal Resistance	θ _{JA}	72	°C/W
	- 0/1		-

Note: FR-4 PCB, 2 oz Copper. Minimum recommended pad layout.

■ **ELECTRICAL CHARACTERISTICS** (T_A =25°C unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage	V _{(BR)R}	I _R =0.50mA	50			V
		I _F =15A, TJ=25°C			0.55	V
Forward Voltage Drop		I _F =15A, TJ=125°C			0.50	V
Peak Reverse Current at Rated DC		V _R =50V, T _J =25°C			500	μA
Blocking Voltage	IRM	V _R =50V, T _J =125°C			50	mA

Note: Pulse Test: Pulse width \leq 300µs, Duty cycle \leq 2%.



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