



5N65-CQ

Preliminary

Power MOSFET

5A, 650V N-CHANNEL POWER MOSFET

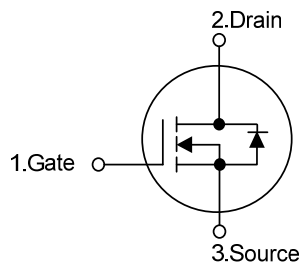
DESCRIPTION

The UTC **5N65-CQ** is a high voltage power MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications of switching power supplies and adaptors.

FEATURES

- * $R_{DS(ON)} < 2.2 \Omega$ @ $V_{GS} = 10V$, $I_D = 2.5A$
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness

SYMBOL



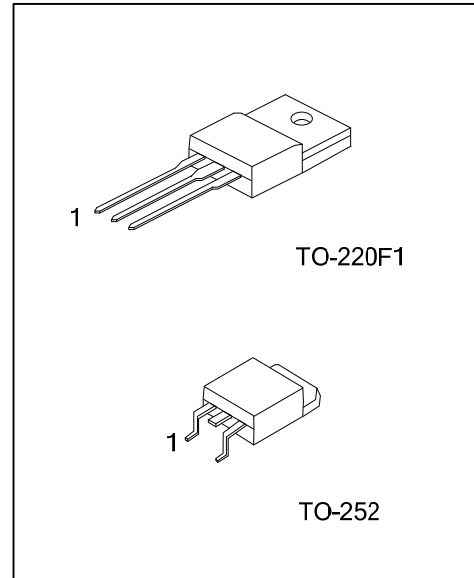
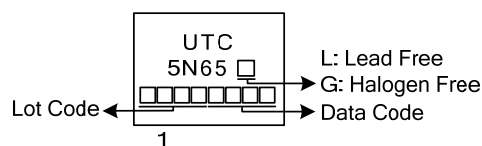
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
5N65L-TF1-T	5N65G-TF1-T	TO-220F1	G	D	S	Tube
5N65L-TN3-R	5N65G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

	(1)Packing Type	(1) T: Tube, R: Tape Reel
	(2)Package Type	(2) TF1: TO-220F1, TM3: TO-251
	(3)Green Package	(3) G: Halogen Free and Lead Free, L: Lead Free

MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	650	V
Gate-Source Voltage		V_{GSS}	± 30	V
Continuous Drain Current		I_D	5	A
Pulsed Drain Current (Note 2)		I_{DM}	20	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	64	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	3.4	V/ns
Power Dissipation	TO-220F1	P_D	36	W
	TO-252		54	W
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 16\text{mH}$, $I_{AS} = 2.82\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\ \Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 5.0\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220F1	θ_{JA}	62.5	$^\circ\text{C/W}$
	TO-252		160	
Junction to Case	TO-220F1	θ_{JC}	3.47	$^\circ\text{C/W}$
	TO-252		2.3	

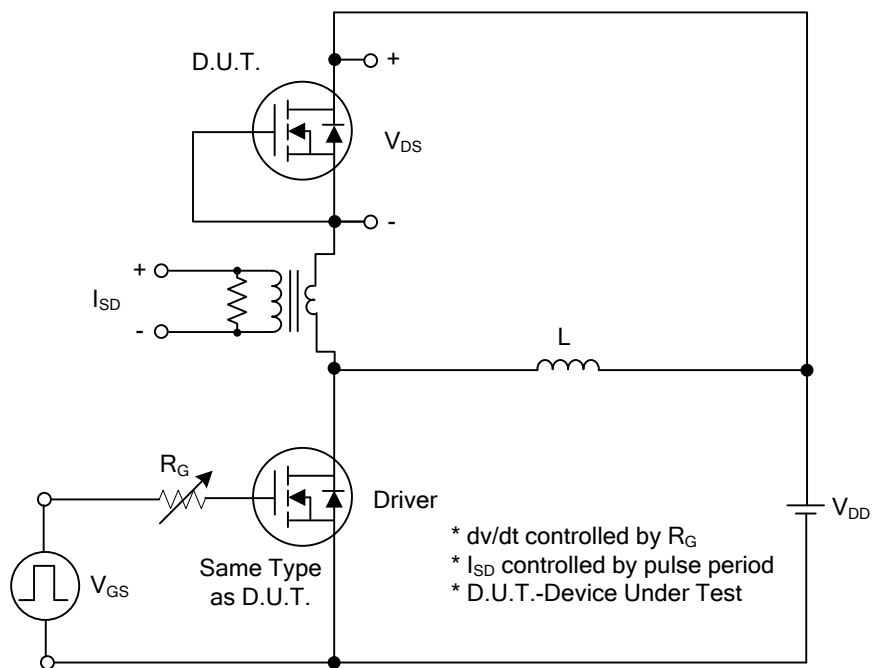
■ ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} = 0V, I _D = 250μA	650			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} = 650V, V _{GS} = 0V			10	μA
Gate- Source Leakage Current	Forward	I _{GSS}	V _{GS} = 30V, V _{DS} = 0V			100	nA
	Reverse		V _{GS} = -30V, V _{DS} = 0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} = V _{GS} , I _D = 250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} = 10V, I _D = 2.5A			2.2	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		C _{ISS}	V _{DS} =25V, V _{GS} =0V, f=1.0 MHz		528		pF
Output Capacitance		C _{OSS}			70		pF
Reverse Transfer Capacitance		C _{RSS}			9		pF
SWITCHING CHARACTERISTICS							
Total Gate Charge		Q _G	V _{DS} =50V, V _{GS} =10V, I _D =1.3A, I _D =100μA (Note 1, 2)		46		nC
Gate-Source Charge		Q _{GS}			4.2		nC
Gate-Drain Charge		Q _{GD}			7		nC
Turn-On Delay Time		t _{D(ON)}	V _{DD} =30V, V _{GS} =10V, I _D =0.5A, R _G =25Ω (Note 1, 2)		46		ns
Turn-On Rise Time		t _R			50		ns
Turn-Off Delay Time		t _{D(OFF)}			160		ns
Turn-Off Fall Time		t _F			46		ns
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS							
Maximum Continuous Drain-Source Diode Forward Current		I _S				5	A
Maximum Pulsed Drain-Source Diode Forward Current		I _{SM}				20	A
Drain-Source Diode Forward Voltage		V _{SD}	I _S =5.0A , V _{GS} =0V			1.4	V
Body Diode Reverse Recovery Time		t _{rr}	I _S =5.0A , V _{GS} =0V		264		ns
Body Diode Reverse Recovery Charge		Q _{rr}	di/dt=100A/μs		1.62		μC

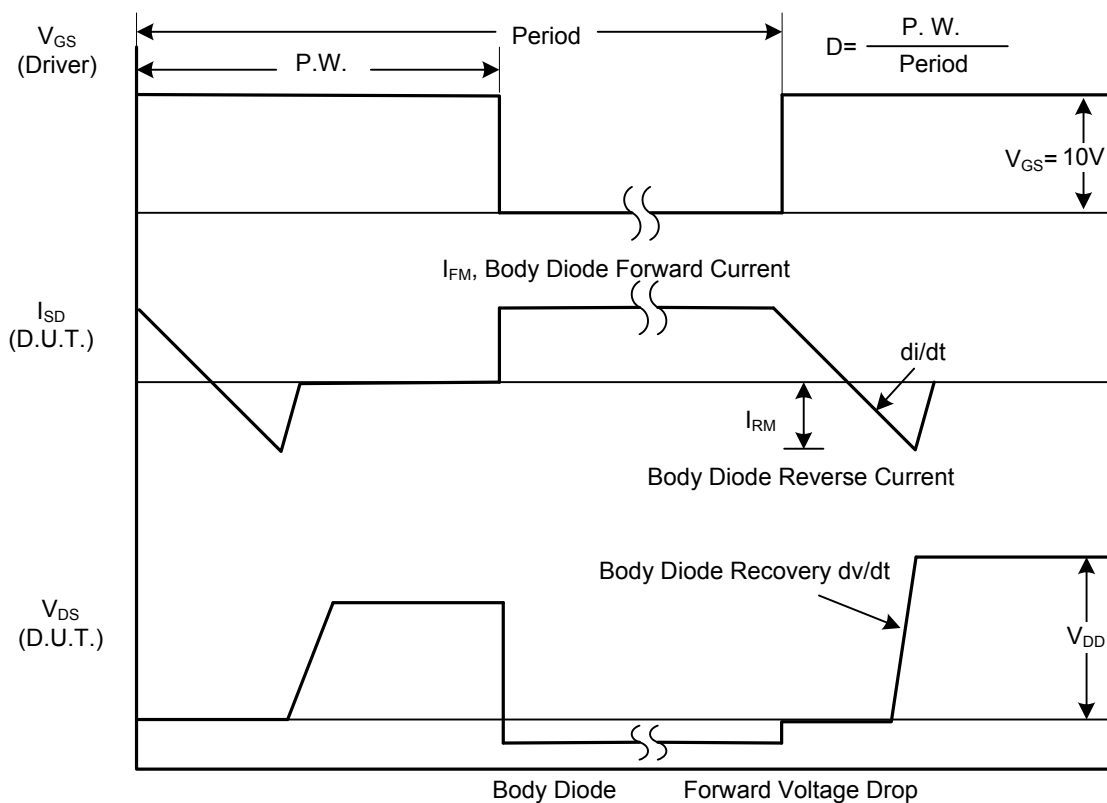
Notes: 1. Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$

2. Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS

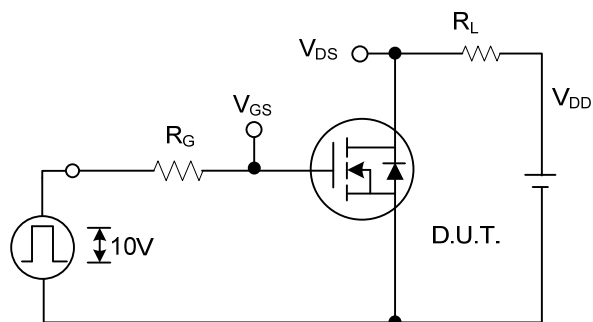


Peak Diode Recovery dv/dt Test Circuit

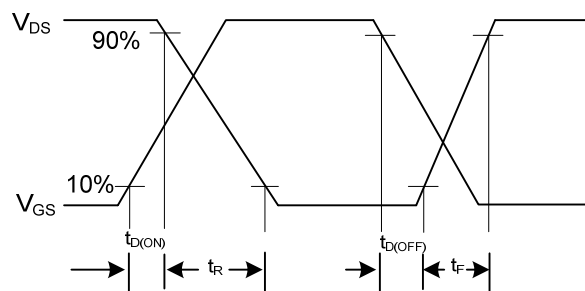


Peak Diode Recovery dv/dt Waveforms

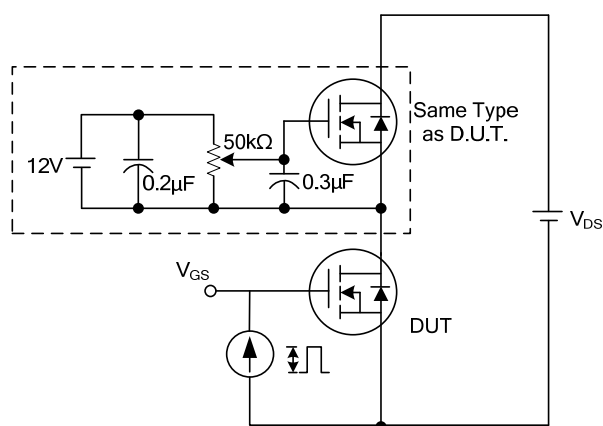
■ TEST CIRCUITS AND WAVEFORMS (Cont.)



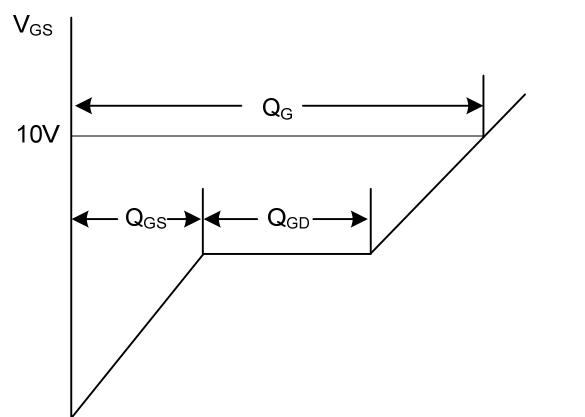
Switching Test Circuit



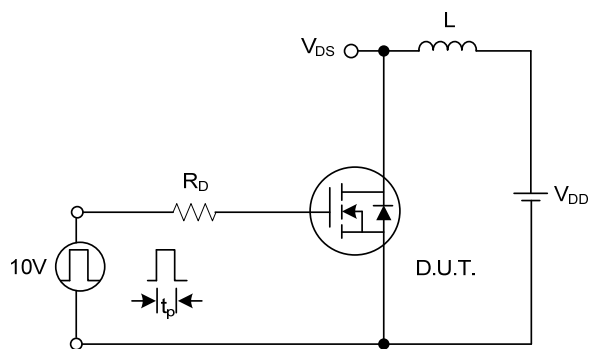
Switching Waveforms



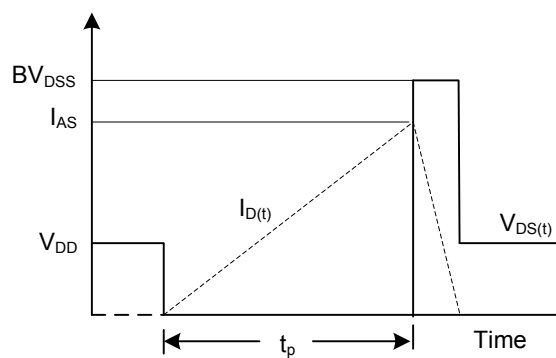
Gate Charge Test Circuit



Charge
Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

UTC assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all UTC products described or contained herein. UTC products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice.