

UTT80N10H

Power MOSFET

80A, 100V N-CHANNEL
POWER MOSFET

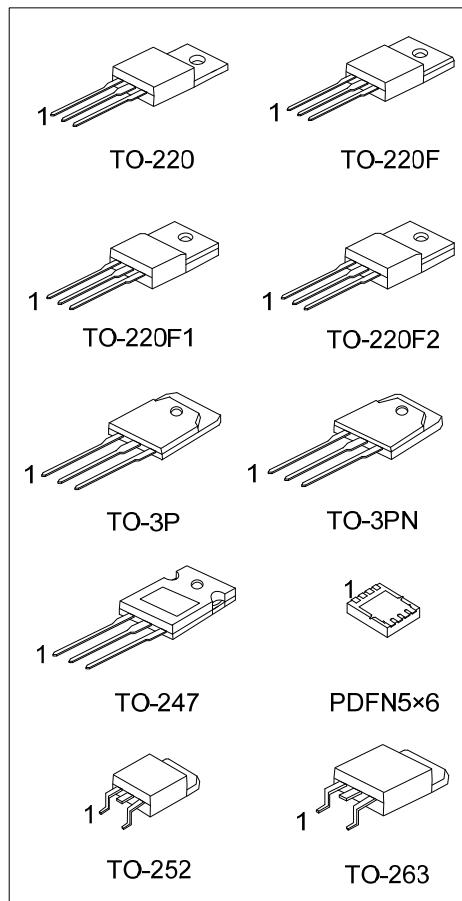
■ DESCRIPTION

The UTC **UTT80N10H** is a N-channel Power MOSFET, it uses UTC's advanced technology to provide the customers with high switching speed and low gate charge, etc.

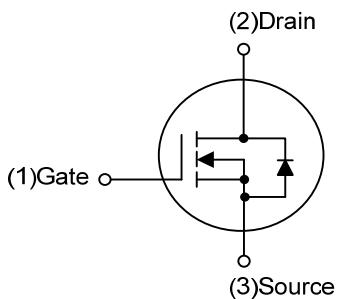
The UTC **UTT80N10H** applies to primary side switch, synchronous rectifier, Motor Drives, etc.

■ FEATURES

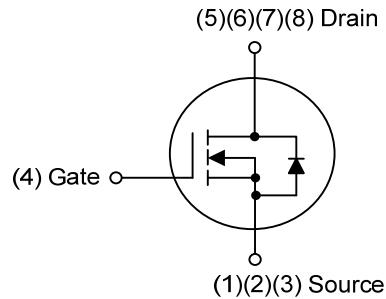
- * $R_{DS(ON)} \leq 14 \text{ m}\Omega$ @ $V_{GS}=10\text{V}$, $I_D=40\text{A}$
- * High Cell Density Trench Technology
- * High Power and Current Handling Capability



■ SYMBOL



TO-220/TO-220F/TO-220F1/TO-220F2
TO-3P/TO-3PN/TO-247/TO-252/TO-263

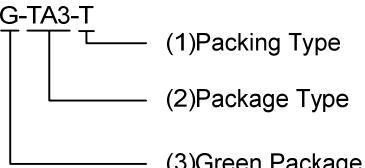


PDFN5x6

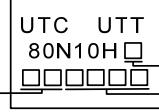
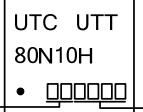
■ ORDERING INFORMATION

Ordering Number	Lead Free	Halogen Free	Package	Pin Assignment								Packing
				1	2	3	4	5	6	7	8	
UTT80N10HL-TA3-T	UTT80N10HG-TA3-T	TO-220	G D S - - - -	-	-	-	-	-	-	-	-	Tube
UTT80N10HL-TF1-T	UTT80N10HG-TF1-T	TO-220F1	G D S - - - -	-	-	-	-	-	-	-	-	Tube
UTT80N10HL-TF2-T	UTT80N10HG-TF2-T	TO-220F2	G D S - - - -	-	-	-	-	-	-	-	-	Tube
UTT80N10HL-TF3-T	UTT80N10HG-TF3-T	TO-220F	G D S - - - -	-	-	-	-	-	-	-	-	Tube
UTT80N10HL-T47-T	UTT80N10HG-T47-T	TO-247	G D S - - - -	-	-	-	-	-	-	-	-	Tube
UTT80N10HL-TN3-R	UTT80N10HG-TN3-R	TO-252	G D S - - - -	-	-	-	-	-	-	-	-	Tape Reel
UTT80N10HL-TQ2-T	UTT80N10HG-TQ2-T	TO-263	G D S - - - -	-	-	-	-	-	-	-	-	Tube
UTT80N10HL-TQ2-R	UTT80N10HG-TQ2-R	TO-263	G D S - - - -	-	-	-	-	-	-	-	-	Tape Reel
UTT80N10HL-T3P-T	UTT80N10HG-T3P-T	TO-3P	G D S - - - -	-	-	-	-	-	-	-	-	Tube
UTT80N10HL-T3N-T	UTT80N10HG-T3N-T	TO-3PN	G D S - - - -	-	-	-	-	-	-	-	-	Tube
UTT80N10HL-P5060-R	UTT80N10HG-P5060-R	PDFN5x6	S S S G D D D D	-	-	-	-	-	-	-	-	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

 UTT80N10HG-TA3-T	(1) T: Tube, R: Tape Reel (2) TA3: TO-220, TF1: TO-220F1, TF2: TO-220F2, TF3: TO-220F, T47: TO-247, TN3: TO-252, TQ2: TO-263, T3P: TO-3P, T3N: TO-3PN, P5060: PDFN5x6 (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING

TO-220 / TO-220F / TO-220F1 / TO-220F2 TO-3P / TO-3PN / TO-247 / TO-252 / TO-263	PDFN5x6
 Lot Code ← 1 → Date Code L: Lead Free G: Halogen Free	 Lot Code ← → Date Code

■ ABSOLUTE MAXIMUM RATINGS ($T_c=25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	100	V
Gate-Source Voltage		V_{GSS}	± 20	V
Drain Current	Continuous	I_D	80	A
	Pulsed (Note 2)	I_{DM}	160	A
Avalanche Current (Note 2)		I_{AR}	20	A
Single Pulsed Avalanche Energy (Note 3)		E_{AS}	22	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	3.6	V/ns
Power Dissipation	TO-220/TO-263	P_D	142	W
	TO-220F		39	W
	TO-220F1		41	W
	TO-220F2		320	W
	TO-3P/TO-3PN		312	W
	TO-247		55	W
	TO-252		39	W
	PDFN5x6		+150	°C
Junction Temperature		T_J	-55 ~ +150	°C
Storage Temperature		T_{STG}		°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.
 Absolute maximum ratings are stress ratings only and functional device operation is not implied.
 2. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.
 3. $L = 0.1\text{mH}$, $I_{AS} = 20\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$
 4. $I_{SD} \leq 30\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, $T_J \leq T_{JMAX}$, $T_J = 25^\circ\text{C}$.

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F	θ_{JA}	62.5	°C/W
	TO-220F1		40	°C/W
	TO-220F2		50	°C/W
	TO-263		110	°C/W
	TO-3P/TO-3PN		65 (Note)	°C/W
	TO-247		0.88	°C/W
	TO-252		3.2	°C/W
	PDFN5x6		3.05	°C/W
Junction to Case	TO-220/TO-263	θ_{JC}	0.39	°C/W
	TO-220F		0.4	°C/W
	TO-220F1		2.27 (Note)	°C/W
	TO-220F2		3.2 (Note)	°C/W
	TO-3P/TO-3PN			
	TO-247			
	TO-252			
	PDFN5x6			

Note: The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

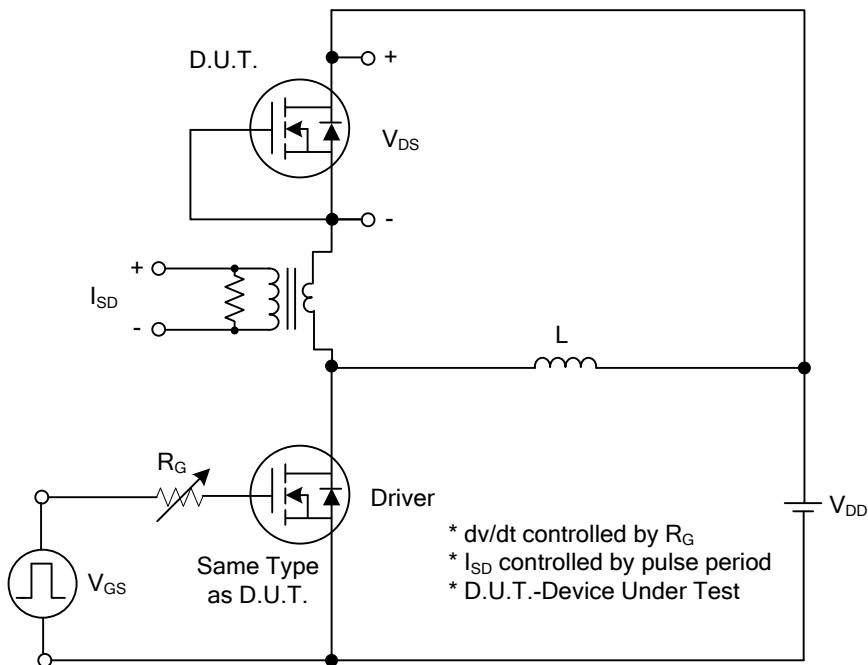
■ ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$	100			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=80\text{V}, V_{GS}=0\text{V}$			1	μA
Gate-Source Leakage Current	Forward	$V_{GS}=+20\text{V}, V_{DS}=0\text{V}$			+100	nA
	Reverse				-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(\text{TH})}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	2.0		4.0	V
Static Drain-Source On-State Resistance	$R_{DS(\text{ON})}$	$V_{GS}=10\text{V}, I_D=40\text{A}$		12	14	$\text{m}\Omega$
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{DS}=25\text{V}, V_{GS}=0\text{V}, f=1.0\text{MHz}$		2750		pF
Output Capacitance	C_{OSS}			285		pF
Reverse Transfer Capacitance	C_{RSS}			230		pF
SWITCHING PARAMETERS						
Total Gate Charge	Q_G	$V_{DS}=80\text{V}, V_{GS}=10\text{V}, I_D=80\text{A}$		80		nC
Gate to Source Charge	Q_{GS}			20		nC
Gate to Drain Charge	Q_{GD}			32		nC
Turn-ON Delay Time	$t_{D(\text{ON})}$	$V_{DD}=50\text{V}, V_{GS}=10\text{V}, I_D=80\text{A}, R_G=6\Omega$		16		ns
Rise Time	t_R			21		ns
Turn-OFF Delay Time	$t_{D(\text{OFF})}$			56		ns
Fall-Time	t_F			34		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				80	A
Maximum Body-Diode Pulsed Current	I_{SM}				160	A
Drain-Source Diode Forward Voltage	V_{SD}	$I_{SD}=80\text{A}$			1.25	V
Body Diode Reverse Recovery Time	t_{rr}	$I_S=30\text{A}, dI/dt=100\text{A}/\mu\text{s}$		134		ns
Body Diode Reverse Recovery Charge	Q_{rr}			247		nC

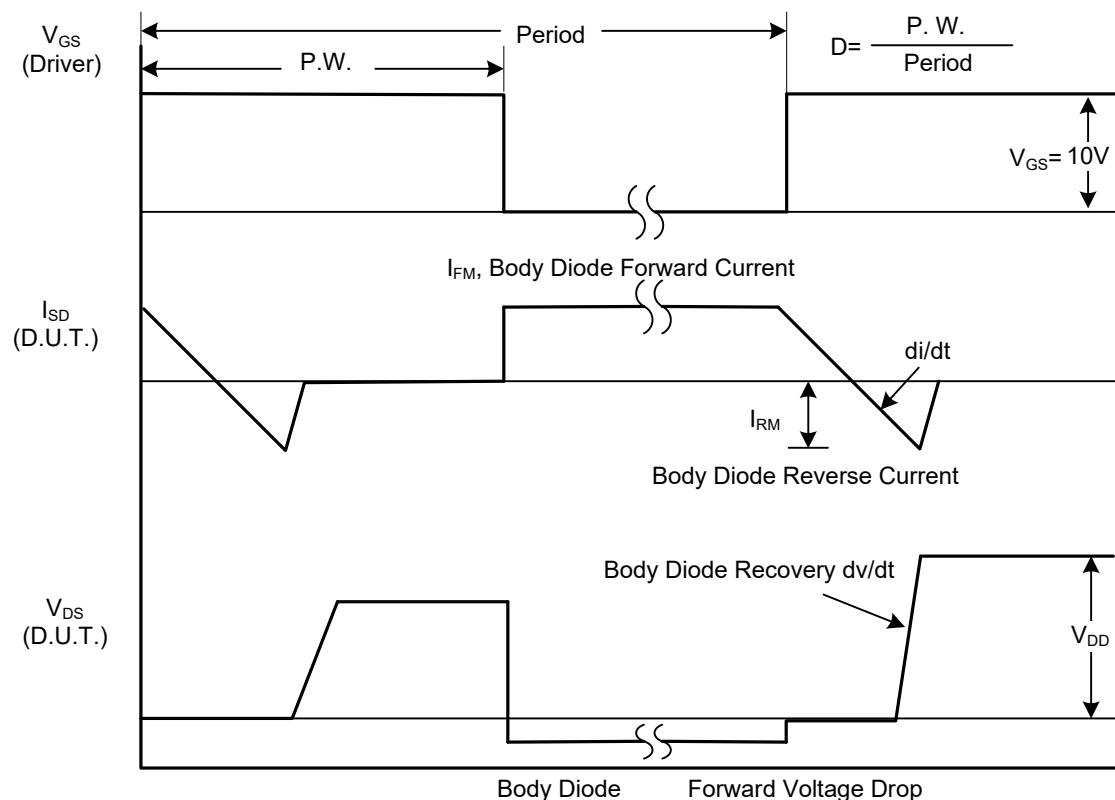
Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating ambient temperature.

■ TEST CIRCUITS AND WAVEFORMS

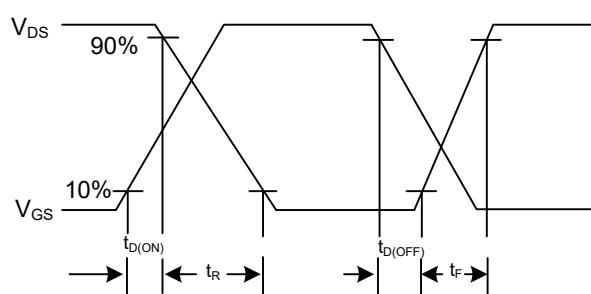
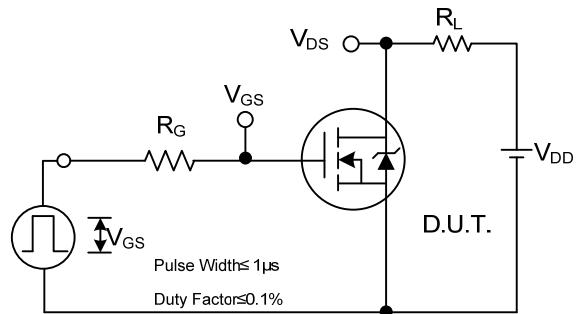


Peak Diode Recovery dv/dt Test Circuit



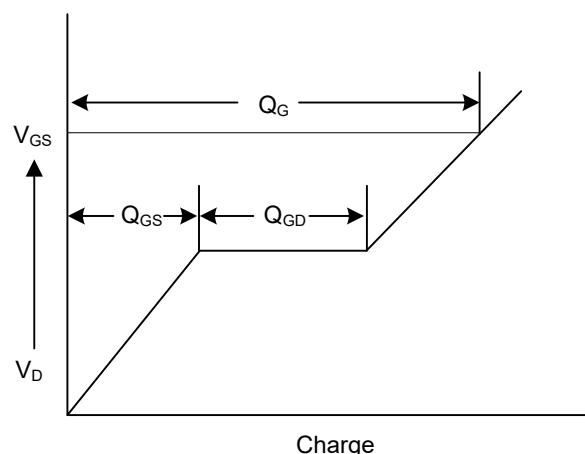
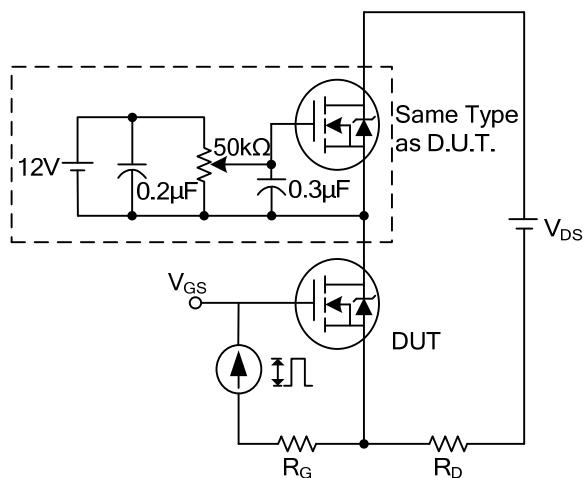
Peak Diode Recovery dv/dt Waveforms

■ TEST CIRCUITS AND WAVEFORMS



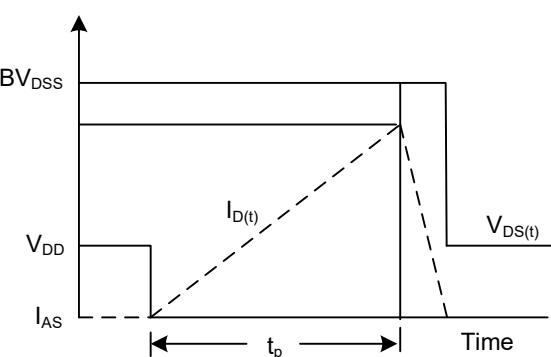
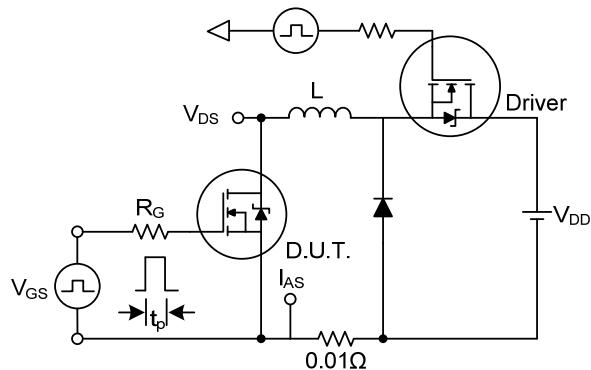
Switching Test Circuit

Switching Waveforms



Gate Charge Test Circuit

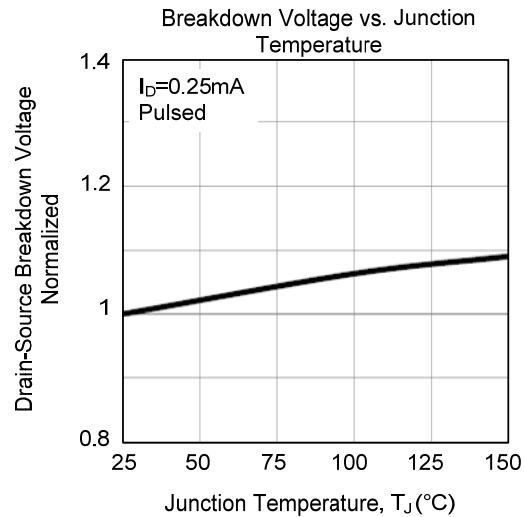
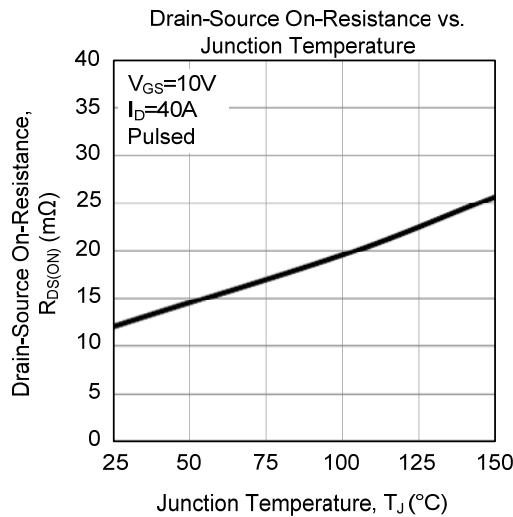
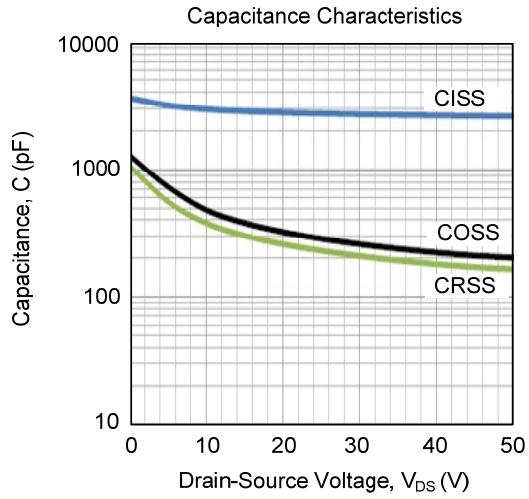
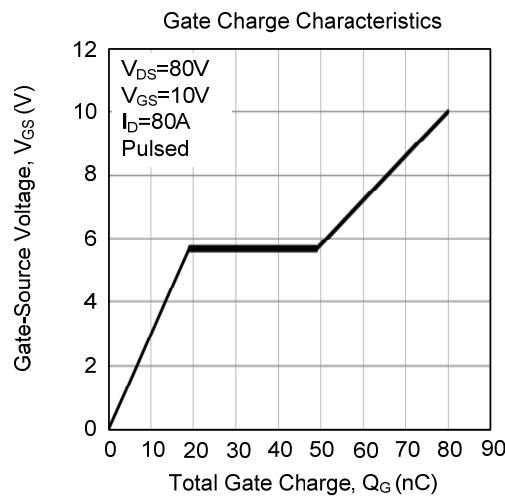
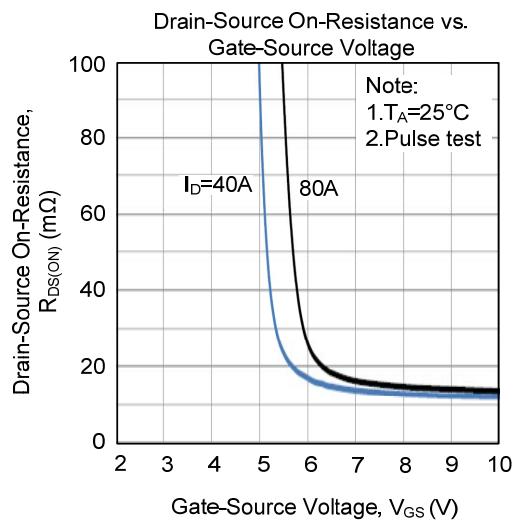
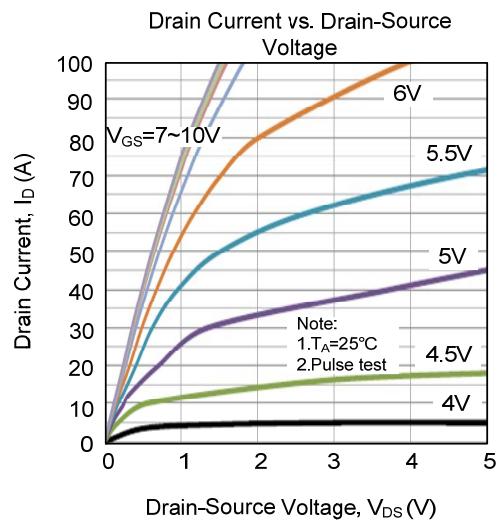
Gate Charge Waveform



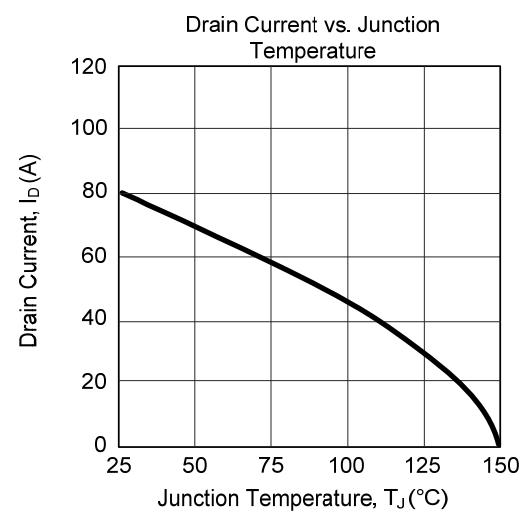
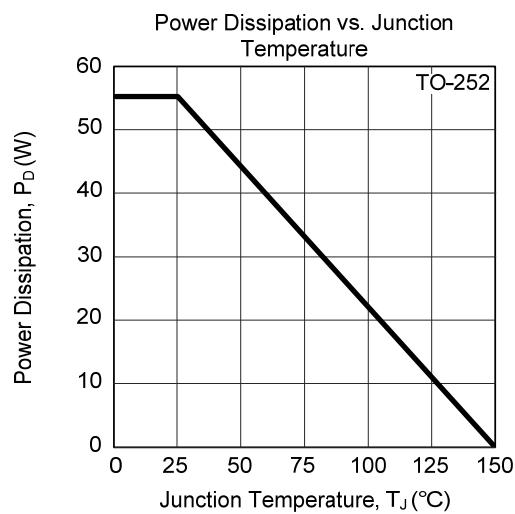
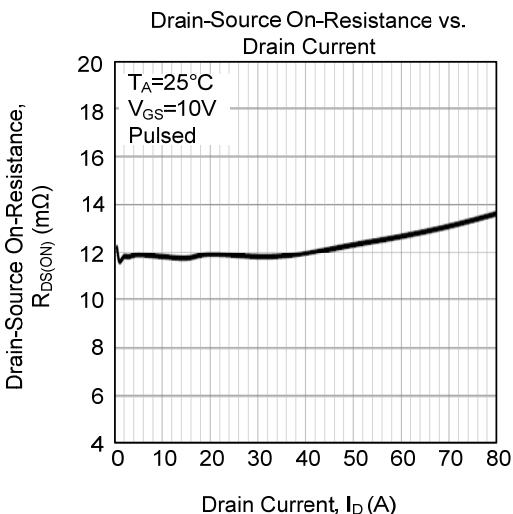
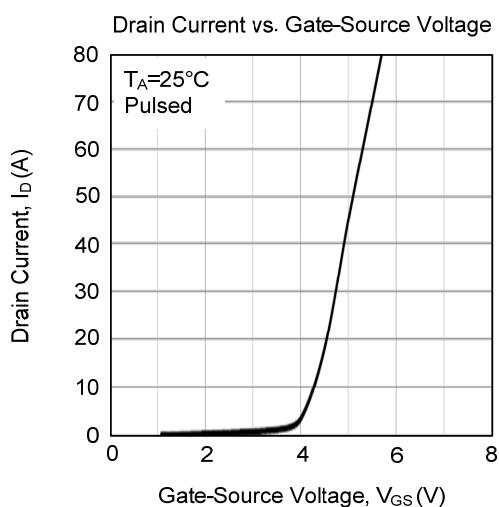
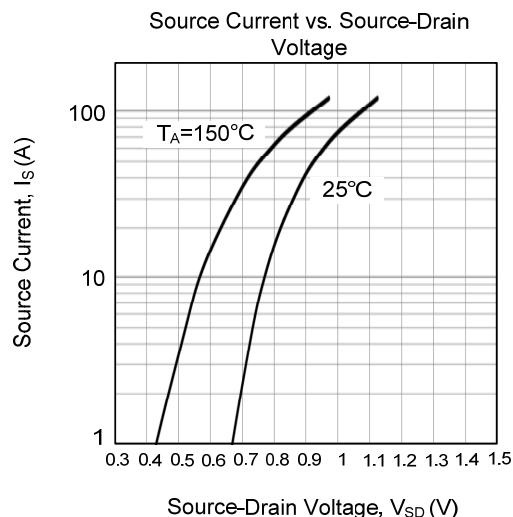
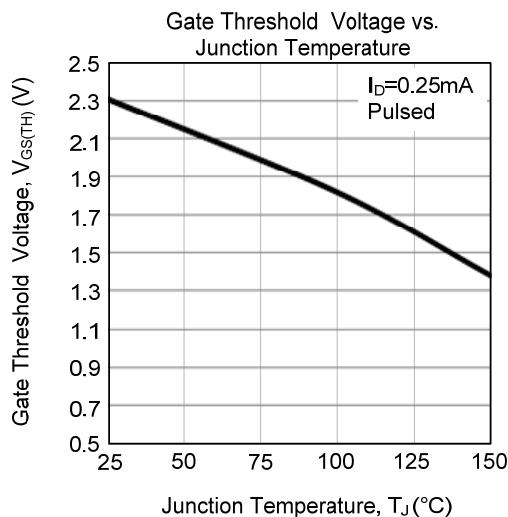
Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

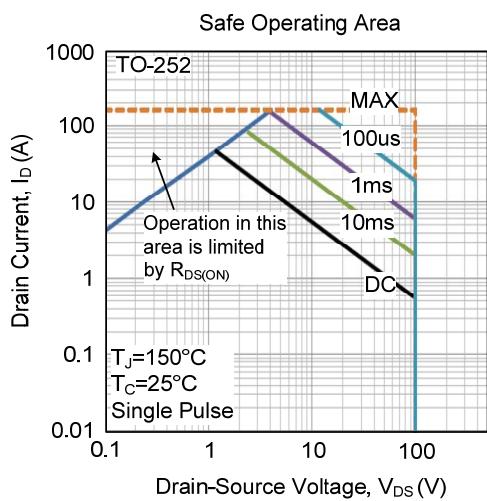
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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