



3N60-TA

Power MOSFET

3A, 600V N-CHANNEL POWER MOSFET

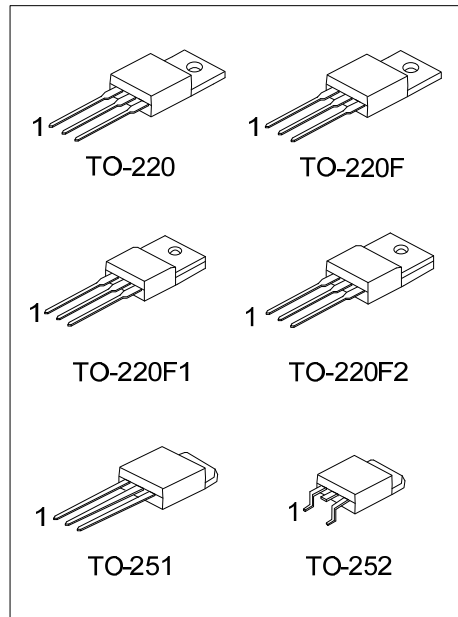
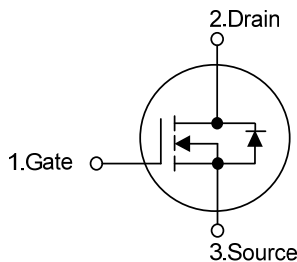
DESCRIPTION

The UTC **3N60-TA** is a high voltage power MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications of switching power supplies and adaptors.

FEATURES

- * $R_{DS(ON)} < 3.3\Omega$ @ $V_{GS}=10V$, $I_D=1.5A$
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness

SYMBOL



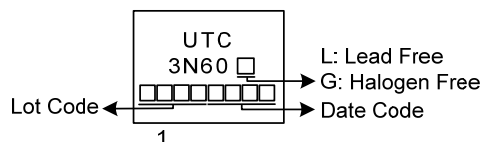
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
3N60L-TA3-T	3N60G-TA3-T	TO-220	G	D	S	Tube
3N60L-TF1-T	3N60G-TF1-T	TO-220F1	G	D	S	Tube
3N60L-TF2-T	3N60G-TF2-T	TO-220F2	G	D	S	Tube
3N60L-TF3-T	3N60G-TF3-T	TO-220F	G	D	S	Tube
3N60L-TM3-T	3N60G-TM3-T	TO-251	G	D	S	Tube
3N60L-TN3-R	3N60G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

	<p>(1) T: Tube, R: Tape Reel</p> <p>(2) TA3: TO-220, TF3: TO-220F, TF1: TO-220F1 TF2: TO-220F2, TM3: TO-251, TN3: TO-252</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
--	---

MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_c = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	600	V
Gate-Source Voltage		V_{GSS}	± 30	V
Continuous Drain Current		I_D	3	A
Pulsed Drain Current (Note 2)		I_{DM}	6	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	101	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	3.3	V/ns
Power Dissipation	TO-220	P_D	75	W
	TO-220F/TO-220F1		34	W
	TO-220F2			
	TO-251/TO-252		45	W
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 10\text{mH}$, $I_{AS} = 4.5\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\ \Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 3.0\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220/TO-220F	θ_{JA}	62.5	$^\circ\text{C}/\text{W}$
	TO-220F1/TO-220F2			
	TO-251/TO-252		110 (Note)	$^\circ\text{C}/\text{W}$
Junction to Case	TO-220	θ_{JC}	1.67	$^\circ\text{C}/\text{W}$
	TO-220F/TO-220F1		3.68	$^\circ\text{C}/\text{W}$
	TO-220F2			
	TO-251/TO-252		2.78 (Note)	$^\circ\text{C}/\text{W}$

Note: The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

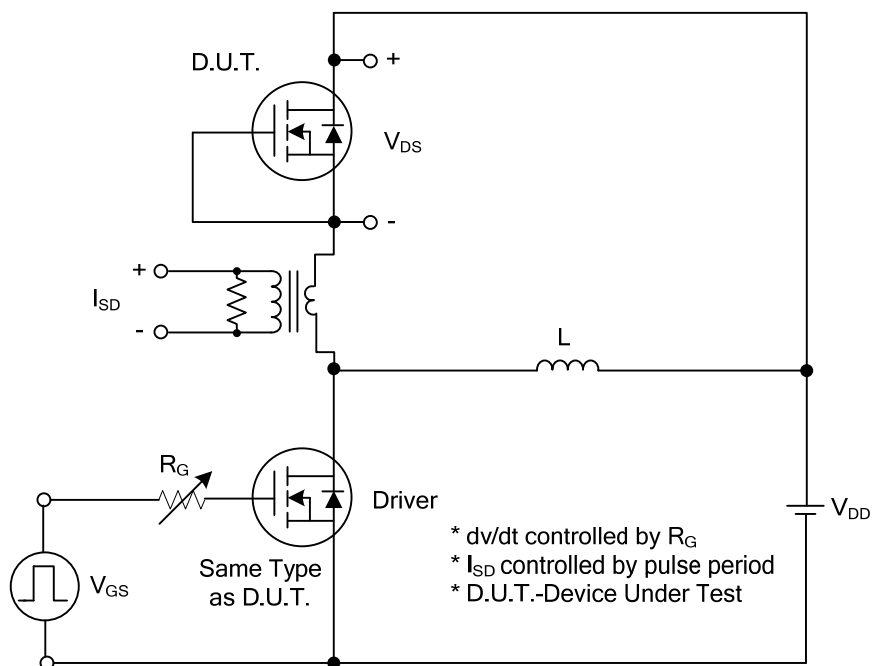
■ ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	$V_{GS} = 0V, I_D = 250\mu A$	600			V
Drain-Source Leakage Current		I_{DSS}	$V_{DS} = 600V, V_{GS} = 0V$			10	μA
Gate- Source Leakage Current	Forward	I_{GSS}	$V_{GS} = 30V, V_{DS} = 0V$			100	nA
	Reverse		$V_{GS} = -30V, V_{DS} = 0V$			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	2.0		4.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 1.5A$			3.3	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		C_{ISS}	$V_{DS}=25V, V_{GS}=0V, f=1.0MHz$		392		pF
Output Capacitance		C_{OSS}			46		pF
Reverse Transfer Capacitance		C_{RSS}			3.4		pF
SWITCHING CHARACTERISTICS							
Total Gate Charge (Note 1)		Q_G	$V_{DS}=100V, V_{GS}=10V, I_D=2A$ $I_G=1mA$ (Note 1, 2)		10		nC
Gate-Source Charge		Q_{GS}			3.4		nC
Gate-Drain Charge		Q_{GD}			1.8		nC
Turn-On Delay Time (Note 1)		$t_{D(ON)}$	$V_{DS}=30V, V_{GS}=10V, I_D=0.5A,$ $R_G=25\Omega$ (Note 1, 2)		33		ns
Turn-On Rise Time		t_R			23		ns
Turn-Off Delay Time		$t_{D(OFF)}$			110		ns
Turn-Off Fall Time		t_F			52		ns
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS							
Maximum Body-Diode Continuous Current		I_S				3	A
Maximum Body-Diode Pulsed Current		I_{SM}				6	A
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	$I_S=3.0A, V_{GS}=0V$			1.4	V
Reverse Recovery Time (Note 1)		t_{rr}	$I_S=3.0A, V_{GS}=0V$		264		ns
Reverse Recovery Charge		Q_{rr}	$di/dt=100A/\mu s$		1.6		μC

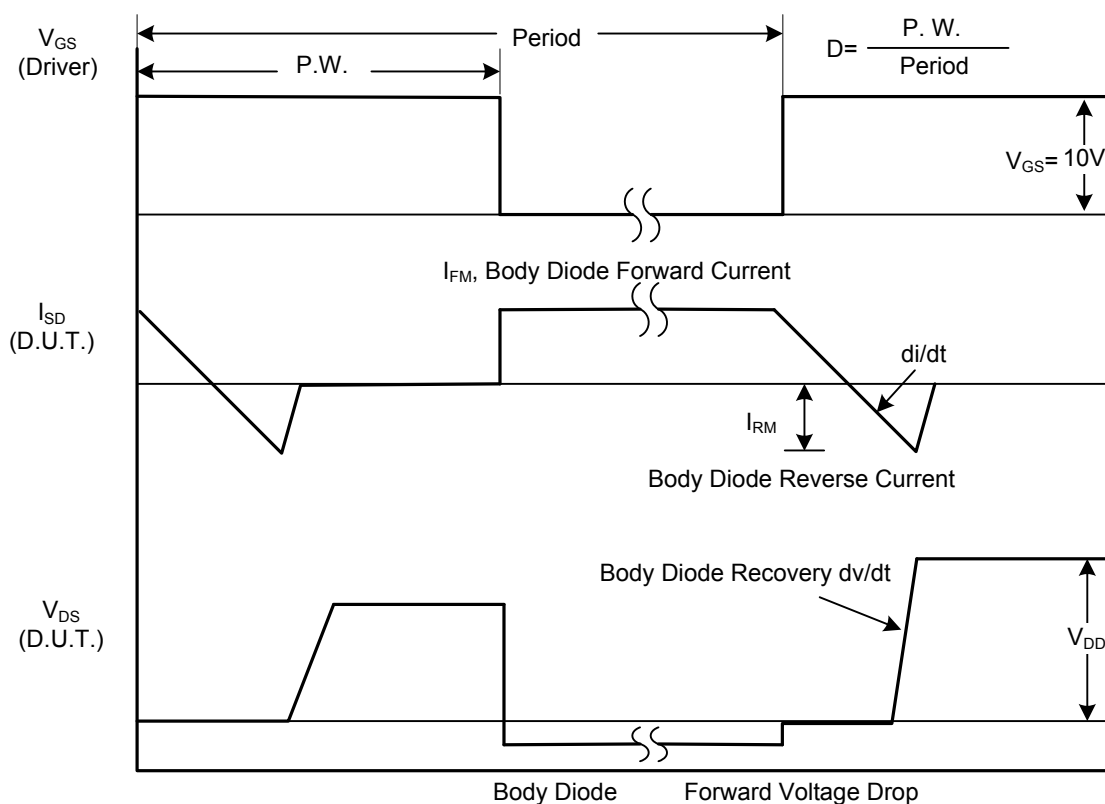
Notes: 1. Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

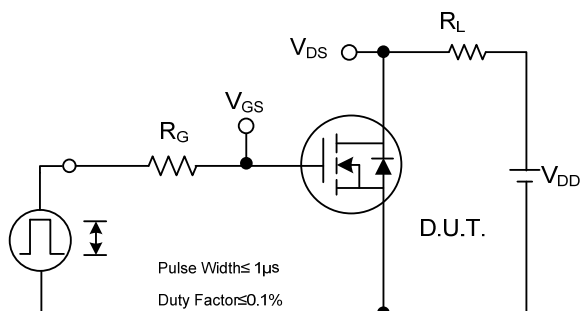


Peak Diode Recovery dv/dt Test Circuit

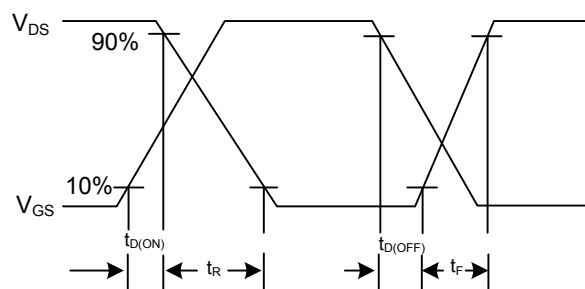


Peak Diode Recovery dv/dt Waveforms

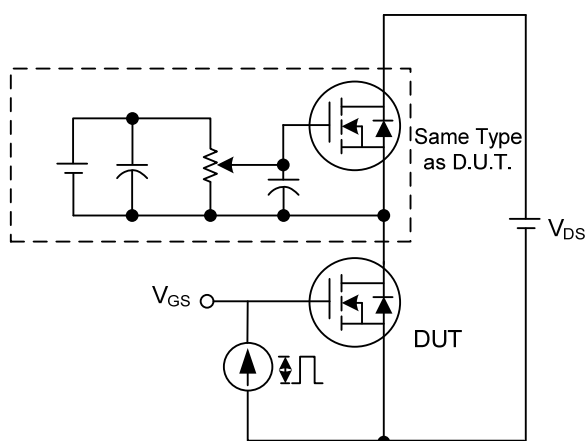
■ TEST CIRCUITS AND WAVEFORMS



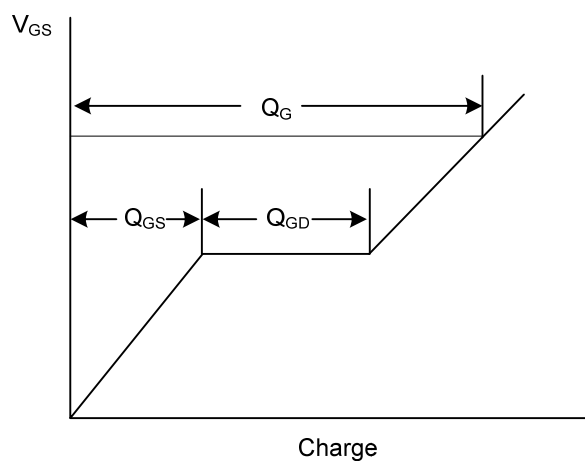
Switching Test Circuit



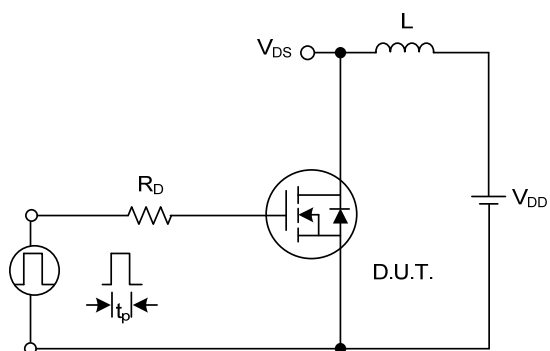
Switching Waveforms



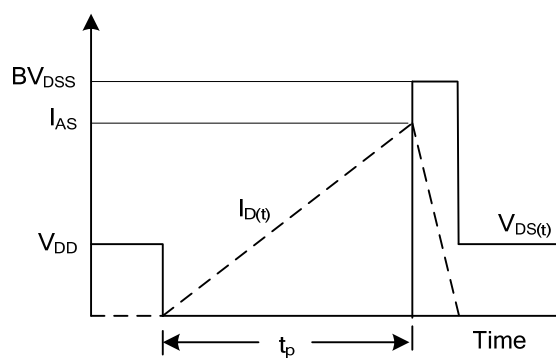
Gate Charge Test Circuit



Gate Charge Waveform

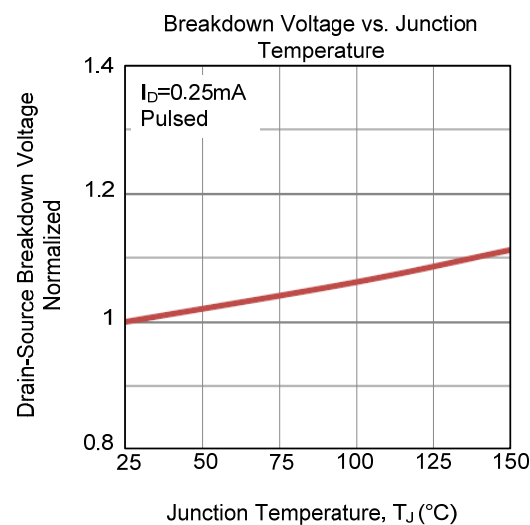
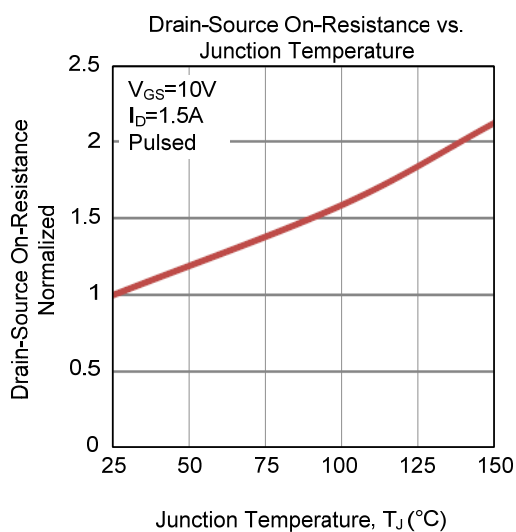
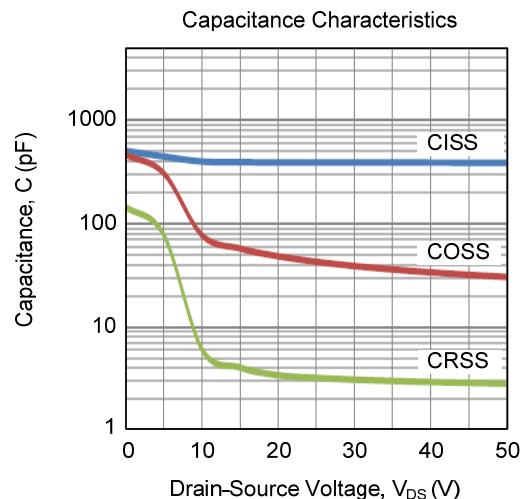
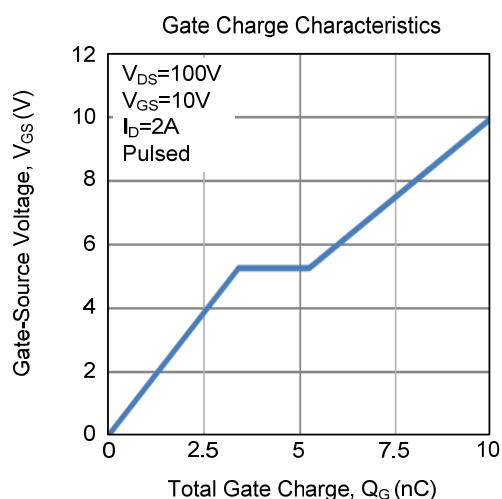
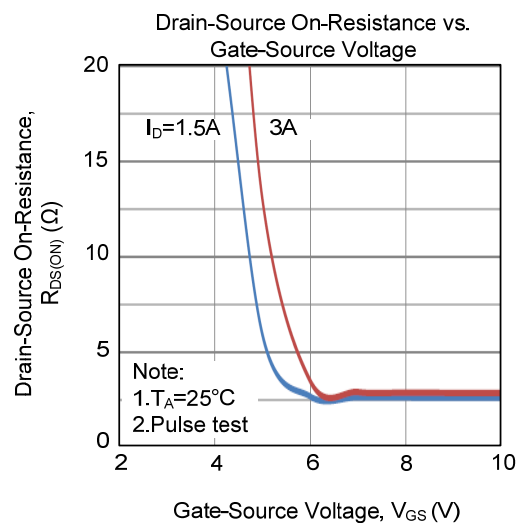
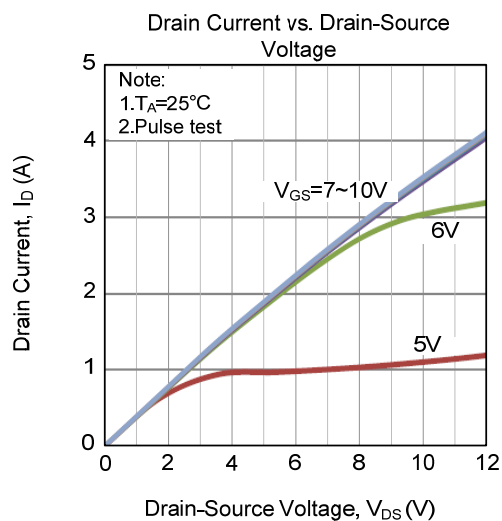


Unclamped Inductive Switching Test Circuit

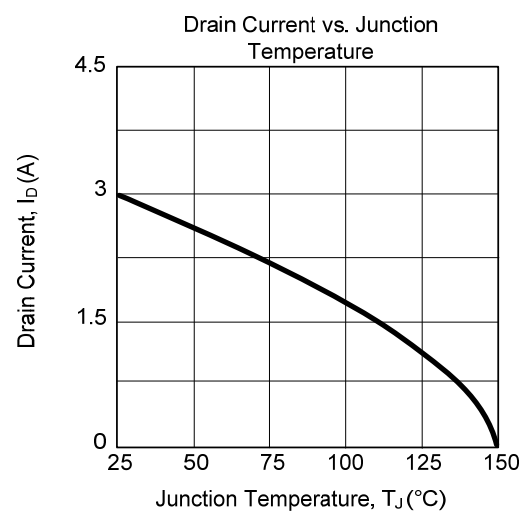
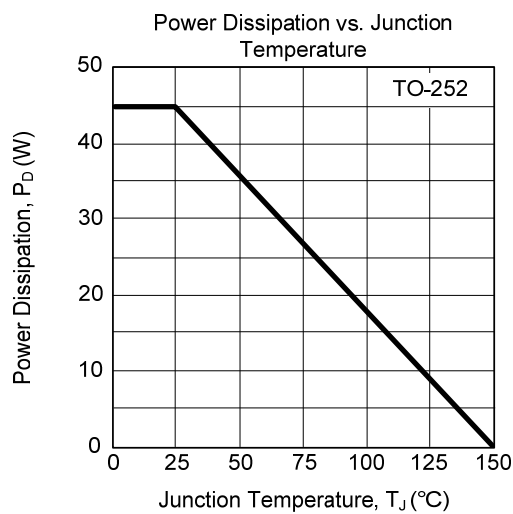
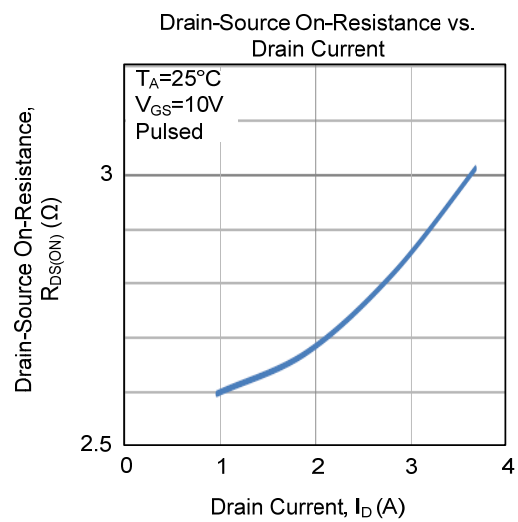
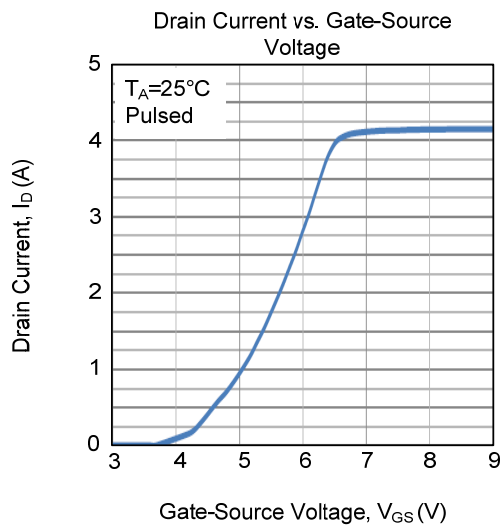
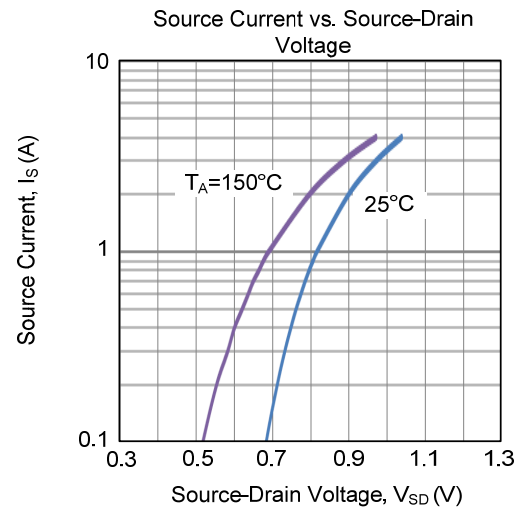
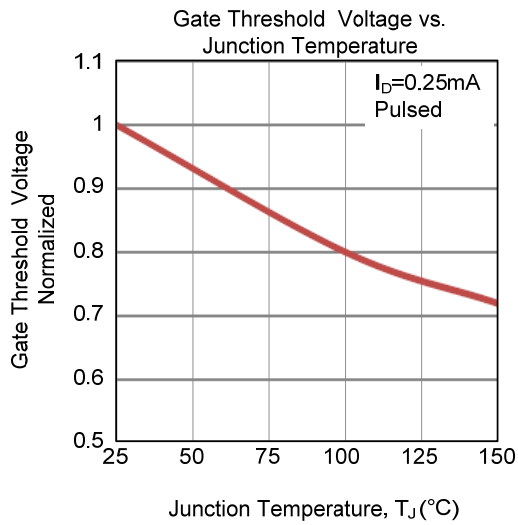


Unclamped Inductive Switching Waveforms

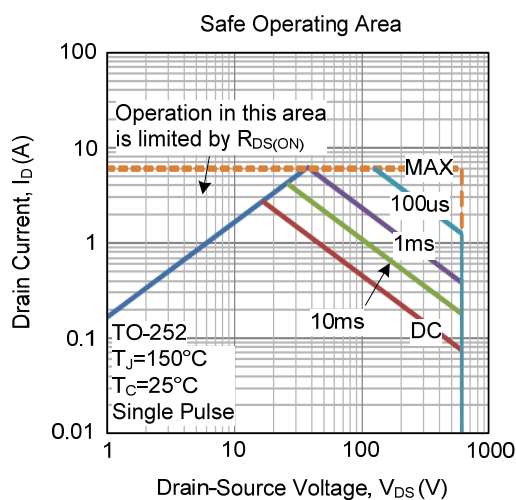
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



UTC assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all UTC products described or contained herein. UTC products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. UTC reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.