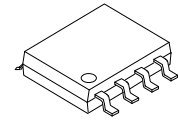
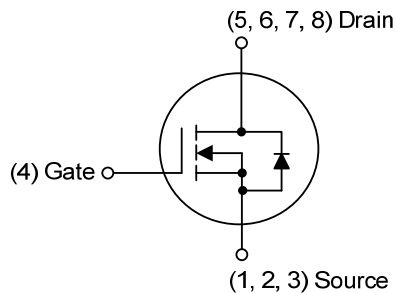


**UT24N04****POWER MOSFET****24A, 40V N-CHANNEL
POWER MOSFET****DESCRIPTION**

The UTC **UT24N04** is a N-channel enhancement MOSFET using UTC's advanced technology to provide the customers with perfect $R_{DS(ON)}$ and high switching speed.

FEATURES

- * $R_{DS(ON)} \leq 17m\Omega$ @ $V_{GS}=10V$, $I_D=12A$
- $R_{DS(ON)} \leq 22m\Omega$ @ $V_{GS}=4.5V$, $I_D=12A$
- * High Switching Speed

SYMBOL

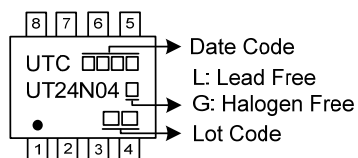
SOP-8

ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UT24N04L-S08-R	UT24N04G-S08-R	SOP-8	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: S: Source G: Gate D: Drain

UT24N04G-S08-R 		(1) R: Tape Reel (2) S08: SOP-8 (3) G: Halogen Free and Lead Free, L: Lead Free
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MARKING

■ ABSOLUTE MAXIMUM RATING ($T_C=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	40	V
Gate-Source Voltage		V_{GSS}	± 20	V
Drain Current	Continuous	I_D	24	A
	Pulsed (Note 2)	I_{DM}	48	A
Single Pulse Avalanche Energy		E_{AS}	27.1	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	2.65	V/ns
Power Dissipation		P_D	6.25	W
Junction Temperature		T_J	+150	$^{\circ}\text{C}$
Storage Temperature Range		T_{STG}	-55 ~ +150	$^{\circ}\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature.

3. $L=0.1\text{mH}$, $I_{AS}=23.3\text{A}$, $V_{DD}=50\text{V}$, $R_G=25\Omega$, Starting $T_J = 25^{\circ}\text{C}$

4. $I_{SD}\leq 25.4\text{A}$, $di/dt\leq 200\text{A}/\mu\text{s}$, $V_{DD}\leq BV_{DSS}$, Starting $T_J = 25^{\circ}\text{C}$

■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	62.5	$^{\circ}\text{C}/\text{W}$
Junction to Case	θ_{JC}	20	$^{\circ}\text{C}/\text{W}$

Note: The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

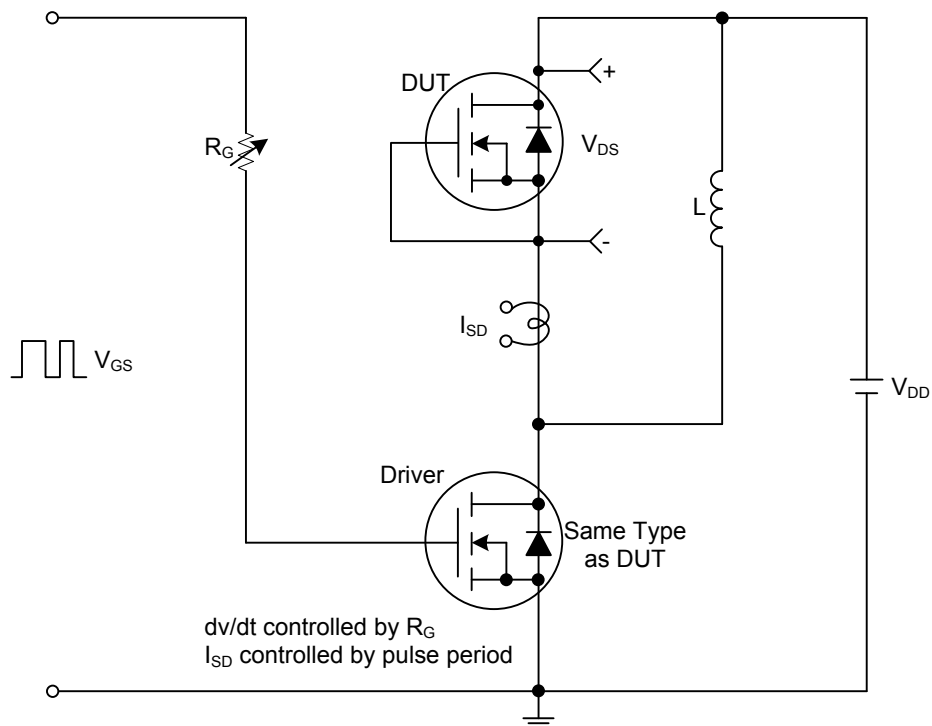
■ ELECTRICAL CHARACTERISTICS (T_J = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =250μA, V _{GS} =0V	40			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =40V, V _{GS} =0V			1	μA
Gate-Source Leakage Current	Forward	I _{GSS}	V _{GS} =+20V, V _{DS} =0V			+100	nA
	Reverse		V _{GS} =-20V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	0.6		1.8	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =12A			17	mΩ
			V _{GS} =4.5V, I _D =12A			22	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		C _{ISS}	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		579		pF
Output Capacitance		C _{OSS}			88		pF
Reverse Transfer Capacitance		C _{RSS}			73		pF
SWITCHING PARAMETERS							
Total Gate Charge (Note 1)		Q _G	V _{DS} =20V, V _{GS} =10V, I _D =24A, I _G =1mA (Note 1, 2)		19		nC
Gate to Source Charge		Q _{GS}			1.8		nC
Gate to Drain Charge		Q _{GD}			3.5		nC
Turn-on Delay Time (Note 1)		t _{D(ON)}	V _{DD} =20V, V _{GS} =10V, I _D =24A, R _G =3.3Ω (Note 1, 2)		3.2		ns
Rise Time		t _R			16		ns
Turn-off Delay Time		t _{D(OFF)}			20		ns
Fall-Time		t _F			20		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I _S				24	A
Maximum Body-Diode Pulsed Current		I _{SM}				48	A
Drain-Source Diode Forward Voltage (Note 1)		V _{SD}	I _S =24A, V _{GS} =0V			1.4	V
Reverse Recovery Time (Note 1)		t _{rr}	I _S =24A, V _{GS} =0V,		24		nS
Reverse Recovery Charge		Q _{rr}	dl _F /dt =100A/μs		12		nC

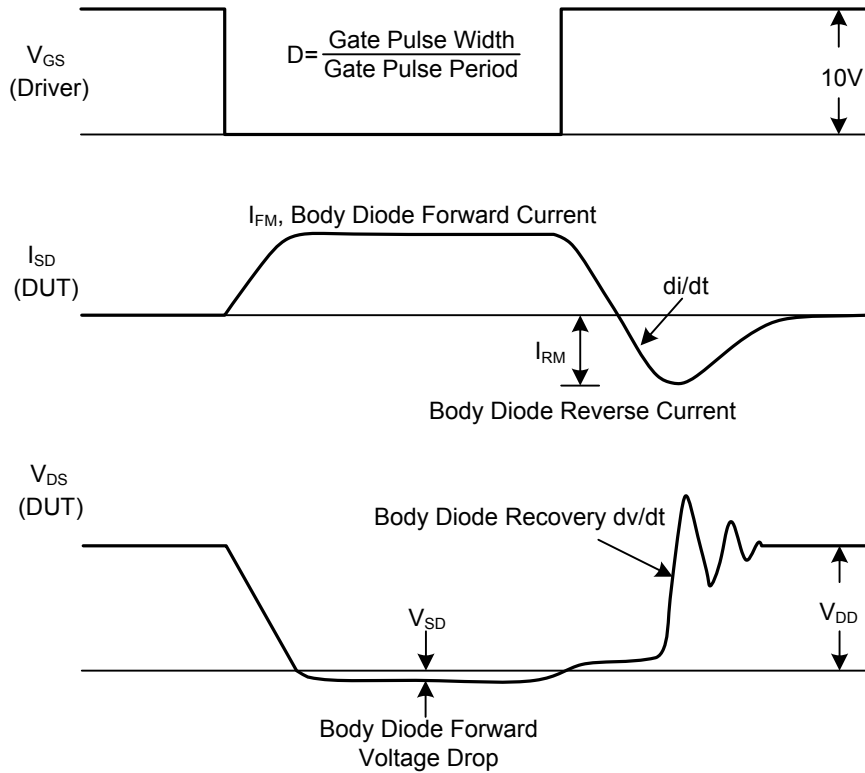
Notes: 1. Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating ambient temperature.

■ TEST CIRCUITS AND WAVEFORMS



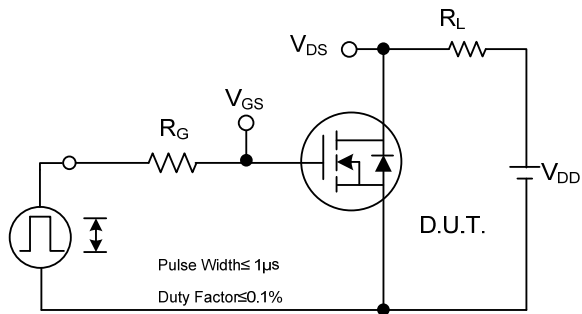
Peak Diode Recovery dv/dt Test Circuit



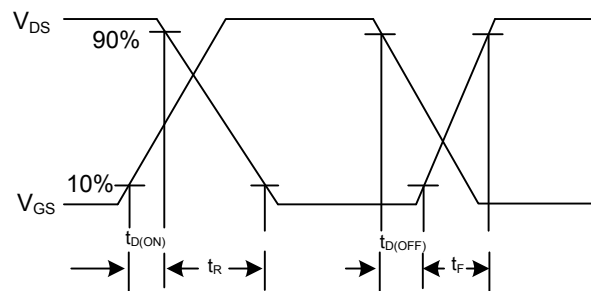
Peak Diode Recovery dv/dt Test Circuit and Waveforms

Peak Diode Recovery dv/dt Waveforms

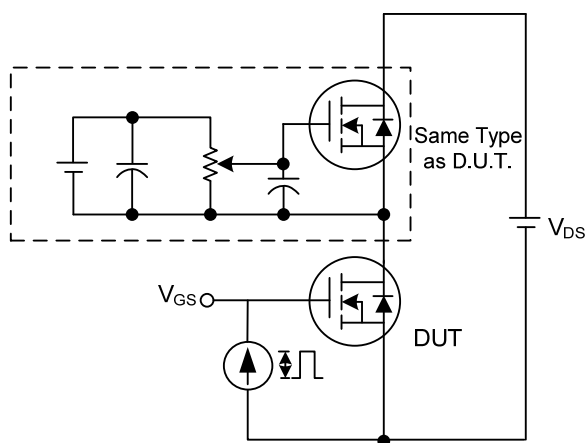
■ TEST CIRCUITS AND WAVEFORMS



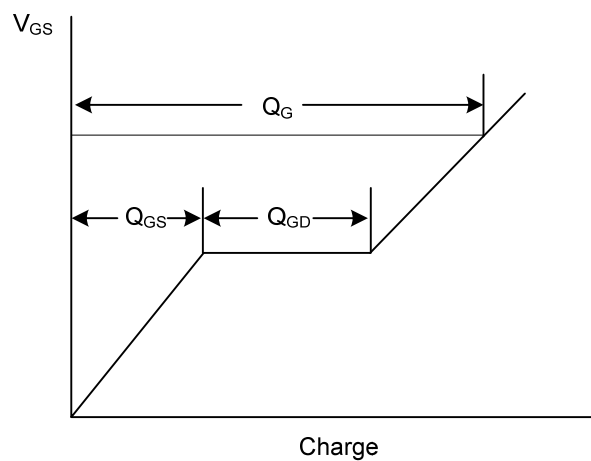
Switching Test Circuit



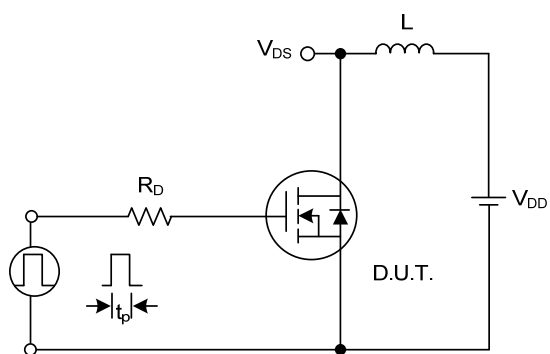
Switching Waveforms



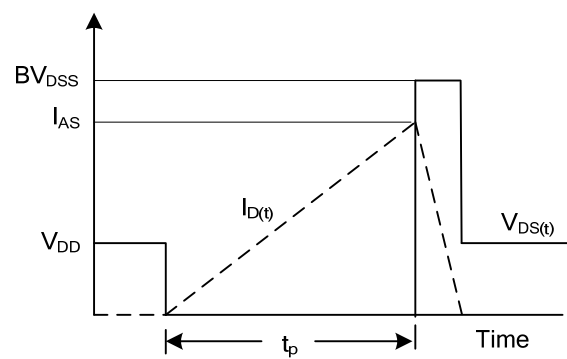
Gate Charge Test Circuit



Gate Charge Waveform

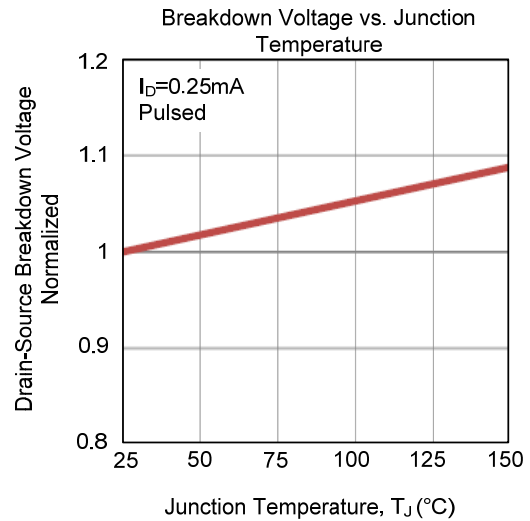
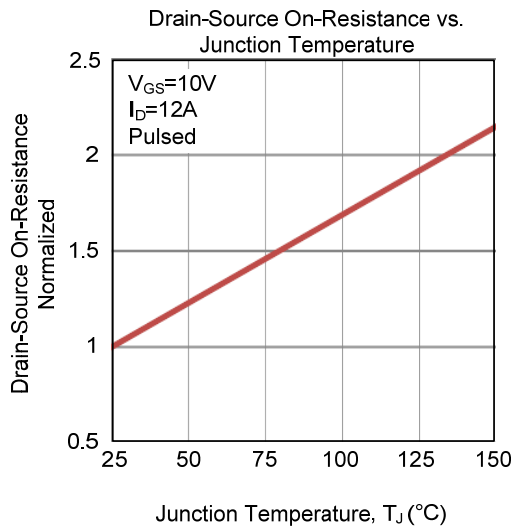
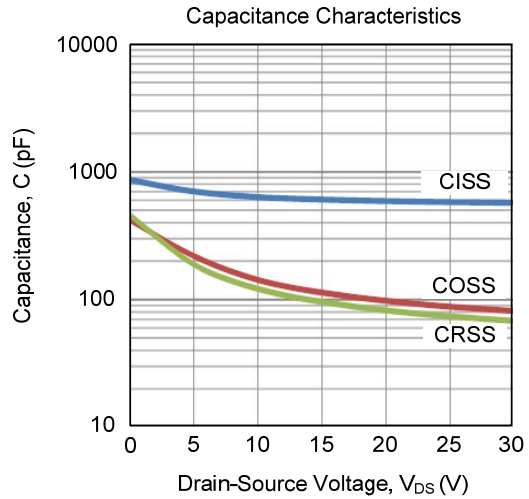
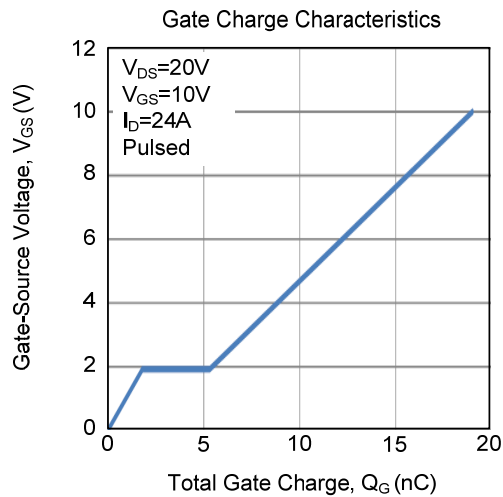
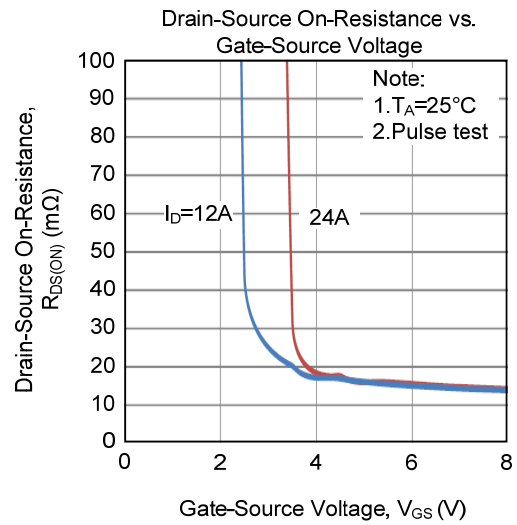
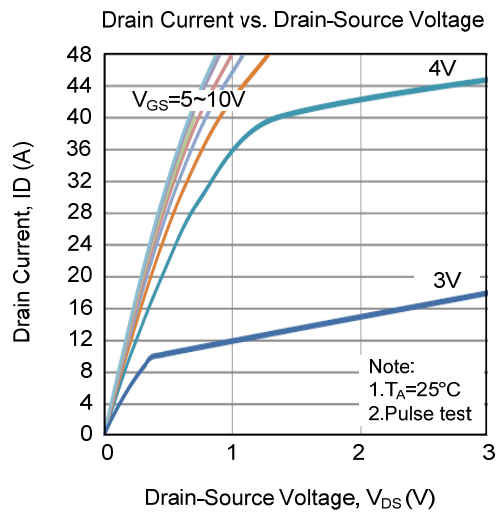


Unclamped Inductive Switching Test Circuit

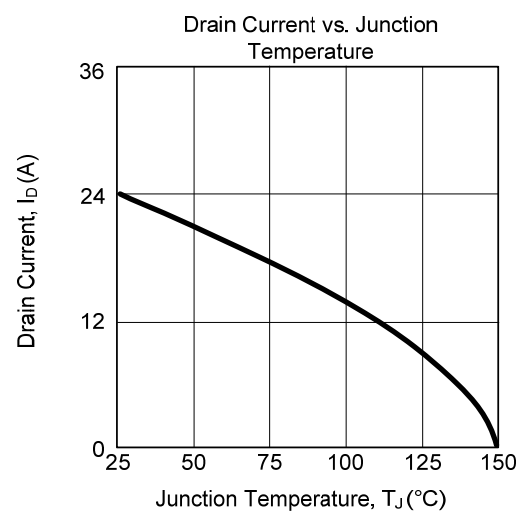
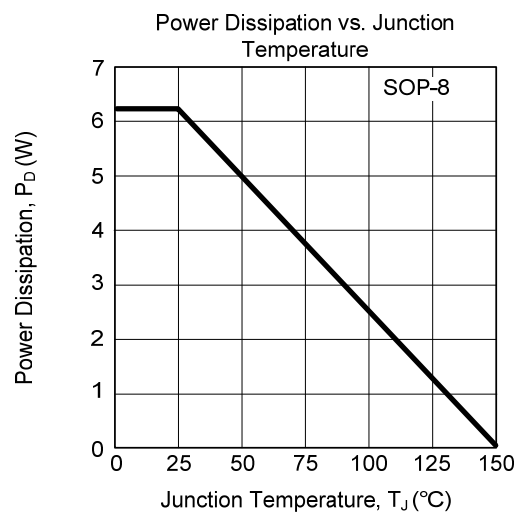
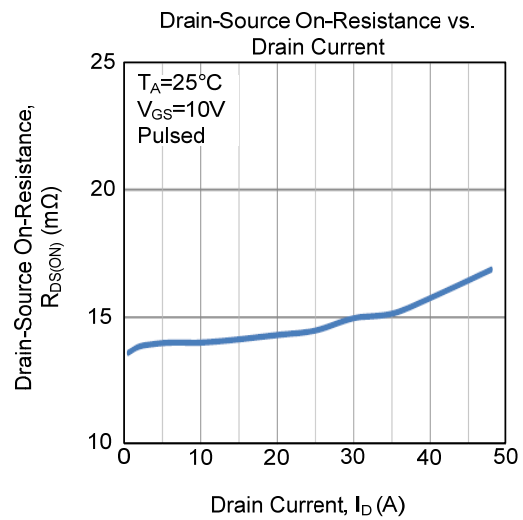
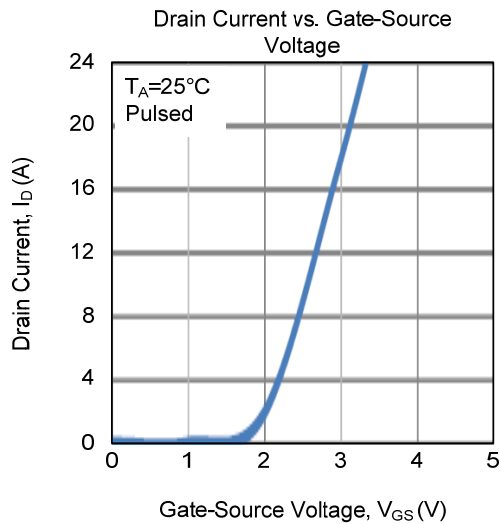
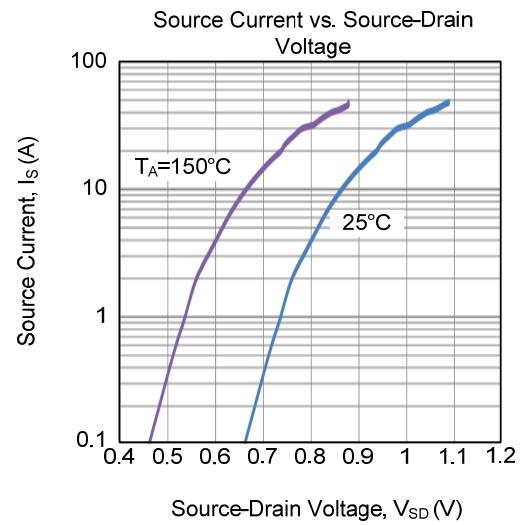
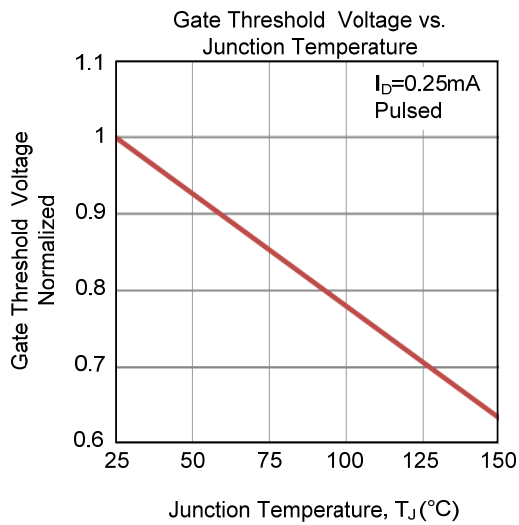


Unclamped Inductive Switching Waveforms

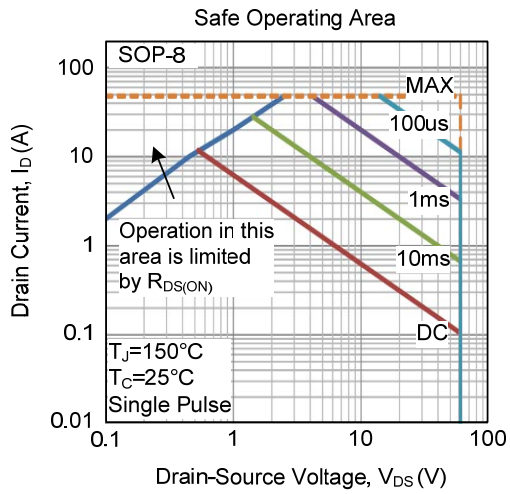
TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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