

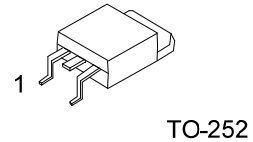
**UTT28N15****POWER MOSFET****28A, 150V N-CHANNEL
POWER MOSFET****DESCRIPTION**

The UTC **UTT28N15** is a N-channel mode power MOSFET using UTC's advanced technology to provide customers with a minimum on-state resistance, low gate charge and high switching speed.

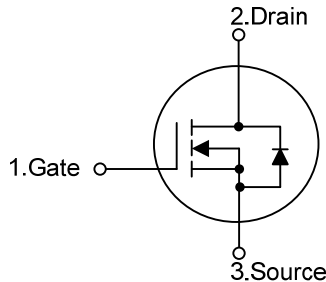
The UTC **UTT28N15** is suitable for high voltage synchronous rectifier and DC/DC converters, etc.

FEATURES

- * $R_{DS(ON)} \leq 54m\Omega$ @ $V_{GS}=10V$, $I_D=14A$
- $R_{DS(ON)} \leq 60m\Omega$ @ $V_{GS}=4.5V$, $I_D=14A$
- * High Switching Speed
- * High Cell Density Trench Technology



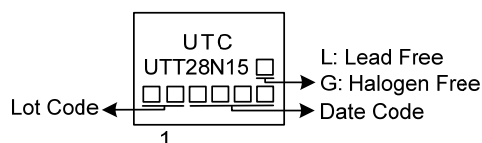
TO-252

SYMBOL**ORDERING INFORMATION**

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UTT28N15L-TN3-R	UTT28N15G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

UTT28N15G-TN3-R (1)Packing Type (2)Package Type (3)Green Package	(1) R: Tape Reel (2) TN3: TO-252 (3) G: Halogen Free and Lead Free, L: Lead Free
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MARKING

■ ABSOLUTE MAXIMUM RATING (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	150	V
Gate-Source Voltage		V _{GSS}	±20	V
Drain Current	Continuous	I _D	28	A
	Pulsed (Note 2)	I _{DM}	56	A
Avalanche Energy (Note 3)	Single Pulsed (Note 3)	E _{AS}	296	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.6	V/nS
Power Dissipation		P _D	41.7	W
Junction Temperature		T _J	+150	°C
Storage Temperature Range		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature

3. L=10mH, I_{AS}=7.7A, V_{DD}=50V, R_G = 25Ω, Starting T_J = 25°C

4. I_{SD} ≤ 28A, di/dt ≤ 100A/μs, V_{DD} ≤ V_{(BR)DSS}, T_J ≤ 25°C

■ THERMAL CHARACTERISTICS

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient		θ _{JA}	110	°C/W
Junction to Case		θ _{JC}	2.99 (Note)	°C/W

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

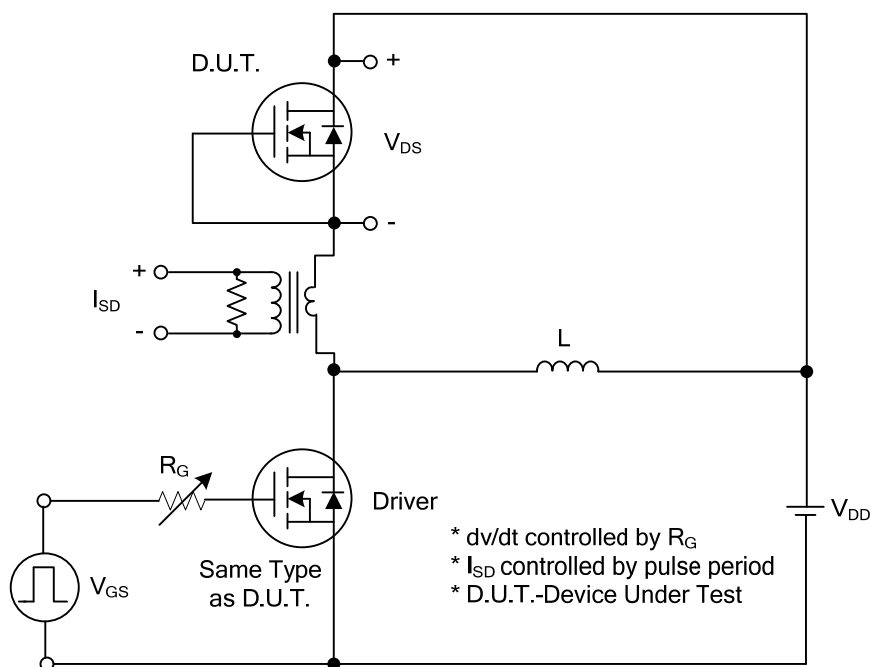
■ ELECTRICAL CHARACTERISTICS (T_J =25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =250μA, V _{GS} =0V	150			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =150V, V _{GS} =0V			10	μA
Gate-Source Leakage Current	Forward	I _{GSS}	V _{GS} =+20V, V _{DS} =0V			+100	nA
	Reverse		V _{GS} =-20V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	1.0		3.0	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =14A			54	mΩ
			V _{GS} =4.5V, I _D =14A			60	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		C _{ISS}	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		2600		pF
Output Capacitance		C _{OSS}			177		pF
Reverse Transfer Capacitance		C _{RSS}			110		pF
SWITCHING PARAMETERS							
Total Gate Charge (Note 1)		Q _G	V _{DS} =120V, V _{GS} =10V, I _D =28A I _G =1mA (Note 1, 2)		63		nC
Gate to Source Charge		Q _{GS}			6.5		nC
Gate to Drain Charge		Q _{GD}			16		nC
Turn-on Delay Time (Note 1)		t _{D(ON)}	V _{DS} =100V, V _{GS} =10V, I _D =28A, R _G =25Ω (Note 1, 2)		20		ns
Rise Time		t _R			36		ns
Turn-off Delay Time		t _{D(OFF)}			220		ns
Fall-Time		t _F			120		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I _S				28	A
Maximum Body-Diode Pulsed Current		I _{SM}				56	A
Drain-Source Diode Forward Voltage (Note 1)		V _{SD}	I _S =28A, V _{GS} =0V			1.4	V
Reverse Recovery Time (Note 1)		t _{rr}	I _S =28A, V _{GS} =0V,		63		nS
Reverse Recovery Charge		Q _{rr}	dl/dt=100A/μs		118		nC

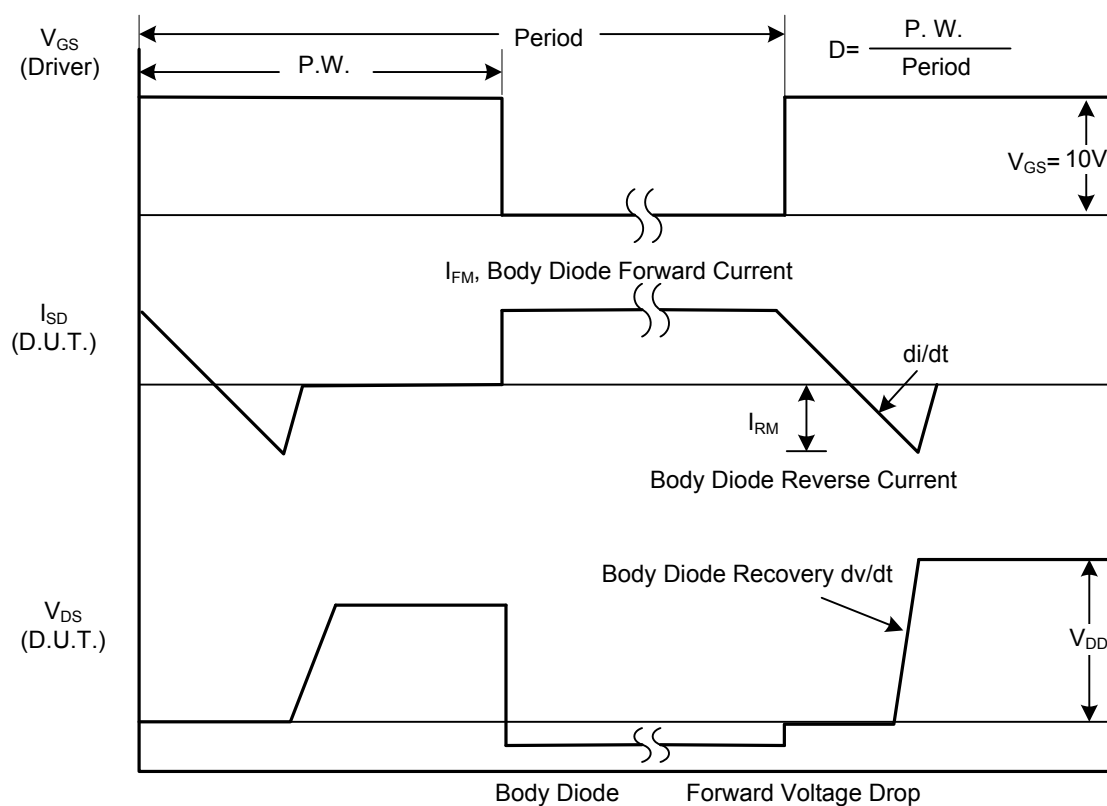
Notes: 1. Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

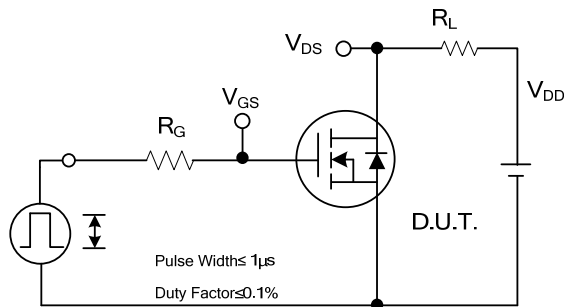


Peak Diode Recovery dv/dt Test Circuit

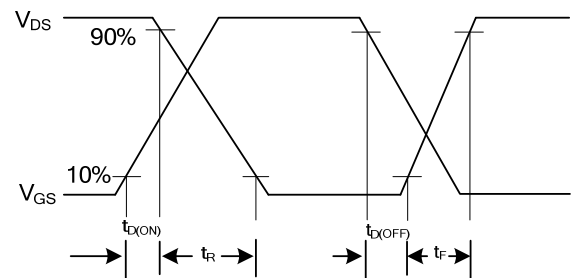


Peak Diode Recovery dv/dt Waveforms

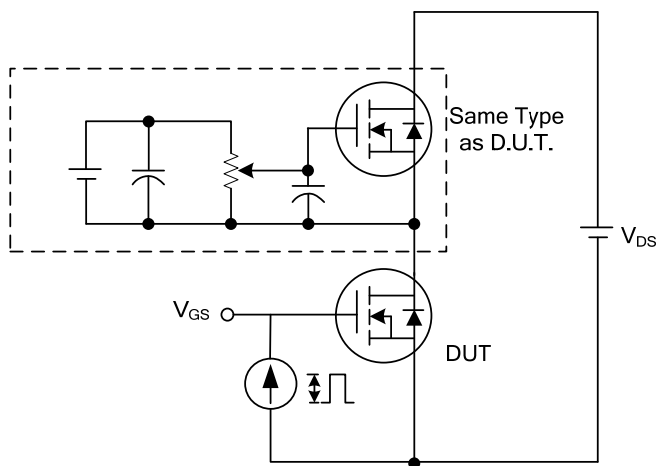
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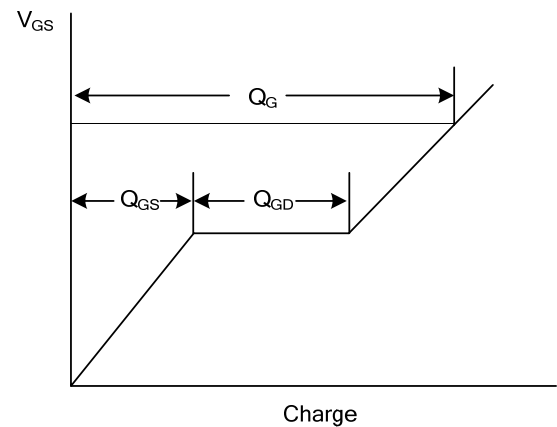
Switching Test Circuit



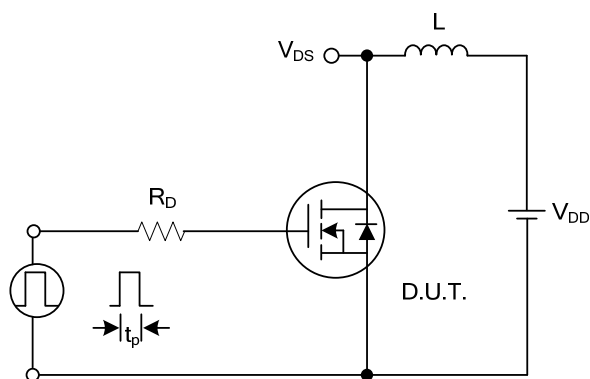
Switching Waveforms



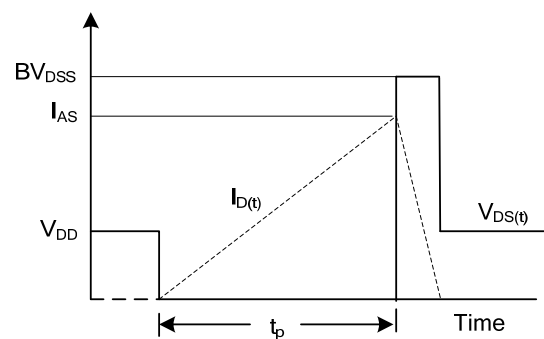
Gate Charge Test Circuit



Gate Charge Waveform

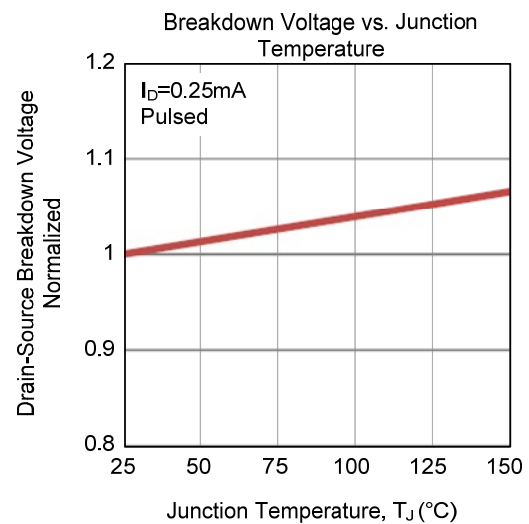
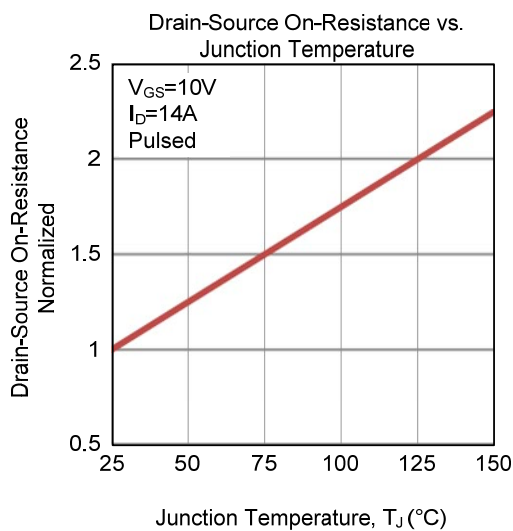
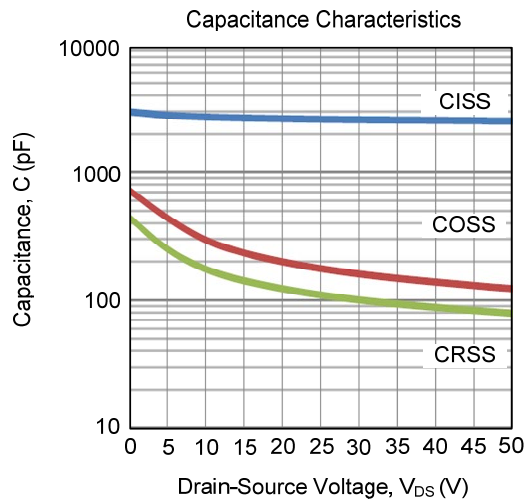
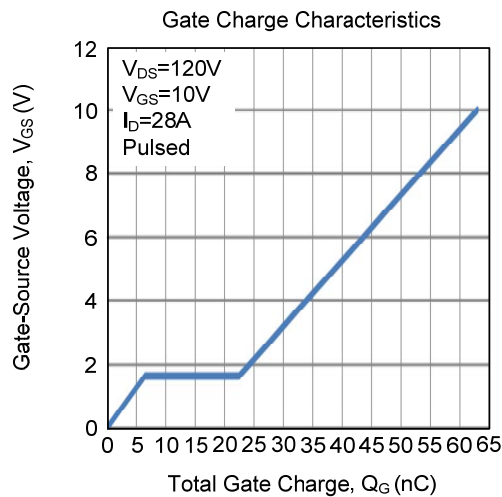
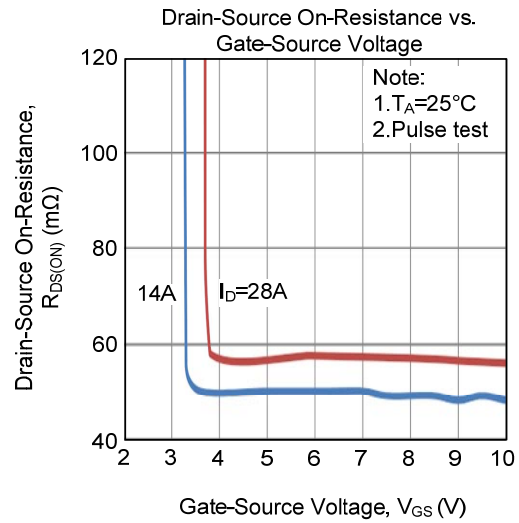
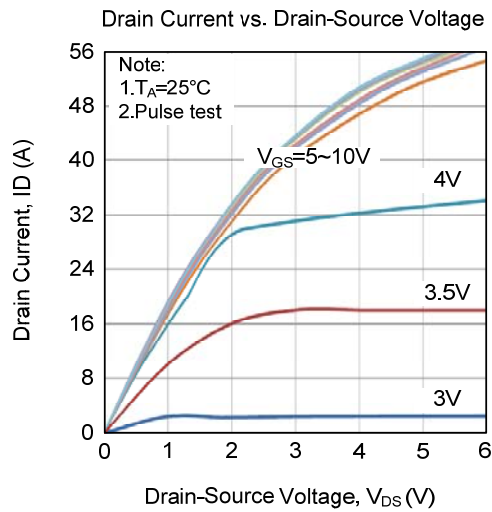


Unclamped Inductive Switching Test Circuit

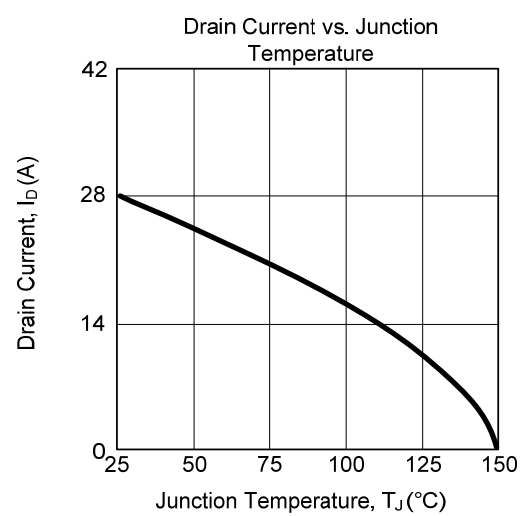
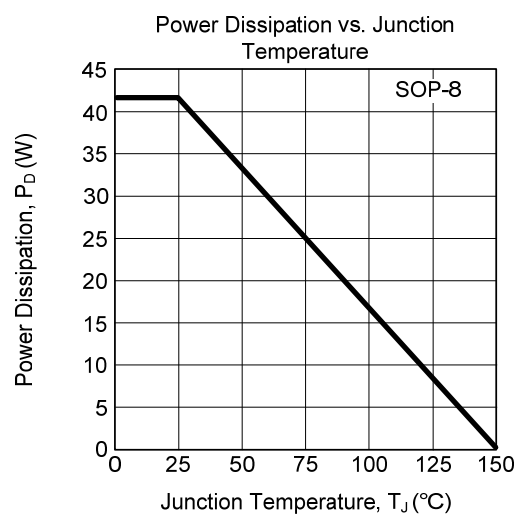
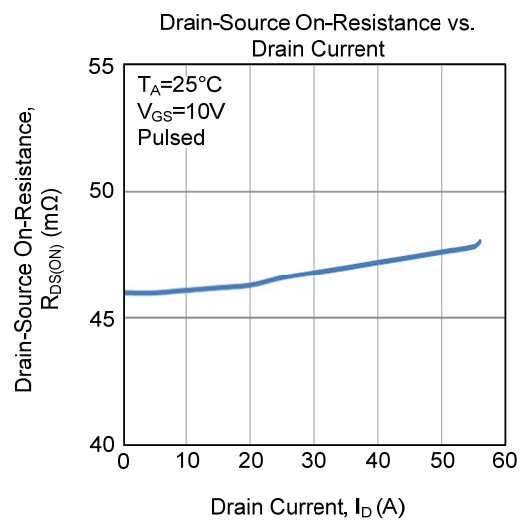
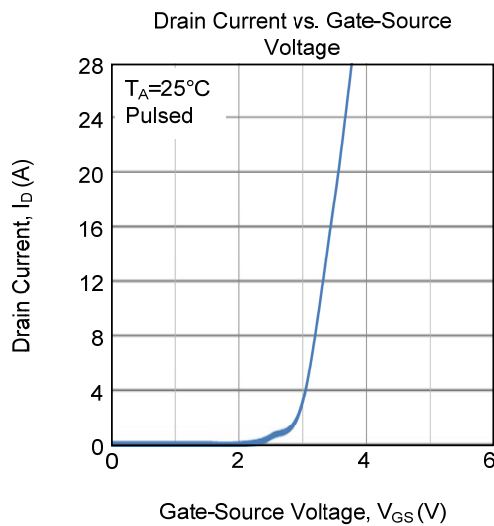
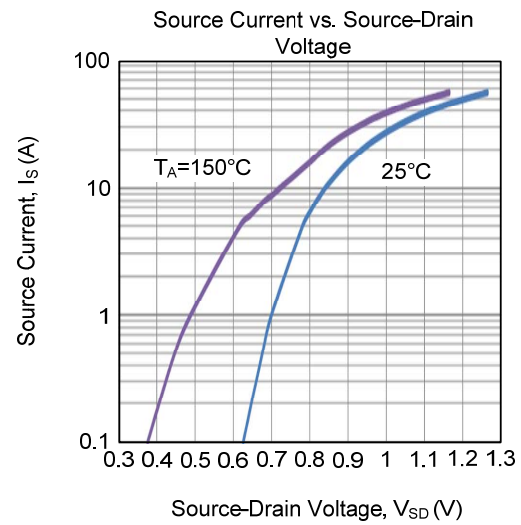
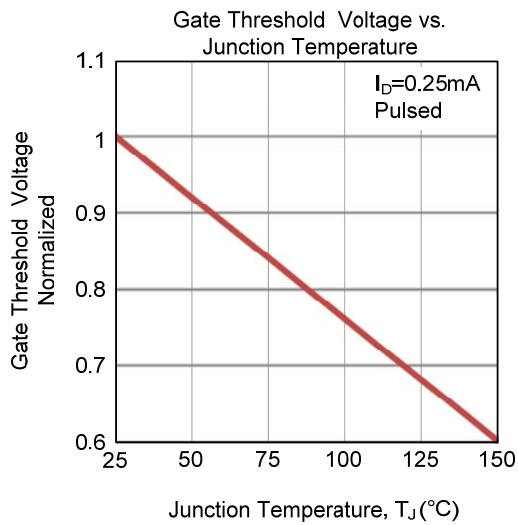


Unclamped Inductive Switching Waveforms

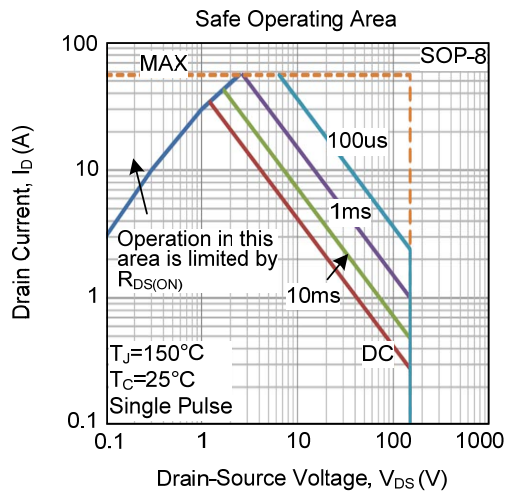
TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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