



DUAL ENHANCEMENT MODE (N-CHANNEL / P-CHANNEL)

DESCRIPTION

The UTC **UT20NP04** incorporates a N-channel MOSFET and a P-channel MOSFET, it uses UTC's advanced technology to provide customers a minimum on-state resistance, high switching speed, low gate charge and cost effectiveness.

The UTC **UT20NP04** is universally applied in low voltage applications.

FEATURES

*N-CHANNEL

$$R_{DS(on)} \leq 45 \text{ m}\Omega @ V_{GS}=10\text{V}, I_D=10\text{A}$$

$$R_{DS(on)} \leq 65 \text{ m}\Omega @ V_{GS}=4.5\text{V}, I_D=10\text{A}$$

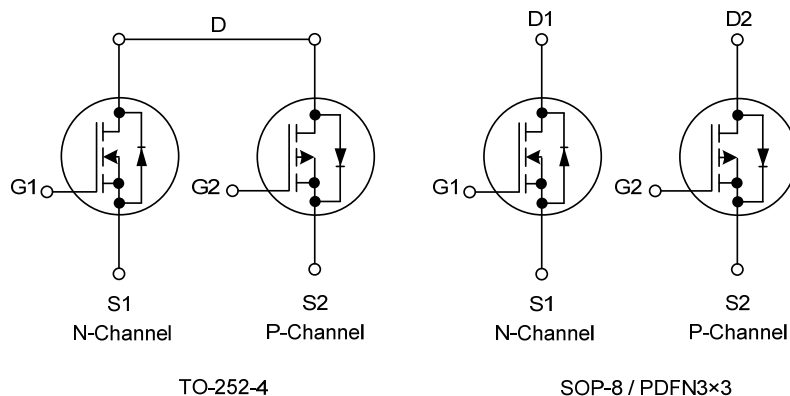
*P-CHANNEL

$$R_{DS(on)} \leq 73 \text{ m}\Omega @ V_{GS}=-10\text{V}, I_D=-10\text{A}$$

$$R_{DS(on)} \leq 150 \text{ m}\Omega @ V_{GS}=-4.5\text{V}, I_D=-10\text{A}$$

* High switching speed

SYMBOL



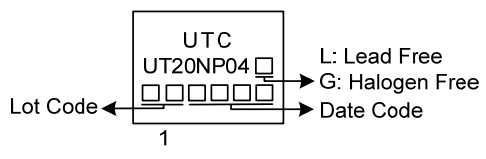
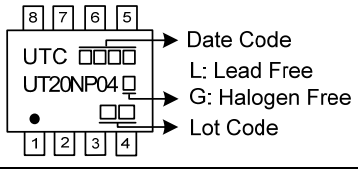
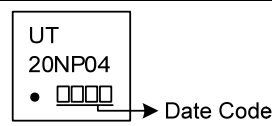
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UT20NP04L-TN4-R	UT20NP04G-TN4-R	TO-252-4	S1	G1	D	S2	G2	-	-	-	Tape Reel
UT20NP04L-S08-R	UT20NP04G-S08-R	SOP-8	S1	G1	S2	G2	D2	D2	D1	D1	Tape Reel
UT20NP04L-P3030-R	UT20NP04G-P3030-R	PDFN3×3	S1	G1	S2	G2	D2	D2	D1	D1	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

UT20NP04G-TN4-R (1)Packing Type (2)Package Type (3)Green Package		(1) R: Tape Reel (2) TN4: TO-252-4, S08: SOP-8, P3030: PDFN3×3 (3) G: Halogen Free and Lead Free, L: Lead Free
---	--	--

MARKING

PACKAGE	MARKING
TO-252-4	 Diagram showing the marking on a TO-252-4 package. The marking includes 'UTC', 'UT20NP04', a lead-free symbol (L), a halogen-free symbol (G), and a date code. The lot code is indicated by a box labeled '1'.
SOP-8	 Diagram showing the marking on an SOP-8 package. The marking includes 'UTC', 'UT20NP04', a lead-free symbol (L), a halogen-free symbol (G), and a date code. The lot code is indicated by a box labeled '1'.
PDFN3x3	 Diagram showing the marking on a PDFN3x3 package. The marking includes 'UT', '20NP04', and a date code.

■ ABSOLUTE MAXIMUM RATINGS ($T_C=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER			SYMBOL	RATINGS		UNIT
				N-CH	P-CH	
Drain-Source Voltage			V _{DSS}	40	-40	V
Gate-Source Voltage			V _{GSS}	±20	±20	V
Drain Current	Continuous	T _C =25°C	I _D	10	-10	A
	Pulsed		I _{DM}	20	-20	A
Avalanche Energy, Single Pulse			E _{AS}	3	26	mJ
Power Dissipation		TO-252-4	P _D	55		W
		SOP-8		2.1		W
		PDFN3×3		18		W
Junction Temperature			T _J	+150		°C
Range of Storage Temperature			T _{STG}	-55 ~ +150		°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature.

3. N-Channel: $L=0.1\text{mH}$, $I_{\text{AS}}=7.7\text{A}$, $V_{\text{DD}}=30\text{V}$, $R_{\text{G}}=25\Omega$, Starting $T_{\text{J}}=25^{\circ}\text{C}$

P-Channel: $L=0.1\text{mH}$, $I_{\text{AS}}=-22.9\text{A}$, $V_{\text{DD}}=-30\text{V}$, $R_{\text{G}}=25\Omega$, Starting $T_{\text{J}}=25^{\circ}\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-252-4	θ_{JA}	110	$^{\circ}\text{C/W}$
	SOP-8		125	$^{\circ}\text{C/W}$
	PDFN3 $\times$ 3		75	$^{\circ}\text{C/W}$
Junction to Case	TO-252-4	θ_{JC}	2.27 (Note)	$^{\circ}\text{C/W}$
	SOP-8		59.5 (Note)	$^{\circ}\text{C/W}$
	PDFN3 $\times$ 3		6.9(Note)	$^{\circ}\text{C/W}$

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

■ **ELECTRICAL CHARACTERISTICS** ($T_J=25^{\circ}\text{C}$, unless otherwise specified)

N-Channel

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =250μA, V _{GS} =0V	40			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =40V, V _{GS} =0V, T _J =25°C			1	μA
			V _{DS} =48V, V _{GS} =0V, T _J =125°C			10	μA
Gate-Source Leakage Current	Forward	I _{GSS}	V _{GS} =+20V			+100	nA
	Reverse		V _{GS} =-20V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	1.0		3.0	V
Static Drain-Source On-State Resistance (Note)		R _{DS(ON)}	V _{GS} =10V, I _D =10A			45	mΩ
			V _{GS} =4.5V, I _D =10A			65	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		C _{ISS}	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		385		pF
Output Capacitance		C _{OSS}			52		pF
Reverse Transfer Capacitance		C _{RSS}			45		pF
SWITCHING PARAMETERS							
Total Gate Charge (Note)		Q _G	V _{GS} =48V, V _{DS} =10V, I _D =10A, I _G =1mA		12		nC
Gate to Source Charge		Q _{GS}			1.8		nC
Gate to Drain Charge		Q _{GD}			2		nC
Turn-ON Delay Time (Note)		t _{D(ON)}	V _{DD} =20V, V _{GS} =10V, I _D =10A, R _G =25Ω		4		ns
Rise Time		t _R			16		ns
Turn-OFF Delay Time		t _{D(OFF)}			42		ns
Fall-Time		t _F			28		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Continuous Drain-Source Diode Forward Current		I _S				10	A
Maximum Pulsed Drain-Source Diode Forward Current		I _{SM}				20	A
Drain-Source Diode Forward Voltage (Note)		V _{SD}	I _S =10A, V _{GS} =0V			1.2	V

■ ELECTRICAL CHARACTERISTICS (Cont.)

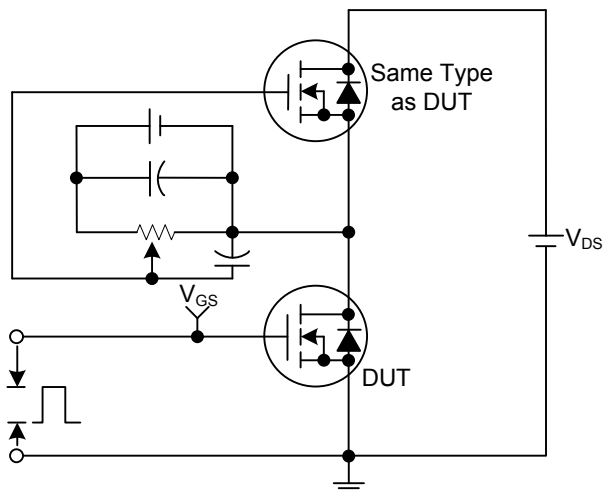
P-Channel

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =-250μA, V _{GS} =0V	-40			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =-40V, V _{GS} =0V, T _J =25°C			-1	μA
			V _{DS} =-48V, V _{GS} =0V, T _J =125°C			-10	μA
Gate-Source Leakage Current	Forward	I _{GSS}	V _{GS} =+20V			+100	nA
	Reverse		V _{GS} =-20V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =-250μA	-1.0		-3.0	V
Static Drain-Source On-State Resistance (Note)		R _{DS(ON)}	V _{GS} =-10V, I _D =-10A			73	mΩ
			V _{GS} =-4.5V, I _D =-10A			150	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		C _{ISS}	V _{GS} =0V, V _{DS} =-25V, f=1.0MHz		610		pF
Output Capacitance		C _{OSS}			95		pF
Reverse Transfer Capacitance		C _{RSS}			82		pF
SWITCHING PARAMETERS							
Total Gate Charge (Note)		Q _G	V _{GS} =-48V, V _{DS} =-10V, I _D =-10A, I _G =-1mA		17		nC
Gate to Source Charge		Q _{GS}			2.5		nC
Gate to Drain Charge		Q _{GD}			4		nC
Turn-ON Delay Time (Note)		t _{D(ON)}	V _{DD} =-20V, V _{GS} =-10V, I _D =-10A, R _G =3Ω		6		ns
Rise Time		t _R			16		ns
Turn-OFF Delay Time		t _{D(OFF)}			17		ns
Fall-Time		t _F			19		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Continuous Drain-Source Diode Forward Current		I _S				-10	A
Maximum Pulsed Drain-Source Diode Forward Current		I _{SM}				-20	A
Drain-Source Diode Forward Voltage (Note)		V _{SD}	I _S =-1.0A, V _{GS} =0V			-2.5	V

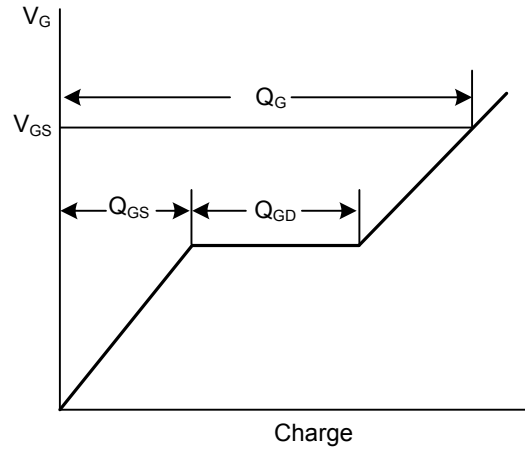
Note: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$.

■ TEST CIRCUITS AND WAVEFORMS

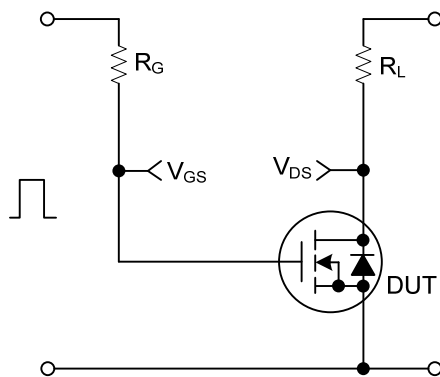
N-CHANNEL



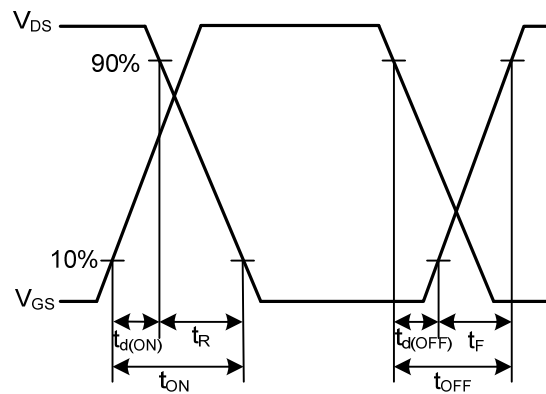
Gate Charge Test Circuit



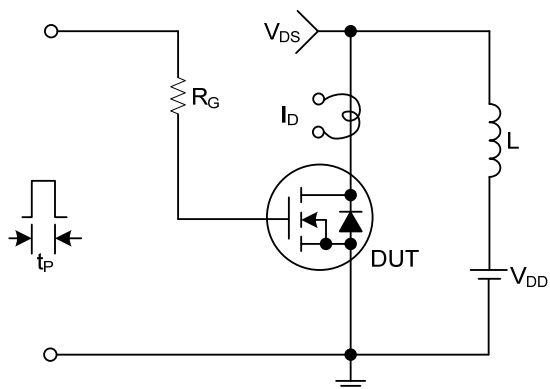
Gate Charge Waveforms



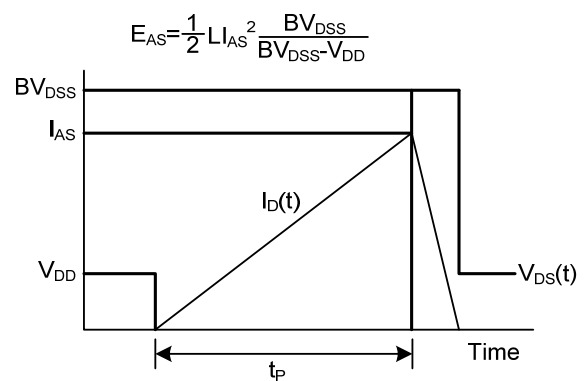
Resistive Switching Test Circuit



Resistive Switching Waveforms



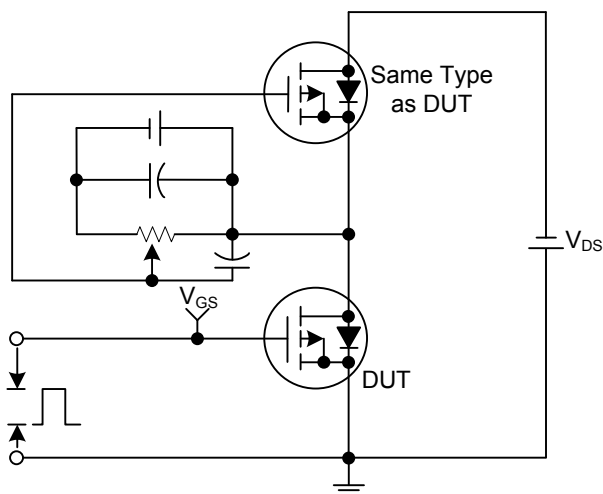
Unclamped Inductive Switching Test Circuit



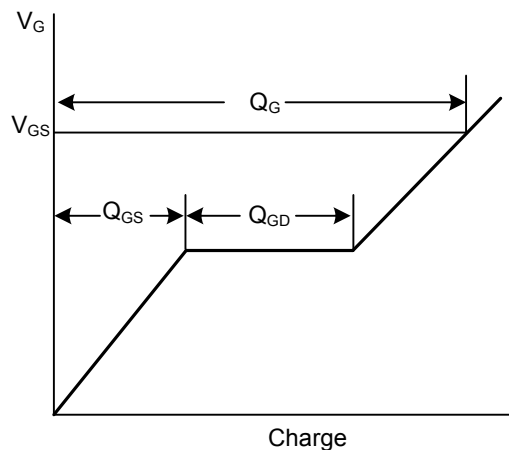
Unclamped Inductive Switching Waveforms

TEST CIRCUITS AND WAVEFORMS

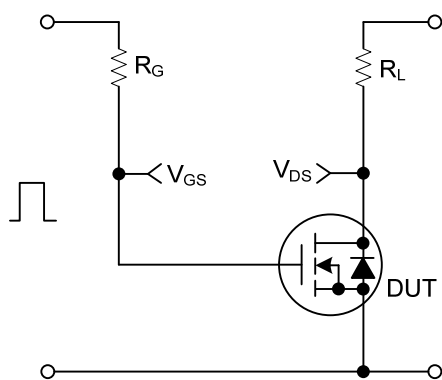
P-CHANNEL



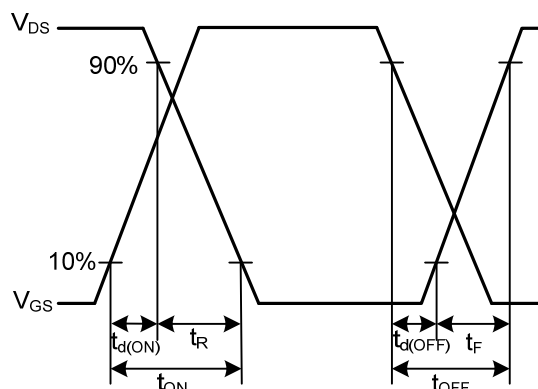
Gate Charge Test Circuit



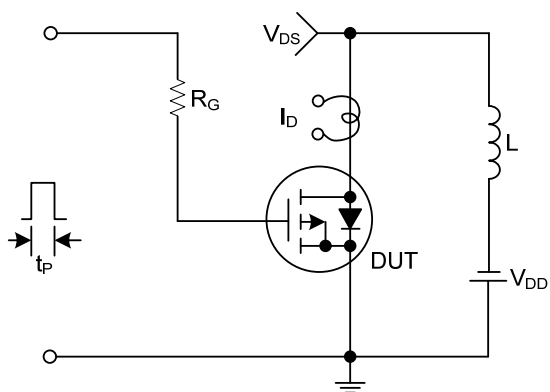
Gate Charge Waveforms



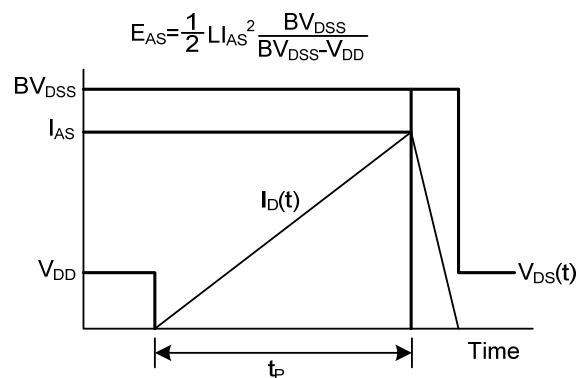
Resistive Switching Test Circuit



Resistive Switching Waveforms



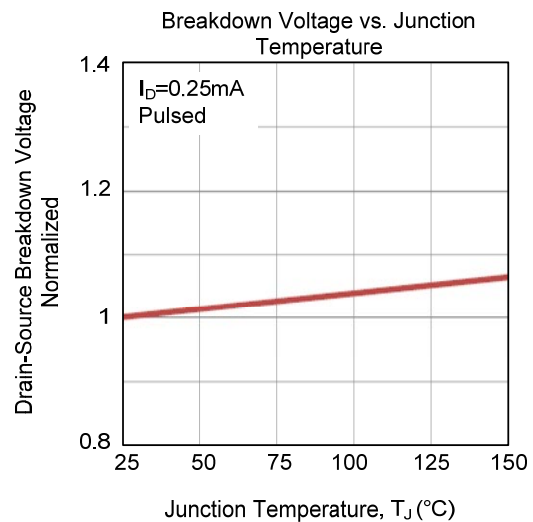
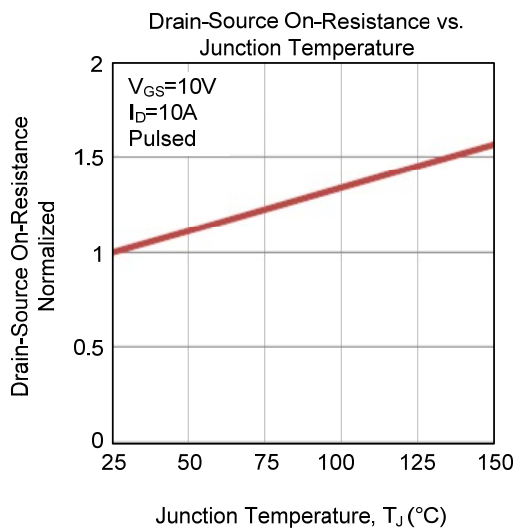
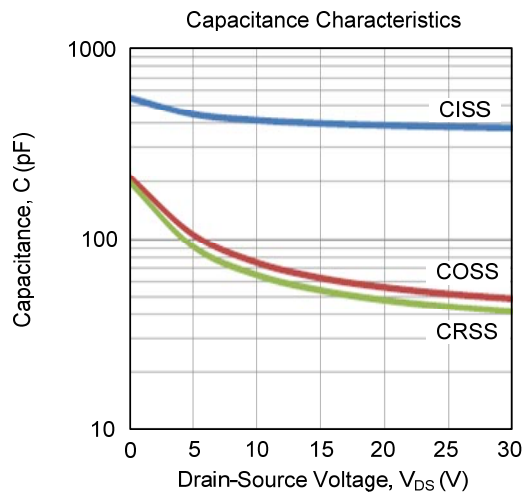
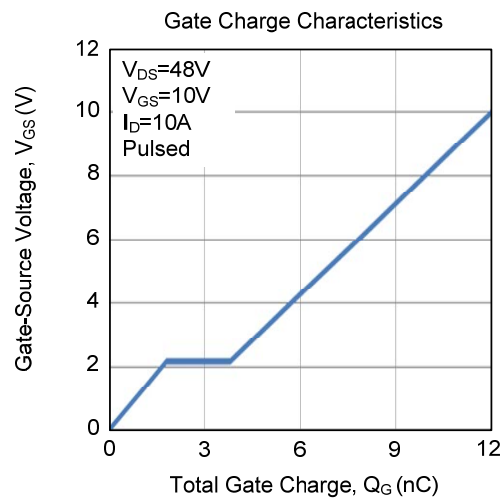
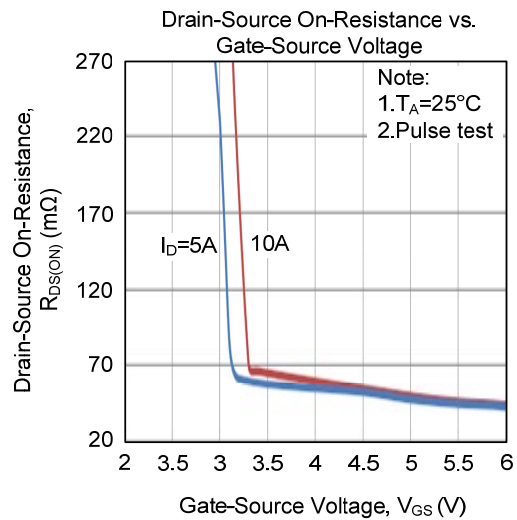
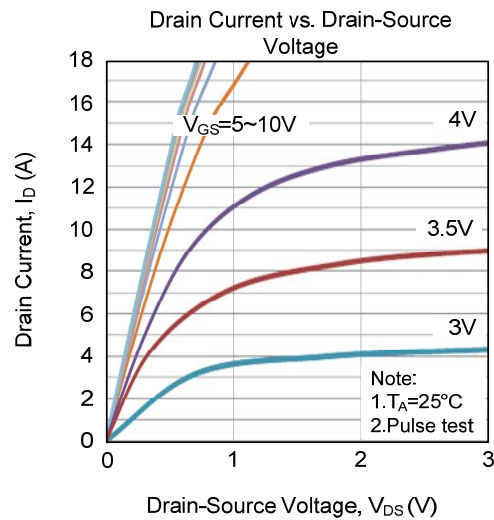
Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

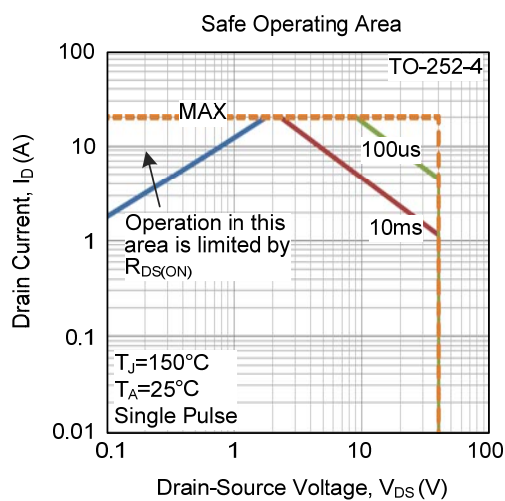
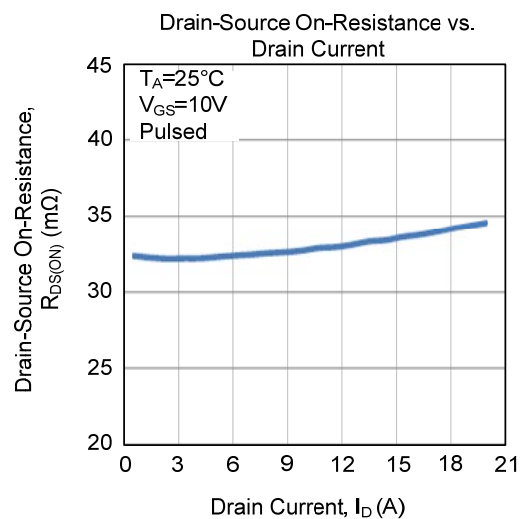
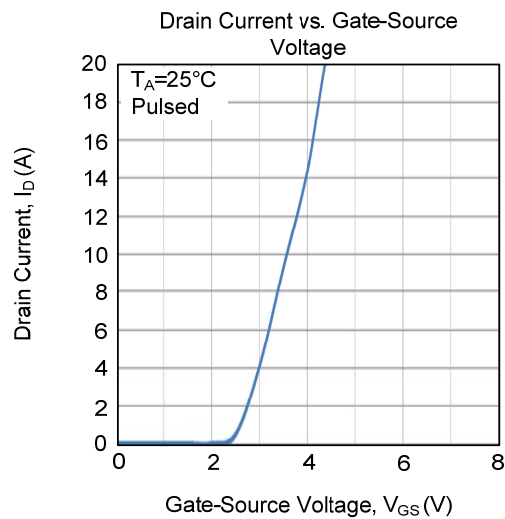
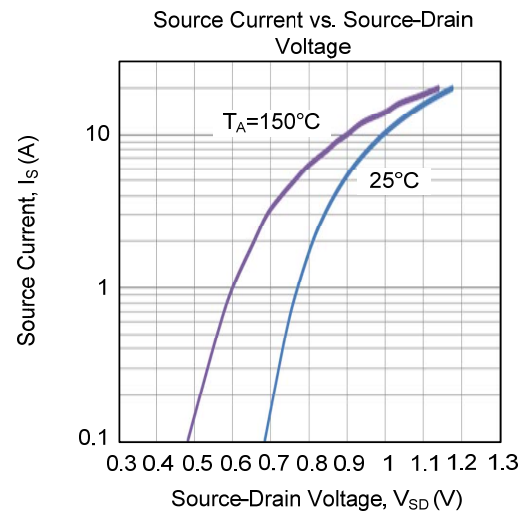
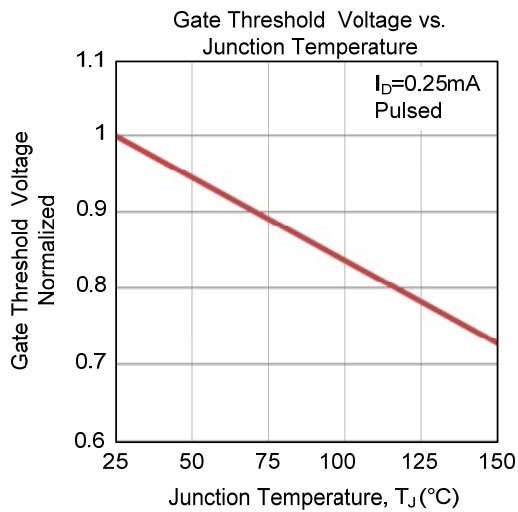
TYPICAL CHARACTERISTICS

N-CHANNEL



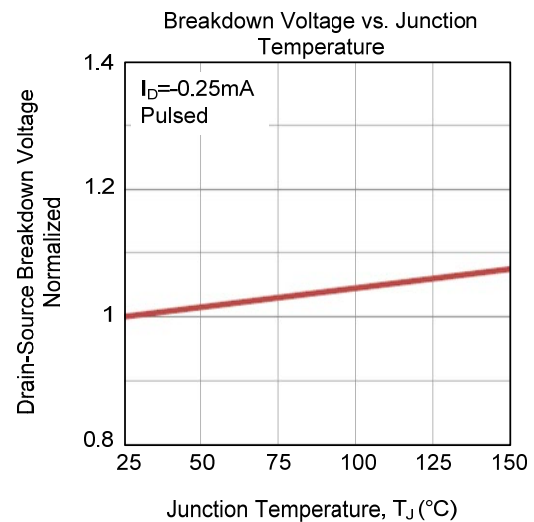
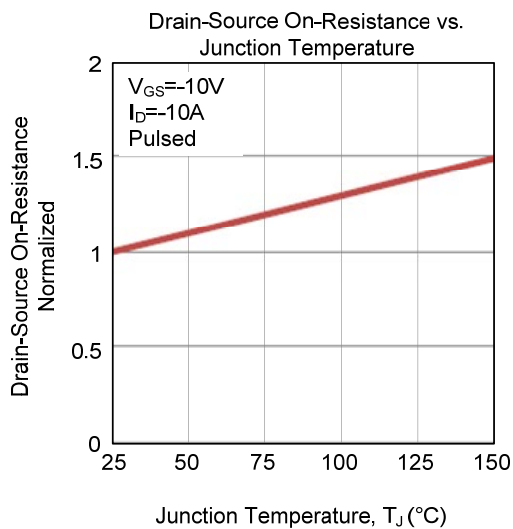
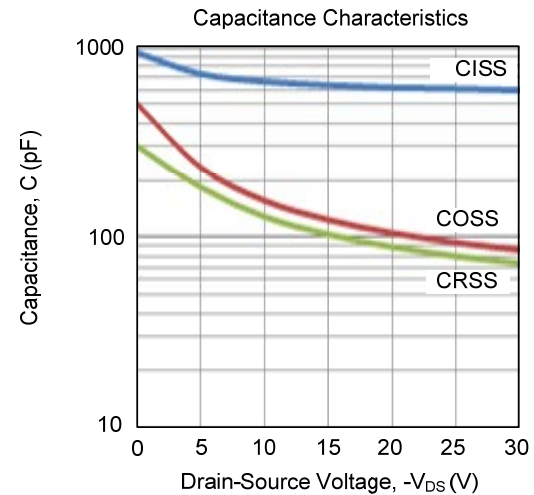
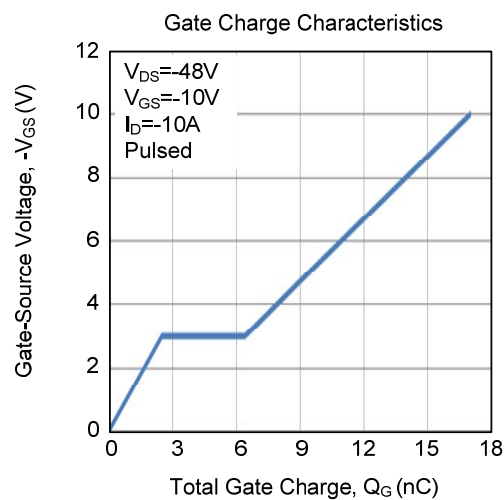
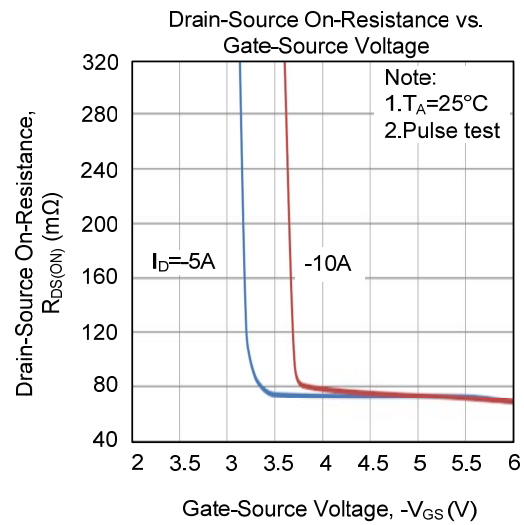
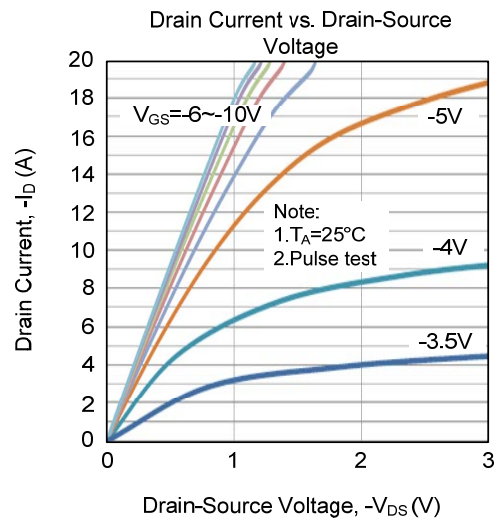
■ TYPICAL CHARACTERISTICS (Cont.)

N-CHANNEL



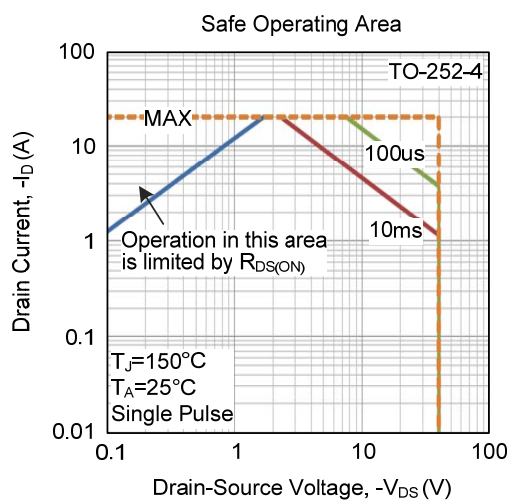
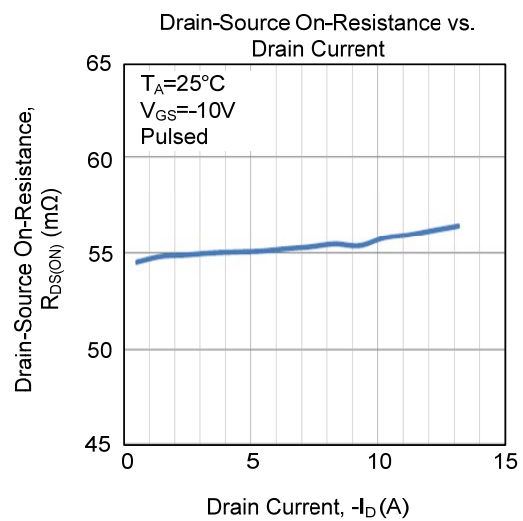
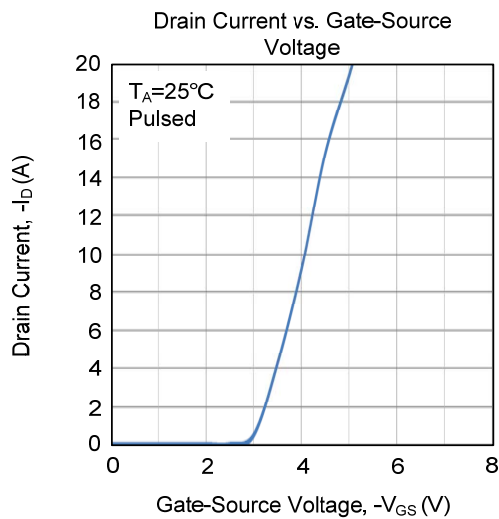
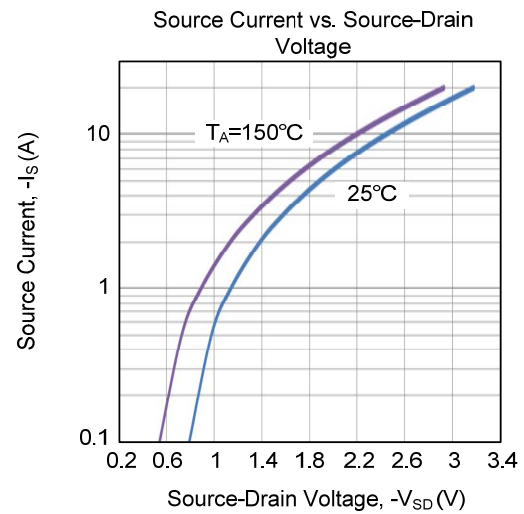
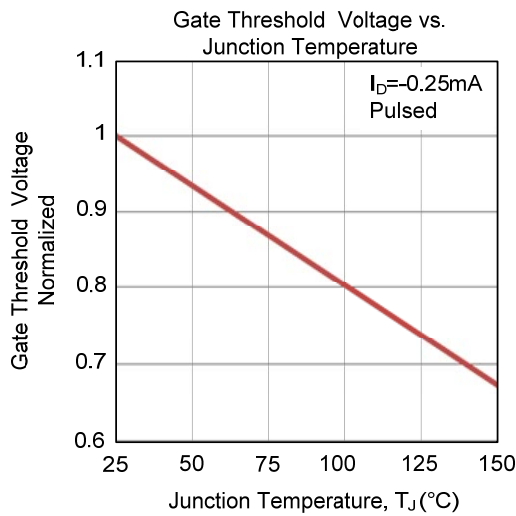
■ TYPICAL CHARACTERISTICS (Cont.)

P-CHANNEL

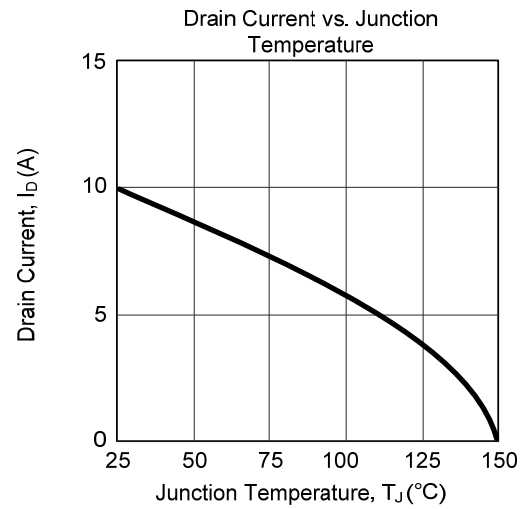
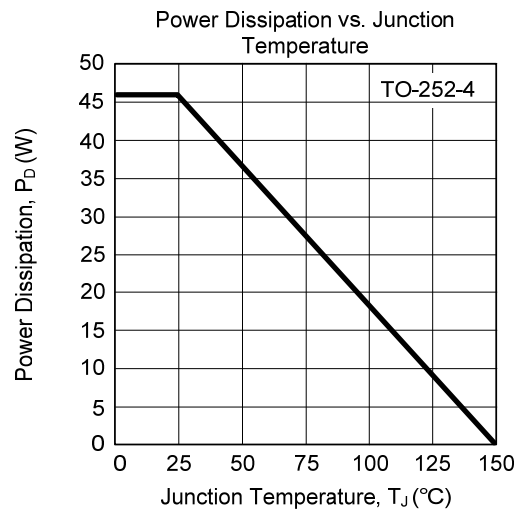


■ TYPICAL CHARACTERISTICS (Cont.)

P-CHANNEL



■ TYPICAL CHARACTERISTICS (Cont.)



UTC assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all UTC products described or contained herein. UTC products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. UTC reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.