

Preliminary

21A, 650V N-CHANNEL SUPER-JUNCTION MOSFET

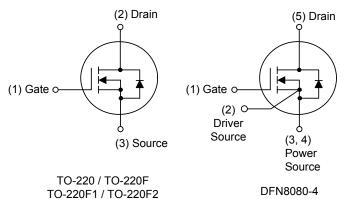
DESCRIPTION

The **UTC F21NM65** is a N-Channel enhancement mode silicon gate super junction power MOSFET with fast body diode and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

FEATURES

- * $R_{DS(ON)} \le 0.22 \ \Omega$ @ $V_{GS}=10V$, $I_D=10.5A$
- * Fast body diode MOSFET technology
- * High Switching Speed
- * 100% Avalanche Tested

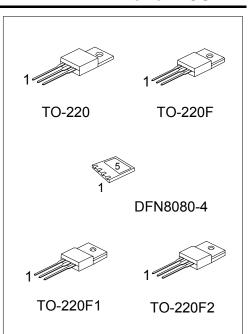
SYMBOL



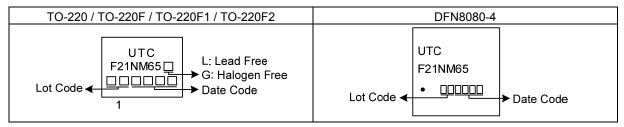
ORDERING INFORMATION

Ordering Number		Deckage	Pin Assignment					Deaking	
Lead Free	Halogen Free	Package 1 2 3 4		5	Packing				
F21NM65L-TA3-T	F21NM65G-TA3-T	TO-220	G	D	S	-	-	Tube	
F21NM65L-TF1-T	F21NM65G-TF1-T	TO-220F1	G	D	S	-	-	Tube	
F21NM65L-TF2-T	F21NM65G-TF2-T	TO-220F2	G	D	S	-	-	Tube	
F21NM65L-TF3-T	F21NM65G-TF3-T	TO-220F	G	D	S	-	-	Tube	
F21NM65L-K04-8080-R	5L-K04-8080-R F21NM65G-K04-8080-R		G	S	S	S	D	Tape Reel	
Note: Pin Assignment: G: Gate D: Drain S: Source									

F21NM65G-TA3-T (1)Packing Type (2)Package Type (3)Green Package	 (1) T: Tube, R: Tape Reel (2) TA3: TO-220, TF3: TO-220F, TF1: TO-220F1, TF2: TO-220F2, K04-8080: DFN8080-4 (3) G: Halogen Free and Lead Free, L: Lead Free 	
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MARKING





■ ABSOLUTE MAXIMUM RATINGS (T_c=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	650	V
Gate-Source Voltage		V _{GSS}	±30	V
Continuous Drain Current		I _D	21	А
Pulsed Drain Current (Note 2)		I _{DM}	42	А
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	277	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	12.5	V/ns
Power Dissipation	TO-220		125	W
	TO-220F/TO-220F1 TO-220F2	P _D	34	W
	DFN8080-4		65	W
Junction Temperature		TJ	+150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L = 30mH, I_{AS} = 4.3A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25°C

4. $I_{SD} \le 21A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F/ TO-220F1/TO-220F2	θ _{JA}	62.5	°C/W
	DFN8080-4		35 (Note)	°C/W
Junction to Case	TO-220		1	°C/W
	TO-220F/TO-220F1 TO-220F2	θ _{JC}	3.67	°C/W
	DFN8080-4		1.92 (Note)	°C/W

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.



■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

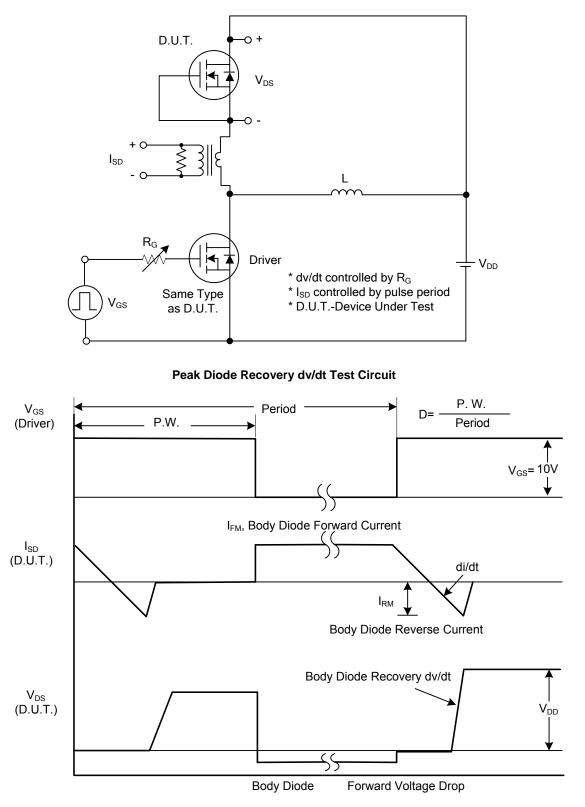
		SYMBOL		MIN	TYP	MAX	UNIT
PARAMETER OFF CHARACTERISTICS	STIVIBUL	TEST CONDITIONS	IVIIIN		IVIAA		
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250µA	650			V	
				000		10	-
Drain-Source Leakage Current		I _{DSS}	V_{DS} =650V, V_{GS} =0V				μA
Gate- Source Leakage Current	Forward	I _{GSS}	V_{GS} =30V, V_{DS} =0V			100 -100	nA nA
ON CHARACTERISTICS	Reverse		V _{GS} =-30V, V _{DS} =0V			-100	nA
		Manager	V	2.5		4.5	V
Gate Threshold Voltage Static Drain-Source On-State Resistance		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250µA V _{GS} =10V, I _D =10.5A	2.0		0.22	Ω
DYNAMIC CHARACTERISTICS	Slance	R _{DS(ON)}	$V_{GS} = 10V, I_D = 10.5A$			0.22	Ω
		C	1		1570		ъĘ
Input Capacitance		CISS					pF
Output Capacitance		C _{OSS}	V _{DS} =25V, V _{GS} =0V, f=1.0MHz		965		pF
Reverse Transfer Capacitance		C_{RSS}			96		pF
SWITCHING CHARACTERISTICS		_	1		I	1	
Total Gate Charge (Note 1)		Q_{G}	V _{DS} =520V, V _{GS} =10V, I _D =21A I _G =1mA (Note 1, 2)		55.7		nC
Gate-Source Charge		Q_{GS}			12		nC
Gate-Drain Charge		Q_{GD}	······································		24		nC
Turn-On Delay Time (Note 1)		t _{D(ON)}			25		ns
Turn-On Rise Time		t _R	V_{DS} =100V, V_{GS} =10V, I_{D} =21A,		35		ns
Turn-Off Delay Time		$t_{D(OFF)}$	R _G =25Ω (Note 1, 2)		180		ns
Turn-Off Fall Time		t⊢			82		ns
DRAIN-SOURCE DIODE CHARAC	TERISTICS	AND MAXI	MUM RATINGS				
Maximum Continuous Drain-Source Diode						01	•
Forward Current		I _S				21	А
Maximum Pulsed Drain-Source Diode Forward		I				40	^
Current		I _{SM}				42	A
Drain-Source Diode Forward Voltage (Note 1)		V _{SD}	I _S =21A , V _{GS} =0V			1.4	V
Reverse Recovery Time (Note 1)		t _{rr}	I _S =21A , V _{GS} =0V		170		ns
Reverse Recovery Charge		Qrr	di/dt=100A/µs		2.4		μC
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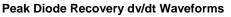
Notes: 1. Pulse Test: Pulse width \leq 300µs, Duty cycle \leq 2%.

2. Essentially independent of operating temperature.



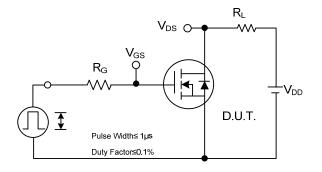
TEST CIRCUITS AND WAVEFORMS

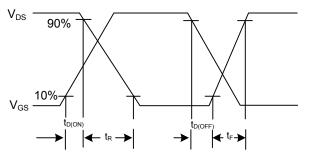




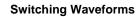


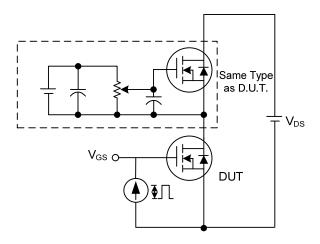
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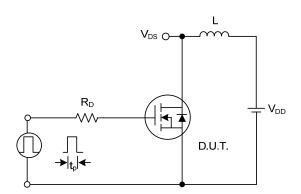


Switching Test Circuit

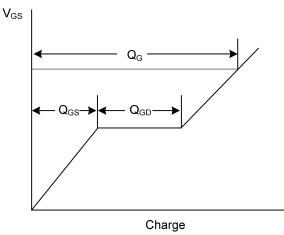




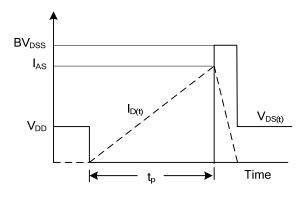
Gate Charge Test Circuit



Unclamped Inductive Switching Test Circuit







Unclamped Inductive Switching Waveforms



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