

UTC UNISONIC TECHNOLOGIES CO., LTD

Preliminary

LR2128

LINEAR INTEGRATED CIRCUIT

300mA SELECTABLE FIXED/ADJUSTABLE LOW DROPOUT LINEAR REGULATOR

DESCRIPTION

As a low dropout linear regulator, the UTC LR2128 only needs low input voltage (2.7~6V) and can deliver current to 300mA for setting the output voltage.

The UTC LR2128 is an ideal for being used in such battery-powered equipments notebook, personal computer and cellular phone. Its typical dropout voltage is 230mV at loading current 300mA.

For setting the output voltage, the UTC LR2128 has two output voltage operation modes: fixed mode senses the output voltage on V_{OUT}, ADJ mode needs two resistors as a voltage divider.

To protect itself against current over-loads and over temperature, the UTC LR2128 has current limit and thermal shutdown functions.

FEATURES

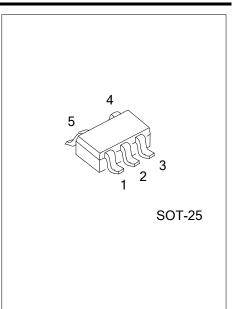
- * Operating Voltage: 2.7~6V
- * Low Voltage Dropout
- * Output Current Guaranteed 300mA
- * For Setting Output Voltage Two Modes -Fixed mode :Fixed Output Voltage 1~5V
- -ADJ mode: Adjustable Output Voltage 0.8~5.5V
- * Internal Current Limit Protection
- * With Soft-Start
- * Internal thermal Protection
- * Work stably with Low ESR Ceramics Capacitor

ORDERING INFORMATION

Ordering Number		Dookogo	Dealing	
Lead Free	Halogen Free	Package	Packing	
LR2128AL-xx-AF5-R	LR2128AG-xx-AF5-R	SOT-25	Tape Reel	
LR2128BL-xx-AF5-R	LR2128BG-xx-AF5-R	SOT-25	Tape Reel	

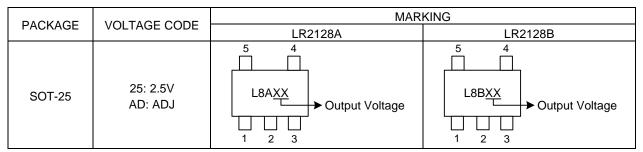
Note: xx: Output Voltage, Refer to Marking Information.

LR2128AG-xx-AF5-R (1)Packing Type (2)Package Type (3)Output Voltage Code (4)Green Package (5)Pin Configuration	 (1) R: Tape Reel (2) AF5: SOT-25 (3) xx: refer to Marking Information (4) G: Halogen Free and Lead Free, L: Lead Free (5) refer to PIN CONFIGURATION
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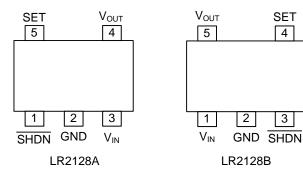


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MARKING INFORMATION



■ PIN CONFIGURATION



■ PIN DESCRIPTION

PIN NO			DECODIDITION				
LR2128A	LR2128B	PIN NAME	DESCRIPTION				
			Control pin for shutdown				
1	3	SHDN	HDN Logic High: enable				
			Logic Low: shutdown				
2	2	GND	Ground				
3	1	V _{IN}	Voltage supply				
4	5	V _{OUT}	Output pin				
5	4	SET	When this pin is connected to ground, turns to fixed output voltage operation for LR2128-XX. When this pin is connected to an external resistor divider, turns to adjustable output voltage mode operation for LR2128-AD.				

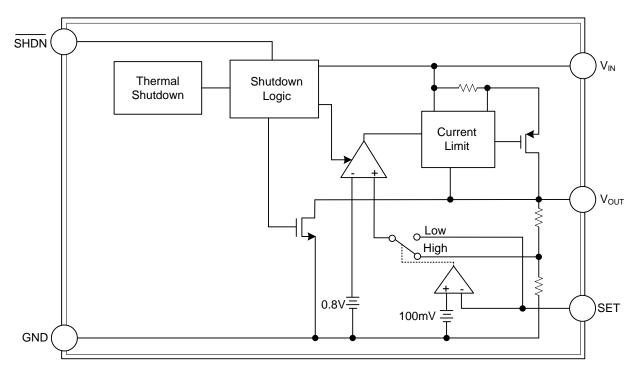


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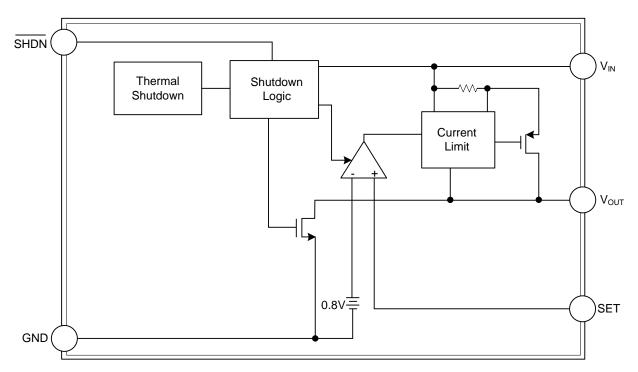
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BLOCK DIAGRAM

For LR2128-XX



For LR2128-AD





■ ABSOLUTE MAXIMUM RATING (T_A=25°C, Unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
V _{IN} Supply Voltage (V _{IN} to GND)	V _{IN}	-0.3 ~ +6.5	V
SHDN Input Voltage (SHDN to GND)	V	-0.3 ~ +6.5	V
Power Dissipation	P _D	380	mW
Junction Temperature	TJ	-40 ~ +125	°C
Storage Temperature	T _{STG}	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	RATINGS	UNIT	
V _{IN} Supply Voltage	V _{IN}	2.7 ~ 6	V	
Output Voltage	V _{OUT}	0.8 ~ 5.5	V	
Vout Output Current	Ι _{Ουτ}	0 ~ 300	mA	
Input Capacitor	C _{IN}	0.22 ~ 100	μF	
Output Capacitor	COUT	1.5 ~ 33	μF	
Junction Temperature	T _{OPR}	-40 ~ +85	°C	

■ ELECTRICAL CHARACTERISTICS

(VIN=VOUT+1V (min VIN=2.8V), IOUT=0~300mA, CIN=1µF, COUT=2.2µF, TA=25°C, unless otherwise specified)

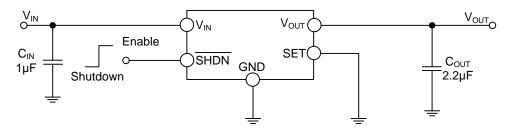
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MÁX	UNIT
Output Voltage	V _{OUT}		0.8		5.5	V
Input Voltage	V _{IN}		2.7		6	V
Line Regulation	$\frac{\Delta V_{OUT}}{\Delta V_{IN} \times V_{OUT}}$	$\triangle V_{OUT}\%/ \triangle V_{IN}$, I _{OUT} =10mA	-0.07		+0.07	%/V
Load Regulation	ΔV _{OUT} V _{OUT}	∆Vout%/Vout	-0.4		+0.4	%
Output Voltage Accuracy		Fixed output voltage, I _{OUT} =10mA	-2		+2	%
Reference Voltage	V_{REF}	Measured on SET, V _{IN} =2.8V, I _{OUT} =10mA	0.784	0.8	0.816	V
Quiescent Current	lq	I _{OUT} =10mA~300mA		90	200	μA
Dropout Voltage	V _D	V _{OUT} =2.5V, I _{OUT} =300mA		230	360	mV
		V _{OUT} =3.3V, I _{OUT} =300mA		170	300	mV
Power Supply Ripple Rejection Ratio	PSRR	f=10kHz, I _{OUT} =300mA		45		dB
Output Voltage Noise	eN	f=80Hz~100KHz, I _{OUT} =300mA		160		μV_{RMS}
Current Limit	ILIMIT		300			mA
Chutdour Threehold	VIH		1.6			V
Shutdown Threshold	VIL				0.4	V
Shutdown Supply Current	I _{OFF}	SHDN=Low, V _{IN} =6V		0.1	1	μA
V_{OUT} Discharge MOSFET $R_{DS(ON)}$		SHDN=Low		60		Ω
Thermal Shutdown Temperature	T _{SHDN}	I _{OUT} =10mA		150		°C
Thermal Shutdown Hysteresis	DT _{SHDN}	I _{OUT} =10mA		20		°C
SET Input Threshold for Fixed/Adjustable Output Voltage Mode		LR2128-XX		80		mV
SET Input Bias Current			-100		100	nA
Soft-Start Interval	T _{SS}			60		μs



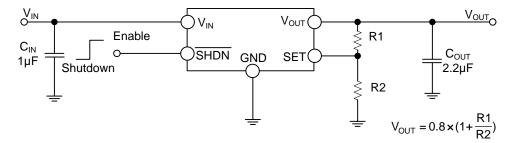
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TYPICAL APPLICATION CIRCUIT

For Fixed Output Voltage Mode (For LR2128-XX)



For Adjustable Output Voltage Mode (For LR2128-AD)



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