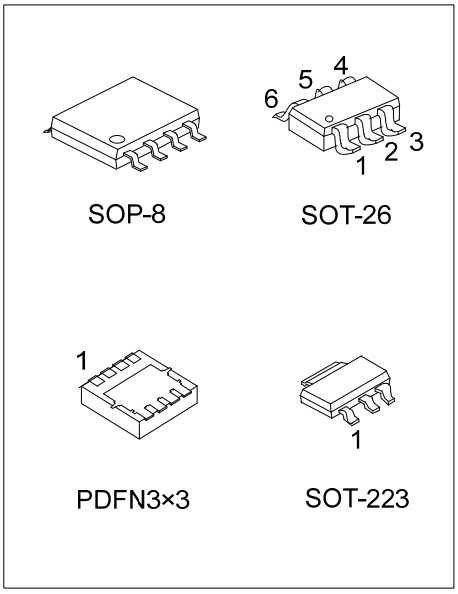
UT5N15

POWER MOSFET

5.0A, 150V N-CHANNEL POWER MOSFET



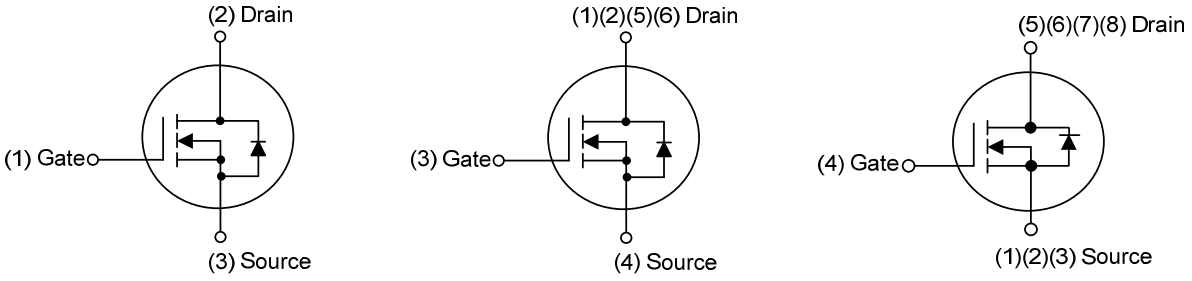
■ DESCRIPTION

The UTC **UT5N15** is a high voltage power MOSFET combines advanced trench MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications of switching power supplies and adaptors.

■ FEATURES

* $R_{DS(ON)} \leq 0.32\ \Omega$ @ V_{GS}=10V, I_D=2.5A
* Fast switching capability
* Avalanche energy tested
* Improved dv/dt capability, high ruggedness

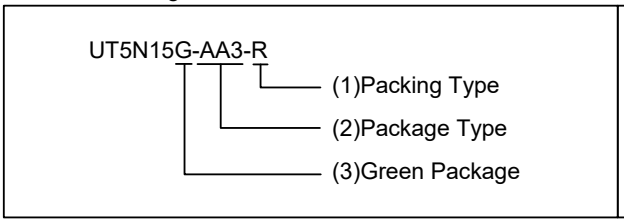
■ SYMBOL



■ ORDERING INFORMATION

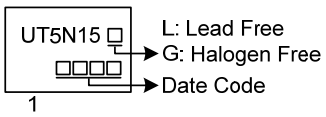
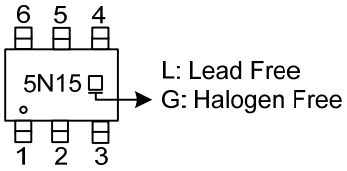
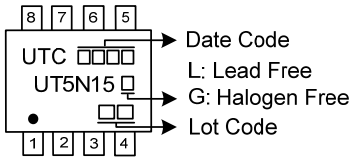
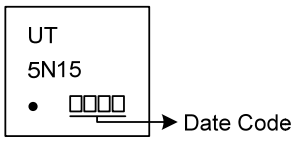
Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UT5N15L-AA3-R	UT5N15G-AA3-R	SOT-223	G	D	S	-	-	-	-	-	Tape Reel
UT5N15L-AG6-R	UT5N15G-AG6-R	SOT-26	D	D	G	S	D	D	-	-	Tape Reel
UT5N15L-S08-R	UT5N15G-S08-R	SOP-8	S	S	S	G	D	D	D	D	Tape Reel
UT5N15L-P3030-R	UT5N15G-P3030-R	PDFN3×3	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source



(1) R: Tape Reel
(2) AA3: SOT-223, AG6: SOT-26, S08: SOP-8
P3030: PDFN3×3
(3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING

SOT-223	SOT-26
	
SOP-8	**PDFN3×3**
	

■ ABSOLUTE MAXIMUM RATING (T_A=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	150	V
Gate-Source Voltage		V_{GSS}	±20	V
Drain Current	Continuous	I_D	5	A
	Pulsed	I_{DM}	10	A
Avalanche Energy	Single Pulsed (Note 4)	E_{AS}	1.2	mJ
Peak Diode Recovery dv/dt (Note 5)		dv/dt	1.2	V/ns
Power Dissipation (Note 3)	SOT-223	P_D	1.2	W
	SOT-26		0.4	W
	SOP-8		1	W
	PDFN3×3		1.4	W
Junction Temperature		T_J	+150	°C
Storage Temperature Range		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. Surface mounted on 1 in² copper pad of FR-4 board. 270°C/W when mounted on minimum copper pad.
4. L = 0.1mH, I_{AS} = 4.8A, V_{DD} = 20V, R_G = 25 Ω, Starting T_J = 25°C
5. I_{SD} ≤ 5.0 A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	SOT-223	θ_{JA}	104	°C/W
	SOT-26		350	°C/W
	SOP-8		125	°C/W
	PDFN3×3		89	°C/W

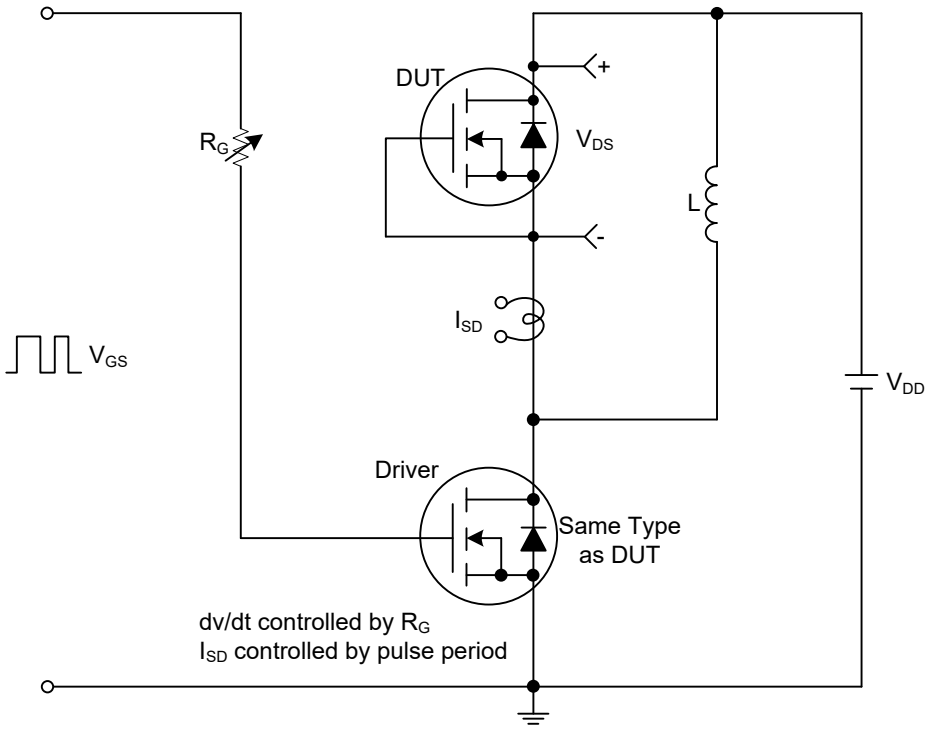
Note: Device mounted on FR-4 substrate P_C board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

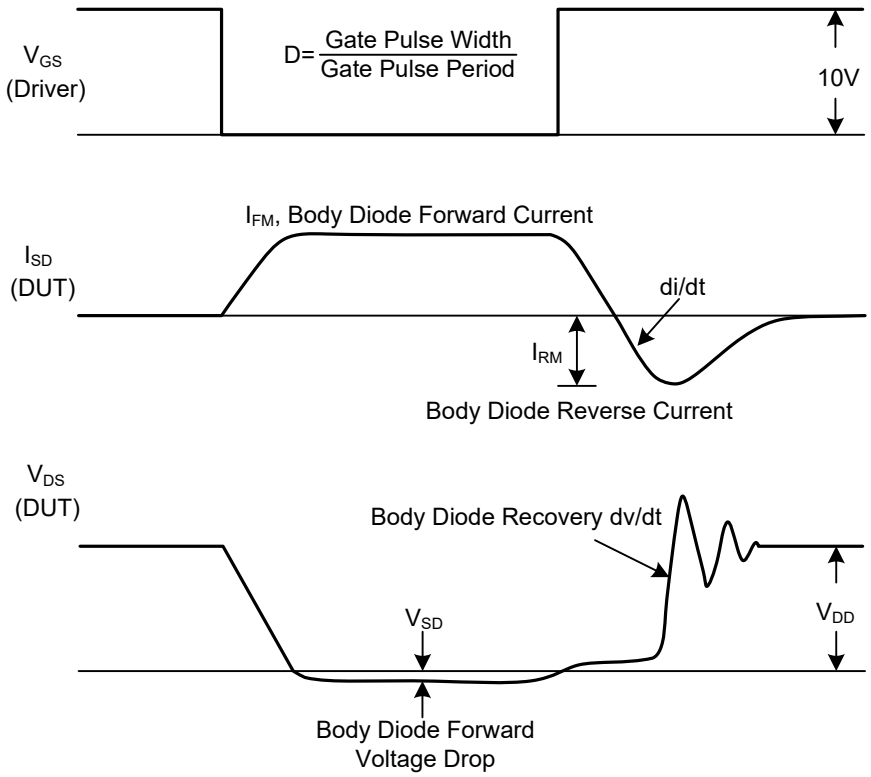
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	I_D=250μA, V_{GS}=0V	150			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=150V, V_{GS}=0V			10	μA
Gate-Source Leakage Current	Forward	I_{GSS}	V_{GS}=+20V, V_{DS}=0V			+100	nA
	Reverse		V_{GS}=-20V, V_{DS}=0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=2.5A			0.32	Ω
DYNAMIC PARAMETERS							
Input Capacitance		C_{ISS}	V_{GS}=0V, V_{DS}=25V, f=1.0MHz		380		pF
Output Capacitance		C_{OSS}			40		pF
Reverse Transfer Capacitance		C_{RSS}			22		pF
SWITCHING PARAMETERS							
Total Gate Charge (Note 1)		Q_G	V_{DS}=120V, V_{GS}=10V, I_D=5.0A I_G=1mA (Note 1, 2)		16.2		nC
Gate to Source Charge		Q_{GS}			4.2		nC
Gate to Drain Charge		Q_{GD}			4.4		nC
Turn-on Delay Time (Note 1)		$t_{D(ON)}$	V_{DD}=100V, V_{GS}=10V, I_D=5.0A, R_G =25Ω (Note 1, 2)		6		ns
Rise Time		t_R			18		ns
Turn-off Delay Time		$t_{D(OFF)}$			24		ns
Fall-Time		t_F			23		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I_S				5	A
Maximum Body-Diode Pulsed Current		I_{SM}				10	A
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	I_S=5.0A, V_{GS}=0V			1.4	V
Reverse Recovery Time		t_{rr}	I_S=5.0A, V_{GS}=0V, d_{IF}/d_t=100A/μs		54		ns
Reverse Recovery Charge		Q_{rr}			90		nC

Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.
2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS



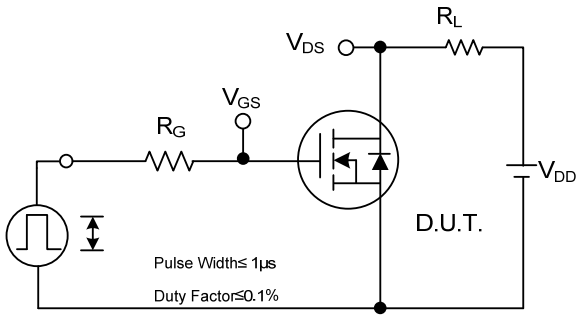
Peak Diode Recovery dv/dt Test Circuit



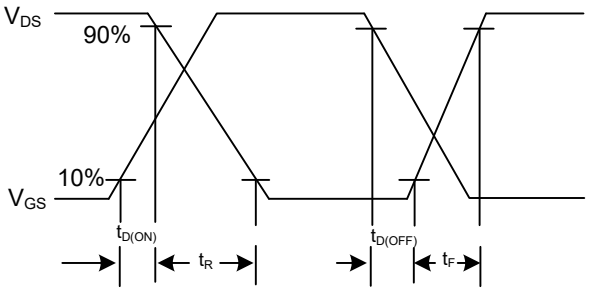
Peak Diode Recovery dv/dt Test Circuit and Waveforms

Peak Diode Recovery dv/dt Waveforms

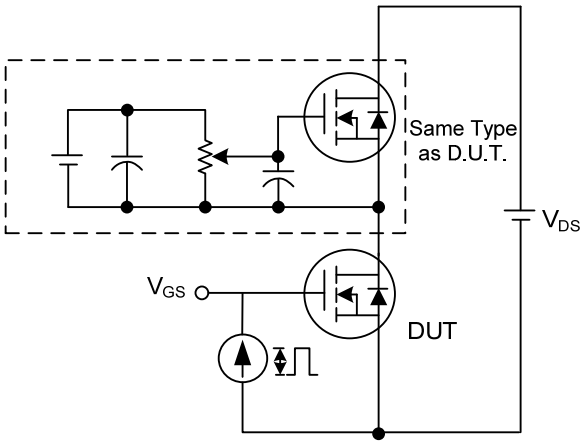
■ TEST CIRCUITS AND WAVEFORMS



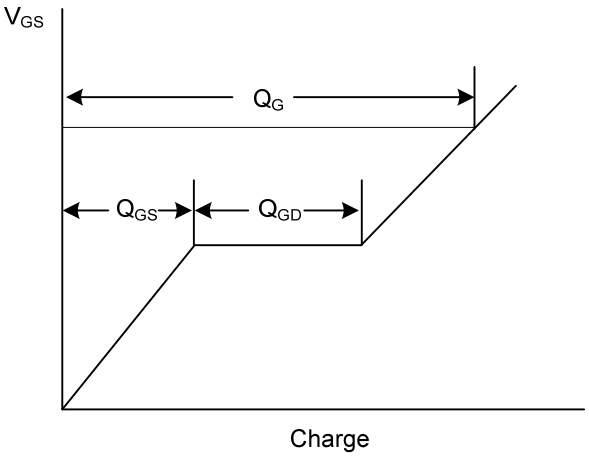
Switching Test Circuit



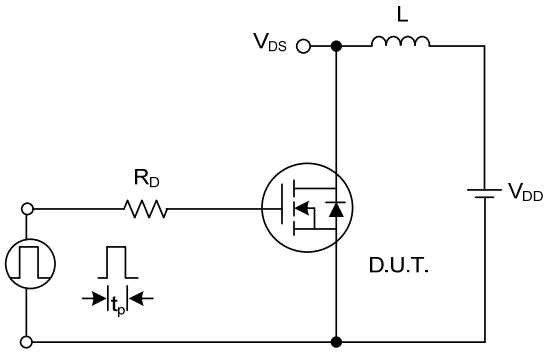
Switching Waveforms



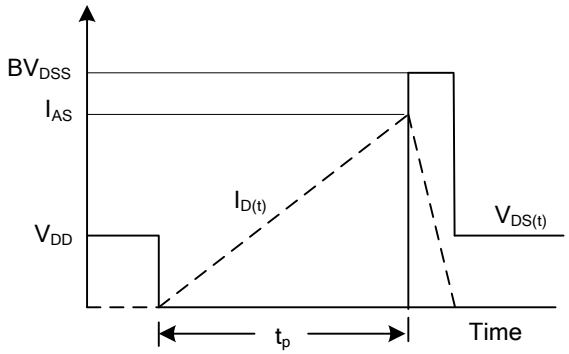
Gate Charge Test Circuit



Gate Charge Waveform

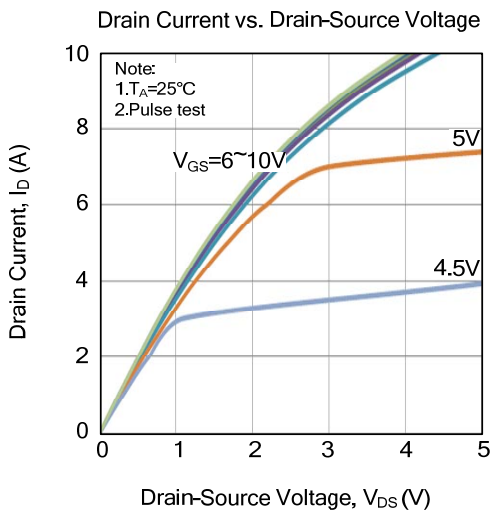


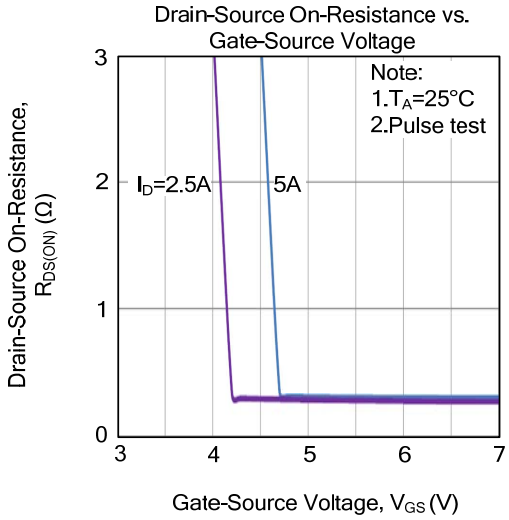
Unclamped Inductive Switching Test Circuit

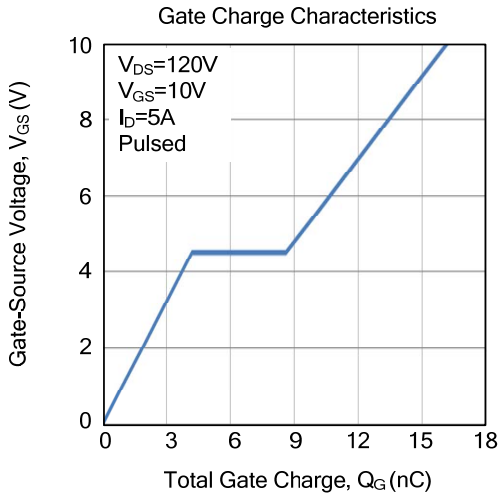


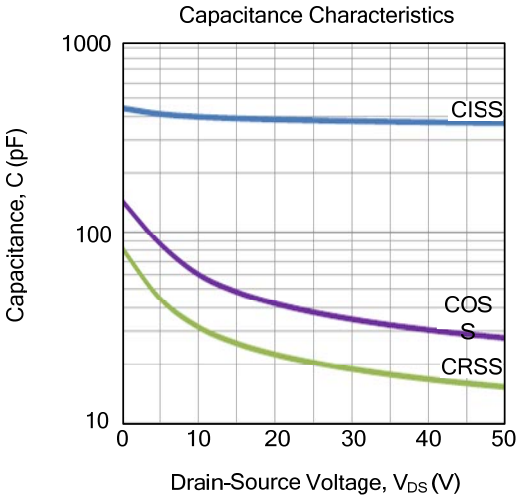
Unclamped Inductive Switching Waveforms

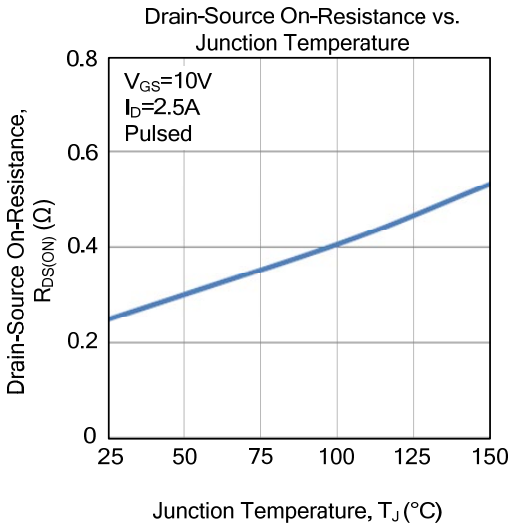
■ TYPICAL CHARACTERISTICS

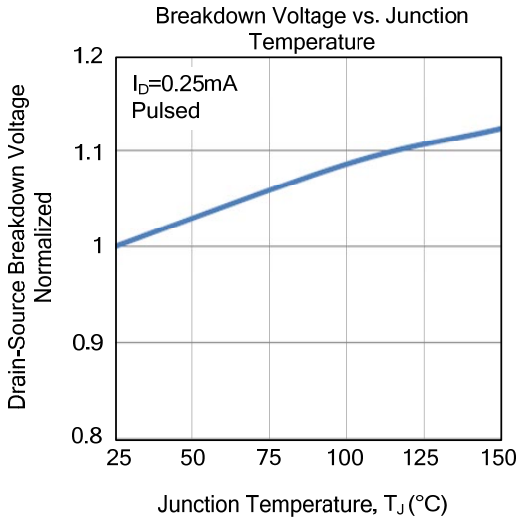






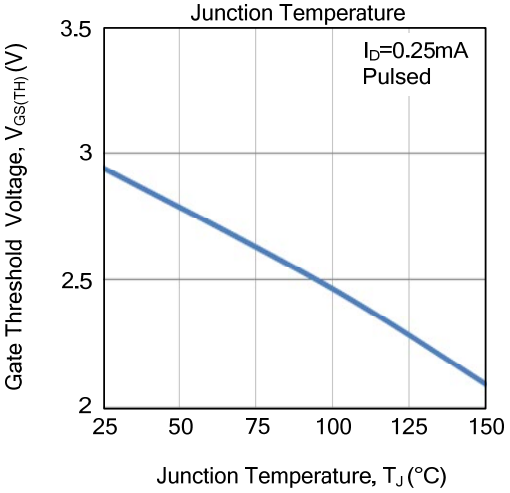




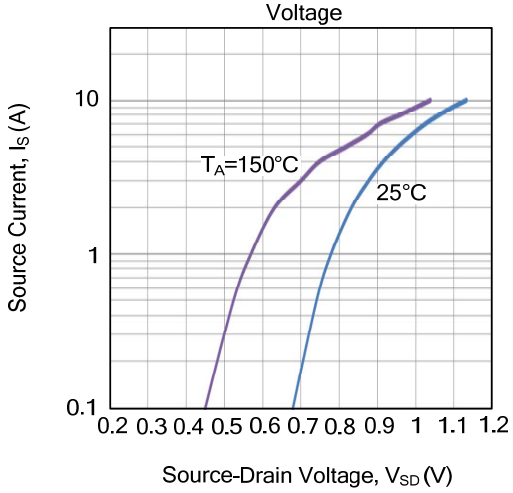


■ TYPICAL CHARACTERISTICS (Cont.)

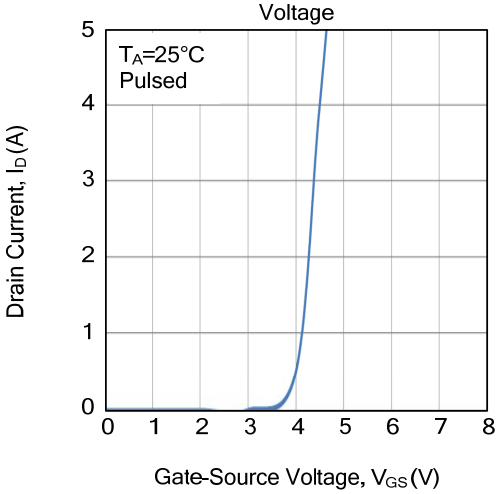
Gate Threshold Voltage vs. Junction Temperature



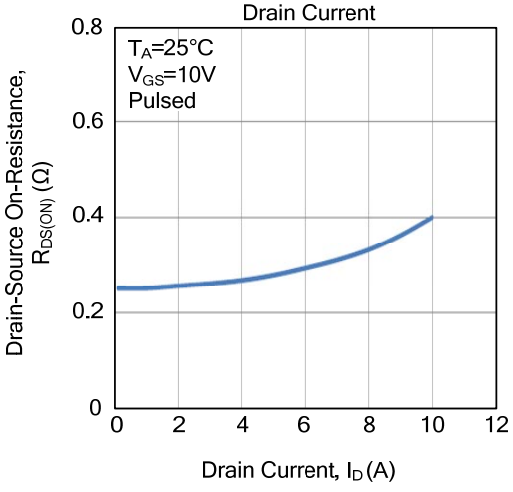
Source Current vs. Source-Drain Voltage



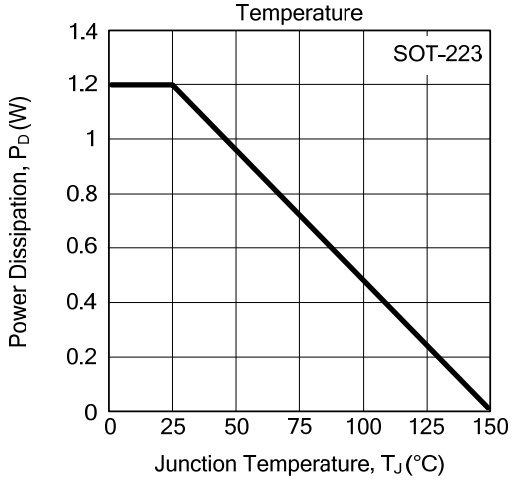
Drain Current vs. Gate-Source Voltage



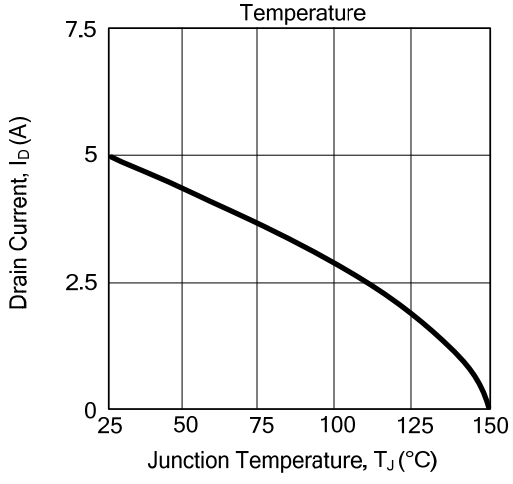
Drain-Source On-Resistance vs. Drain Current



Power Dissipation vs. Junction Temperature



Drain Current vs. Junction Temperature



■ TYPICAL CHARACTERISTICS (Cont.)