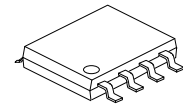


**UT24NN04****Power MOSFET****24A, 40V DUAL N-CHANNEL
ENHANCEMENT MODE
POWER MOSFET****DESCRIPTION**

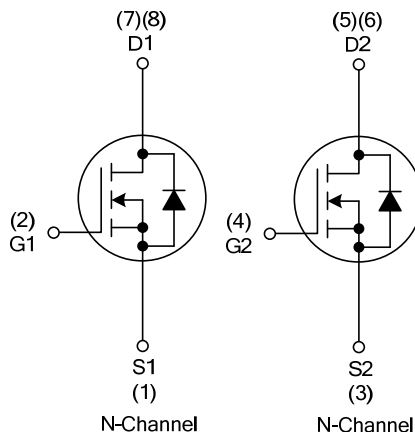
The UTC **UT24NN04** is a N-channel Power MOSFET, it uses UTC's advanced technology to provide the customers with low $R_{DS(ON)}$ characteristic by high cell density trench technology.

FEATURES

- * $R_{DS(ON)} \leq 15 \text{ m}\Omega$ @ $V_{GS}=10\text{V}$, $I_D=12\text{A}$
- * $R_{DS(ON)} \leq 20 \text{ m}\Omega$ @ $V_{GS}=4.5\text{V}$, $I_D=12\text{A}$
- * Fast Switching Speed
- * Simple Drive Requirement



SOP-8

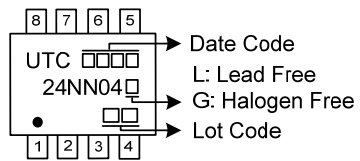
SYMBOL**ORDERING INFORMATION**

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	15	
UT24NN04L-S08-R	UT24NN04G-S08-R	SOP-8	S1	G1	S2	G2	D2	D2	D1	D1	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

UT24NN04G-S08-R		(1) Packing Type	(1) R: Tape Reel
		(2) Package Type	(2) S08: SOP-8
		(3) Green Package	(3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_A=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	40	V
Gate-Source Voltage		V_{GSS}	± 20	V
Drain Current	Continuous	I_D	24	A
	Pulsed (Note 2)	I_{DM}	48	A
Single Pulse Avalanche Energy		E_{AS}	11	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	7.8	V/ns
Power Dissipation ($T_C=25^{\circ}\text{C}$)		P_D	2	W
Junction Temperature		T_J	+150	$^{\circ}\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^{\circ}\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature.

3. $L=0.1\text{mH}$, $I_{AS}=15\text{A}$, $V_{DD}=30\text{V}$, $R_G=25\Omega$, Starting $T_J = 25^{\circ}\text{C}$

4. $I_{SD}\leq 24\text{A}$, $di/dt\leq 200\text{A}/\mu\text{s}$, $V_{DD}\leq BV_{DSS}$, Starting $T_J = 25^{\circ}\text{C}$

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	125	$^{\circ}\text{C}/\text{W}$
Junction to Case	θ_{JC}	60	$^{\circ}\text{C}/\text{W}$

Note: Device mounted on FR-4 substrate P_c board, 2oz copper, with 1inch square copper plate.

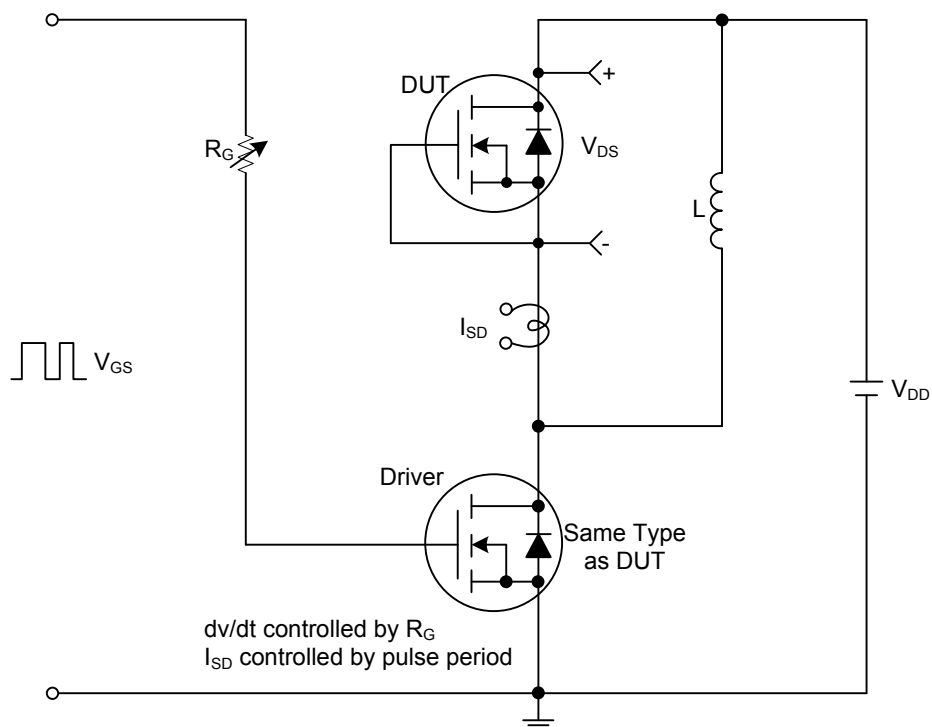
■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V, I _D =250μA	40			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =40V, V _{GS} =0V			1	μA
Gate-Source Leakage Current	Forward	I _{GSS}	V _{DS} =0V, V _{GS} =20V			60	nA
	Reverse		V _{DS} =0V, V _{GS} =-20V			-60	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	1.0		3.0	V
Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =12A			15	mΩ
			V _{GS} =4.5V, I _D =12A			20	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		C _{ISS}	V _{DS} =25V, V _{GS} =0V, f=1.0MHz		1260		pF
Output Capacitance		C _{OSS}			140		pF
Reverse Transfer Capacitance		C _{RSS}			125		pF
SWITCHING PARAMETERS							
Total Gate Charge (Note 1)		Q _G	V _{DS} =32V, V _{GS} =10V, I _D =24A (Note 1, 2)		42		nC
Gate-Source Charge		Q _{GS}			5.5		nC
Gate-Drain Charge		Q _{GD}			13		nC
Turn-ON Delay Time (Note 1)		t _{D(ON)}	V _{DD} =20V, V _{GS} =10V, I _D =24A, R _G =3Ω (Note 1, 2)		8		ns
Turn-ON Rise Time		t _R			17		ns
Turn-OFF Delay Time		t _{D(OFF)}			32		ns
Turn-OFF Fall Time		t _F			20		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I _S				24	A
Maximum Body-Diode Pulsed Current		I _{SM}				48	A
Drain-Source Diode Forward Voltage (Note 1)		V _{SD}	I _S =24A, V _{GS} =0V			1.4	V
Reverse Recovery Time (Note 1)		t _{rr}	I _S =24A, V _{GS} =0V,		33		nS
Reverse Recovery Charge		Q _{rr}	dI _F /dt =100A/μs		19		nC

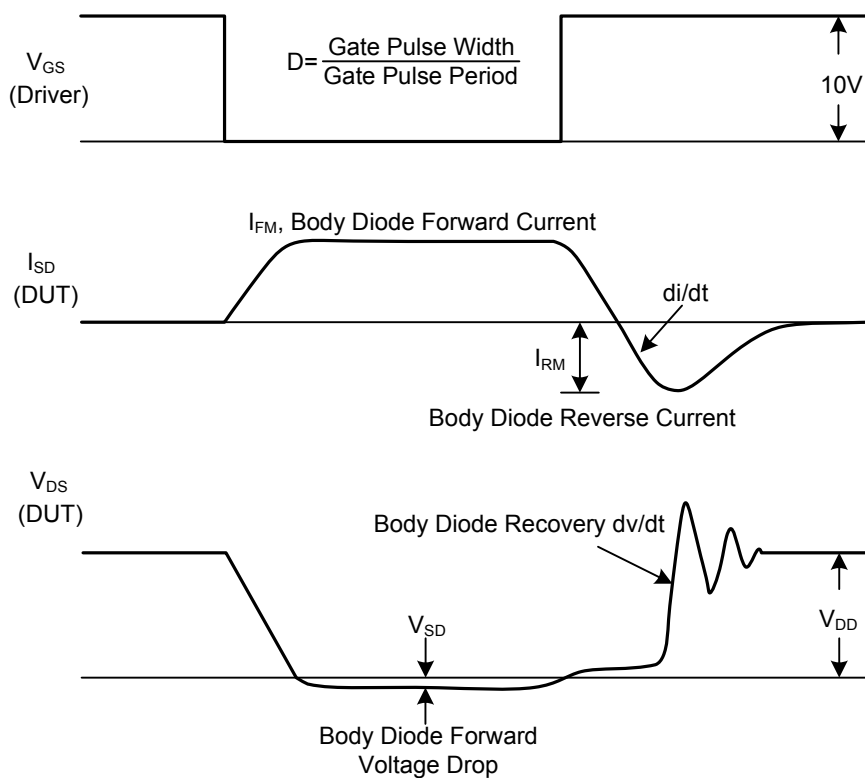
Notes: 1. Pulse Test: Pulse width ≤ 600μs, Duty cycle ≤ 2%.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS



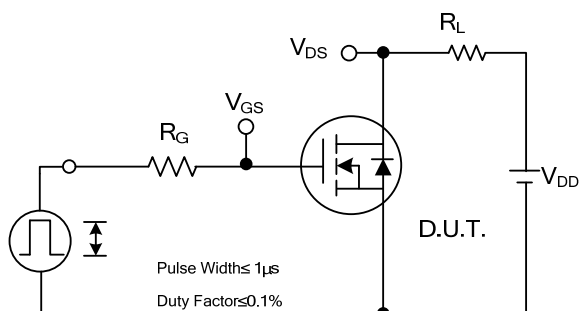
Peak Diode Recovery dv/dt Test Circuit



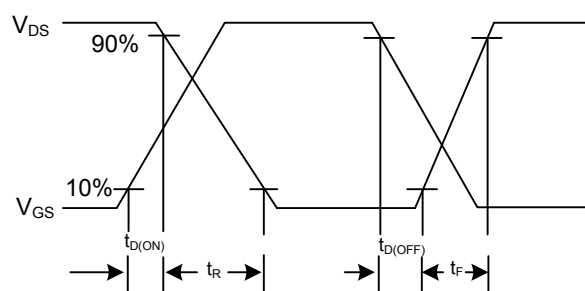
Peak Diode Recovery dv/dt Test Circuit and Waveforms

Peak Diode Recovery dv/dt Waveforms

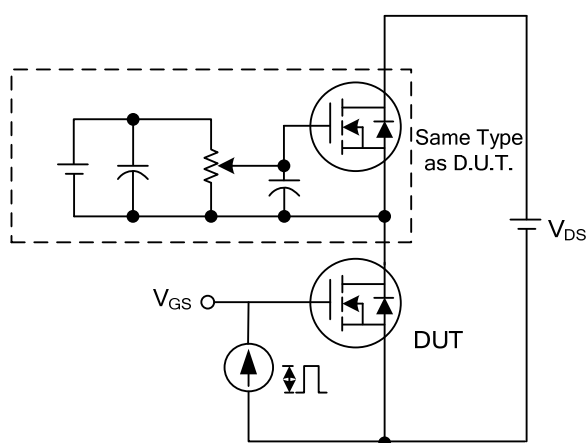
■ TEST CIRCUITS AND WAVEFORMS



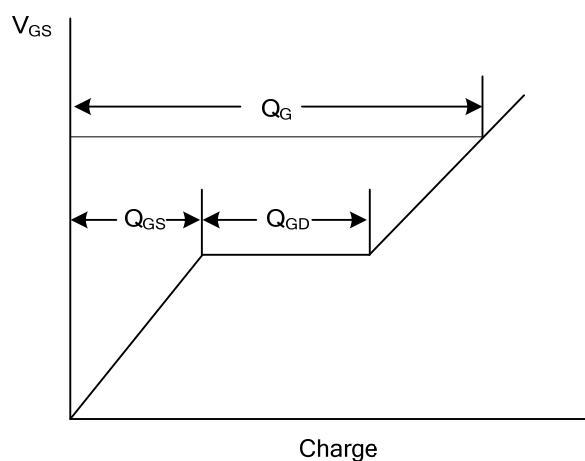
Switching Test Circuit



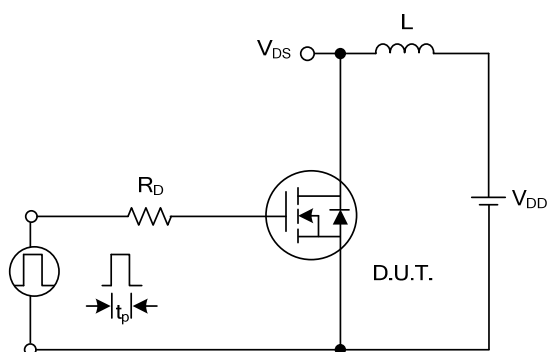
Switching Waveforms



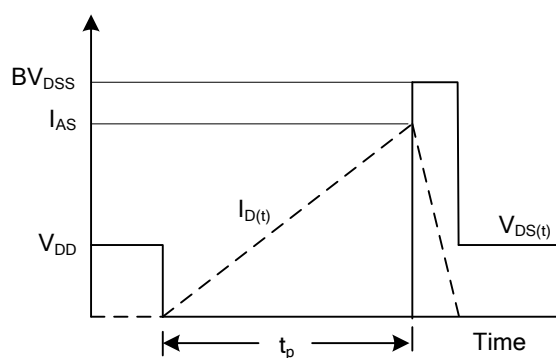
Gate Charge Test Circuit



Gate Charge Waveform

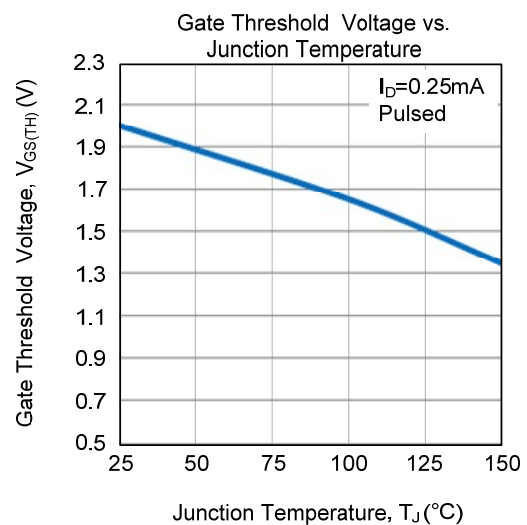
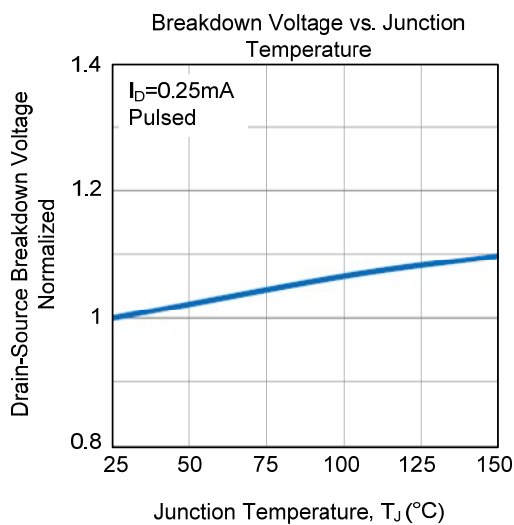
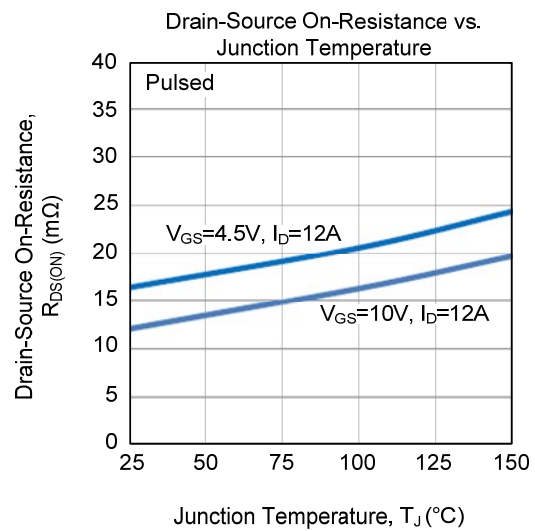
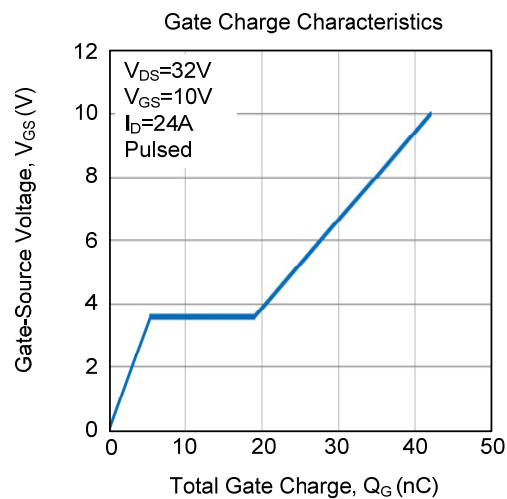
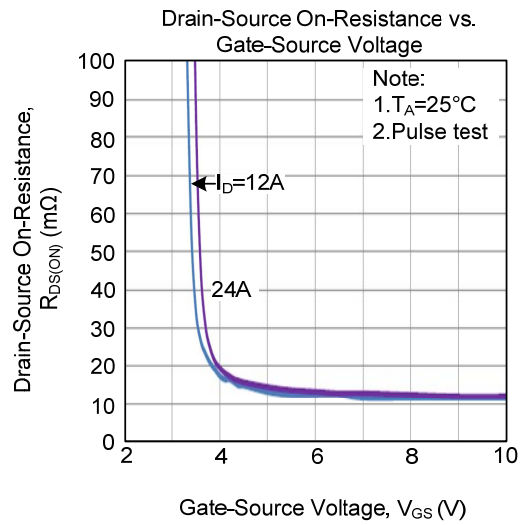
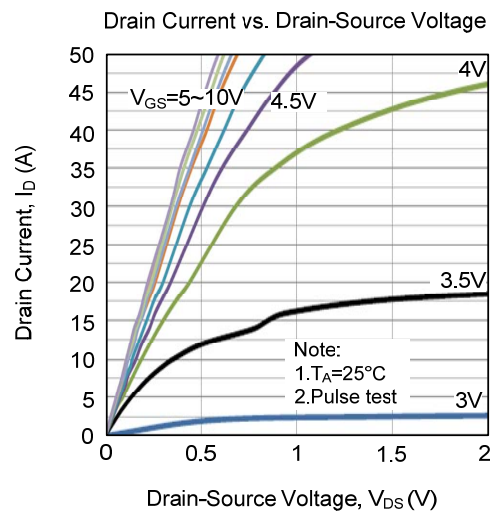


Unclamped Inductive Switching Test Circuit

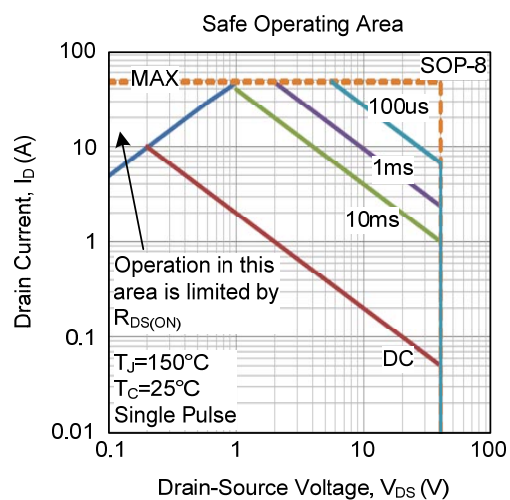
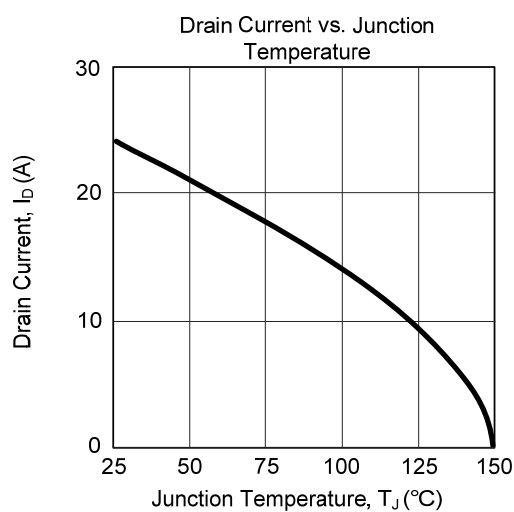
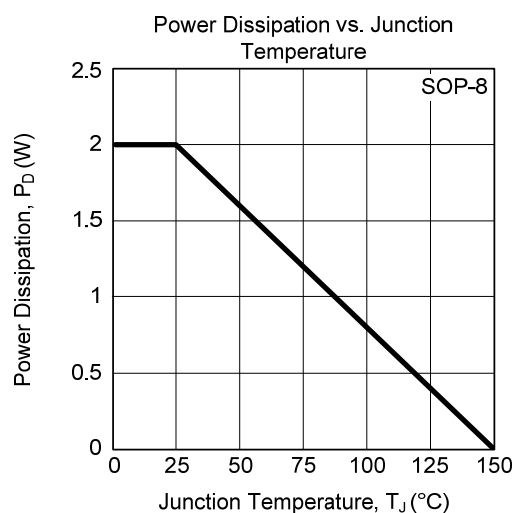
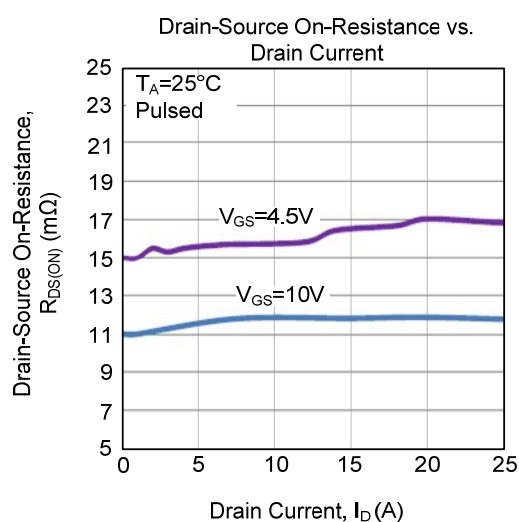
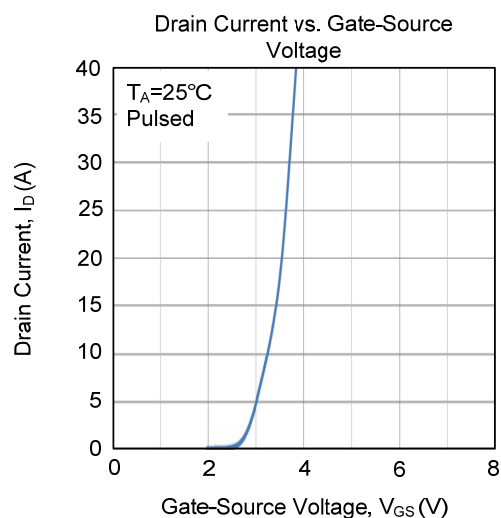
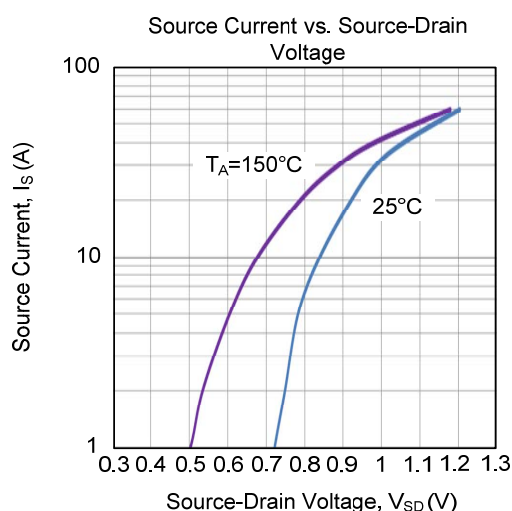


Unclamped Inductive Switching Waveforms

TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



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