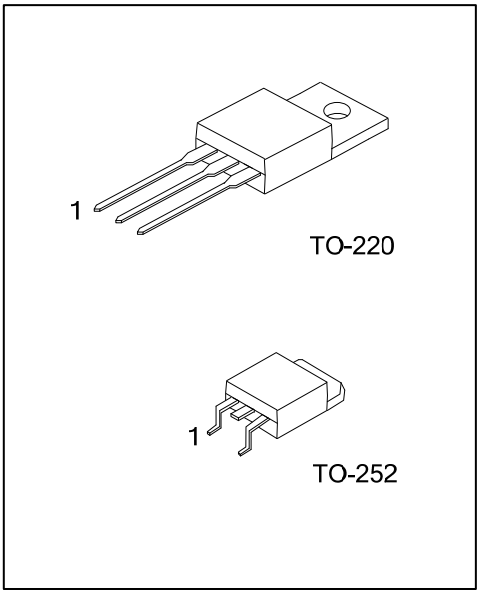
10A, 300V N-CHANNEL POWER MOSFET



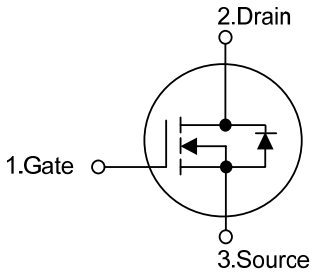
■ DESCRIPTION

The **UTC 10N30-HC** is a high voltage and high current power MOSFET, designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient AC to DC converters and bridge circuits.

■ FEATURES

* $R_{DS(ON)} \leq 0.7\ \Omega$ @ V_{GS}=10V, I_D=5.0A
* Fast switching
* Improved dv/dt capability

■ SYMBOL



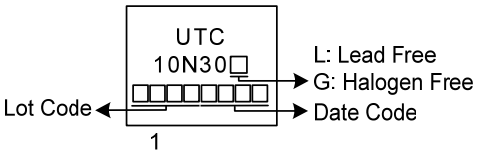
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
10N30L-TA3-T	10N30G-TF3-T	TO-220	G	D	S	Tube
10N30L-TN3-R	10N30G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

10N30G-TF3-T (1)Packing Type	(1) T: Tube, R: Tape Reel
(2)Package Type	(2) TA3: TO-220, TN3: TO-252
(3)Green Package	(3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING



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■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	300	V
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	Continuous	I_D	10	A
	Pulsed (Note 2)	I_{DM}	20	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	27	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	1.9	V/ns
Power Dissipation	TO-220	P_D	60	W
	TO-252		45	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. L = 1.0mH, I_{AS} = 7.3A, V_{DD} = 50V, R_G = 25 Ω Starting T_J = 25°C
4. I_{SD} ≤ 10A, di/dt ≤200A/μs, V_{DD} ≤BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220	θ_{JA}	62.5	°C/W
	TO-252		110	
Junction to Case	TO-220	θ_{JC}	2.08	°C/W
	TO-252		2.7 (Note)	

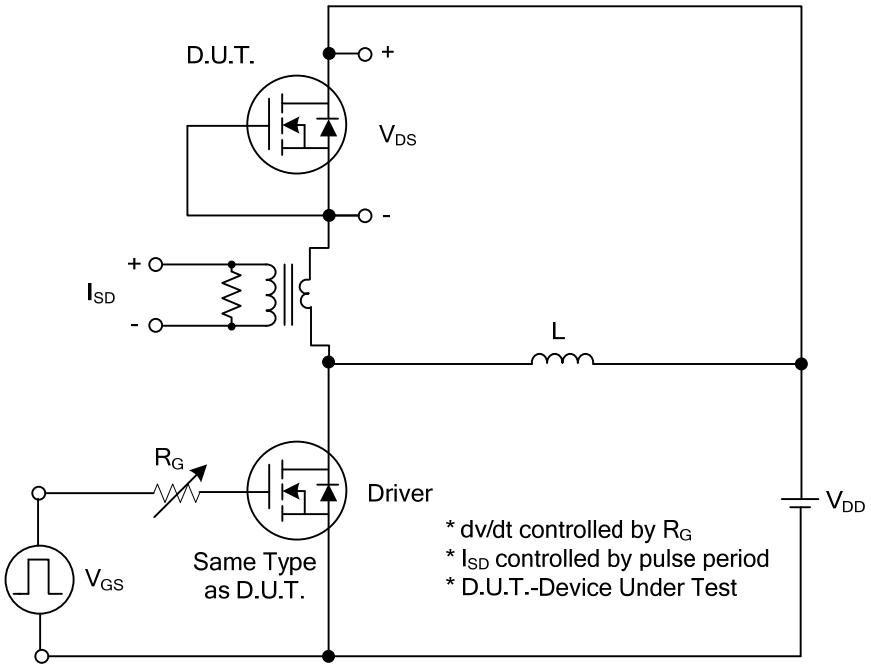
Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS (T_J = 25°C, unless otherwise specified)

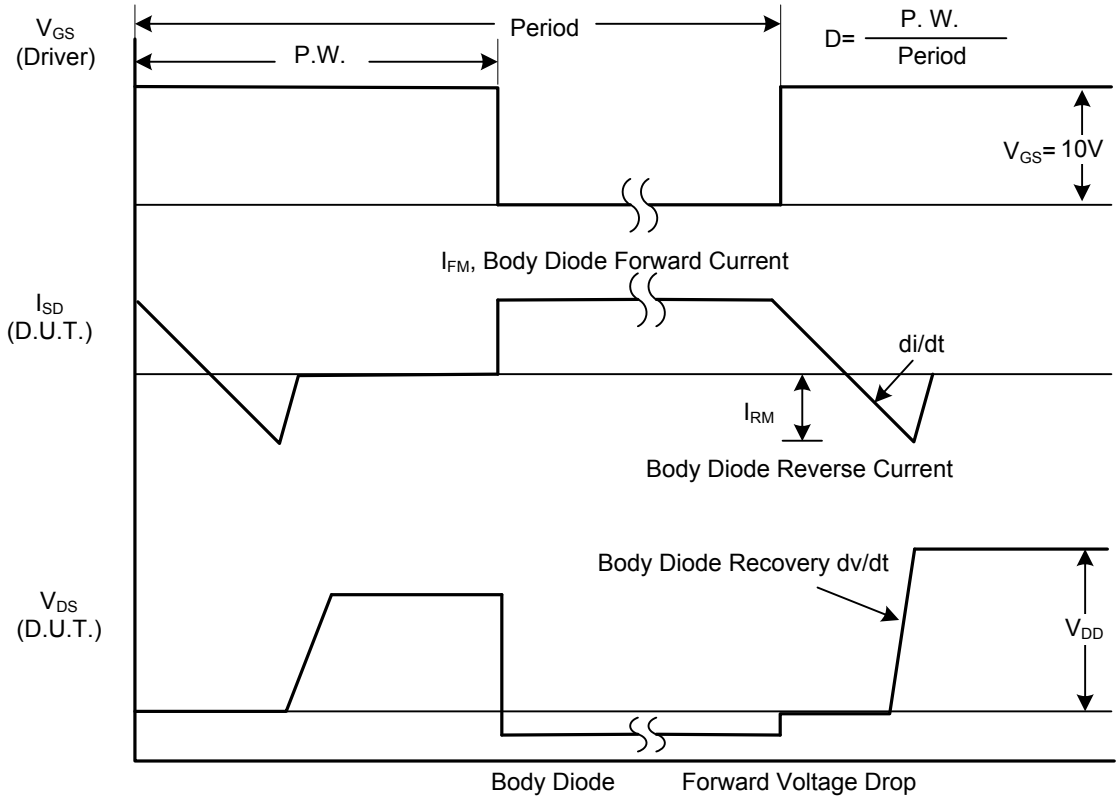
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	V_{GS}=0V, I_D= 250μA	300			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=300V, V_{GS}=0V			10	μA
Gate-Source Leakage Current	Forward	I_{GSS}	V_{GS}=30V, V_{DS}=0V			100	nA
	Reverse		V_{GS}=-30V, V_{DS}=0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=5.0A			0.7	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		C_{ISS}	V_{GS}=0V, V_{DS}=25V, f=1.0 MHz		436.9		pF
Output Capacitance		C_{OSS}			66.8		pF
Reverse Transfer Capacitance		C_{RSS}			4.6		pF
SWITCHING CHARACTERISTICS							
Total Gate Charge (Note 1)		Q_G	V_{DS}=240V, V_{GS}=10V, I_D=10A I_G=1mA (Note 1, 2)		13.3		nC
Gateource Charge		Q_{GS}			4.8		nC
Gate-Drain Charge		Q_{GD}			1.6		nC
Turn-on Delay Time (Note 1)		$t_{D(ON)}$	V_{DD}=100V, V_{GS}=10V,, I_D=10A, R_G=25Ω (Note 1, 2)		5.8		ns
Rise Time		t_R			16.4		ns
Turn-off Delay Time		$t_{D(OFF)}$			24.6		ns
Fall-Time		t_F			20.6		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Continuous Drain-Source Diode Forward Current		I_S				10	A
Maximum Pulsed Drain-Source Diode Forward Current		I_{SM}				20	A
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	V_{GS}=0V, I_S=10A			1.4	V
Reverse Recovery Time (Note 1)		t_{rr}	V_{GS}=0V, I_S=10A, dI_F/dt=100A/μs (Note1)		158		ns
Reverse Recovery Charge		Q_{rr}			1.89		μC

Notes: 1. Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2%.
2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

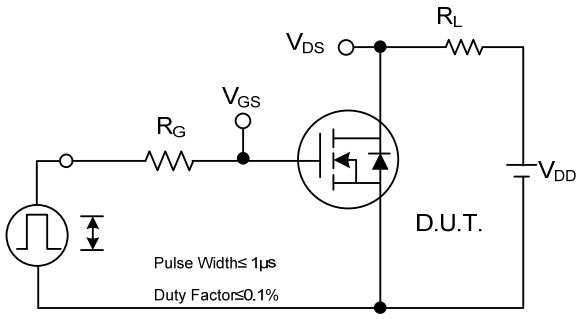


Peak Diode Recovery dv/dt Test Circuit

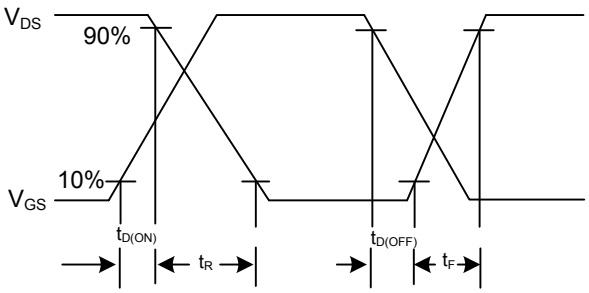


Peak Diode Recovery dv/dt Waveforms

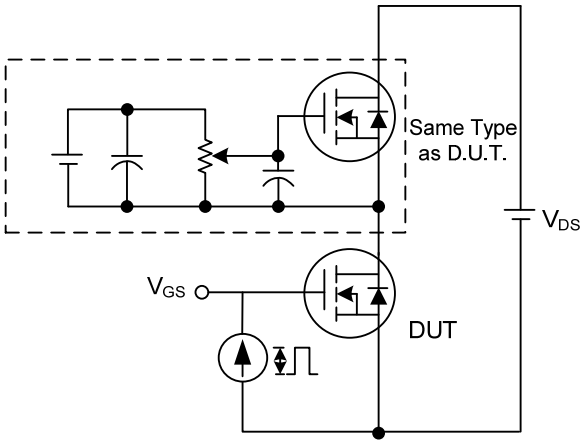
■ TEST CIRCUITS AND WAVEFORMS



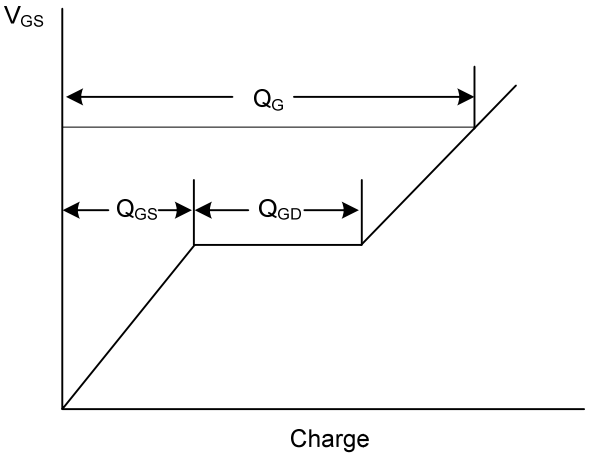
Switching Test Circuit



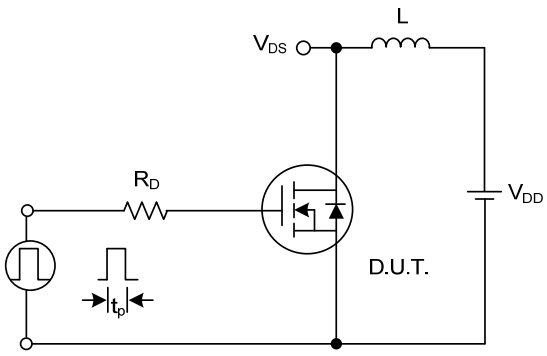
Switching Waveforms



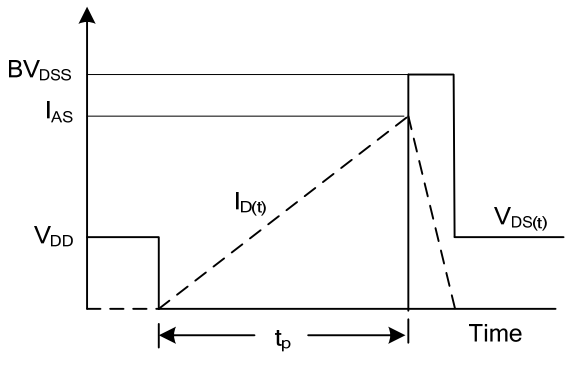
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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