

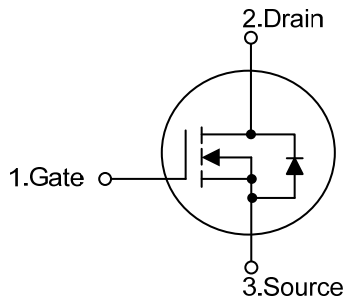
**UTT65N20****Power MOSFET****65A, 200V N-CHANNEL
POWER MOSFET****DESCRIPTION**

The UTC **UTT65N20** is a N-channel mode power MOSFET using UTC's advanced technology to provide customers with a minimum on-state resistance, low gate charge and high switching speed.

The UTC **UTT65N20** is suitable for high voltage synchronous rectifier and AC/DC converters, etc.

FEATURES

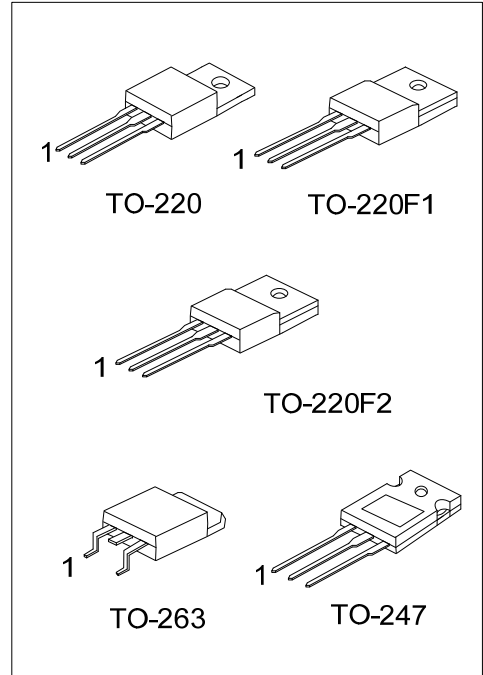
- * $R_{DS(ON)} \leq 32 \text{ m}\Omega$ @ $V_{GS}=10\text{V}$, $I_D=32.5\text{A}$
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness

SYMBOL**ORDERING INFORMATION**

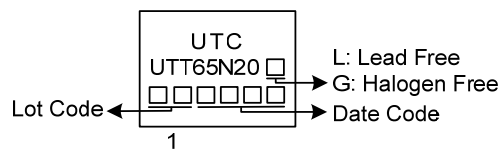
Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UTT65N20L-TA3-T	UTT65N20G-TA3-T	TO-220	G	D	S	Tube
UTT65N20L-TF1-T	UTT65N20G-TF1-T	TO-220F1	G	D	S	Tube
UTT65N20L-TF2-T	UTT65N20G-TF2-T	TO-220F2	G	D	S	Tube
UTT65N20L-TQ2-T	UTT65N20G-TQ2-T	TO-263	G	D	S	Tube
UTT65N20L-TQ2-R	UTT65N20G-TQ2-R	TO-263	G	D	S	Tape Reel
UTT65N20L-T47-T	UTT65N20G-T47-T	TO-247	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

UTT65N20G-TA3-T	(1)Packing Type	(1) T: Tube, R: Tape Reel
	(2)Package Type	(2) TA3: TO-220, TF1: TO-220F1, TF2: TO-220F2
	(3)Green Package	T47: TO-247, TQ2: TO-263
		(3) G: Halogen Free and Lead Free, L: Lead Free



■ MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_c=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	200	V
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current		I _D	65	A
Pulsed Drain Current (Note 2)		I _{DM}	130	A
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	15.7	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	33.1	V/ns
Power Dissipation	TO-220/TO-263	P _D	130	W
	TO-220F1/TO-220F2		52	W
	TO-247		180	W
Junction Temperature		T _J	+150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L = 0.1mH, I_{AS} = 17.7A, V_{DD} = 50V, R_G = 25 Ω Starting T_J = 25°C.

4. I_{SD} ≤ 30A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C.

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220/TO-220F1	θ _{JA}	62.5	°C/W
	TO-220F2/TO-263			
	TO-247		50	°C/W
Junction to Case	TO-220/TO-263	θ _{JC}	0.96	°C/W
	TO-220F1/TO-220F2		2.4	°C/W
	TO-247		0.69	°C/W

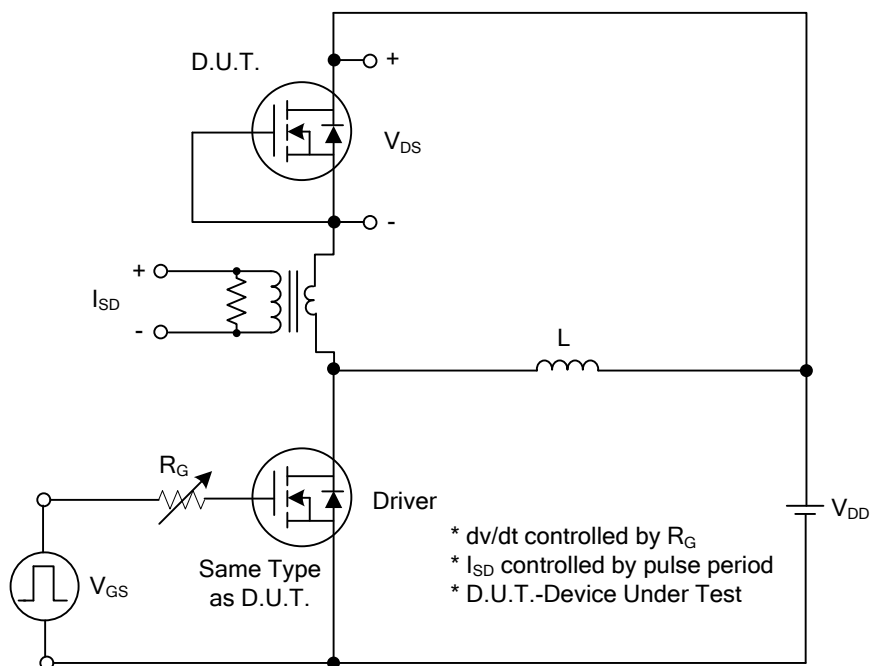
■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V, I _D =250μA	200			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =200V, V _{GS} =0V			10	μA
Gate- Source Leakage Current	Forward	I _{GSS}	V _{GS} =20V, V _{DS} =0V			100	nA
	Reverse		V _{GS} =-20V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =32.5A			32	mΩ
DYNAMIC CHARACTERISTICS							
Input Capacitance		C _{ISS}	V _{DS} =25V, V _{GS} =0V, f=1.0MHz		8375		pF
Output Capacitance		C _{OSS}			408.6		pF
Reverse Transfer Capacitance		C _{RSS}			269.4		pF
SWITCHING CHARACTERISTICS							
Total Gate Charge (Note 1)		Q _G	V _{DS} =160V, V _{GS} =10V, I _D =65A I _G =1mA (Note 1, 2)		173.3		nC
Gate-Source Charge		Q _{GS}			67		nC
Gate-Drain Charge		Q _{GD}			43.6		nC
Turn-On Delay Time (Note 1)		t _{D(ON)}	V _{DS} =100V, V _{GS} =10V, I _D =65A, R _G =6Ω (Note 1, 2)		42.4		ns
Turn-On Rise Time		t _R			26.1		ns
Turn-Off Delay Time		t _{D(OFF)}			109		ns
Turn-Off Fall Time		t _F			51.8		ns
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS							
Maximum Body-Diode Continuous Current		I _S				65	A
Maximum Body-Diode Pulsed Current		I _{SM}				130	A
Drain-Source Diode Forward Voltage (Note 1)		V _{SD}	I _S =65A , V _{GS} =0V			1.4	V
Reverse Recovery Time (Note 1)		t _{rr}	I _S =65A , V _{GS} =0V		170		ns
Reverse Recovery Charge		Q _{rr}	di/dt=100A/μs		2.1		μC

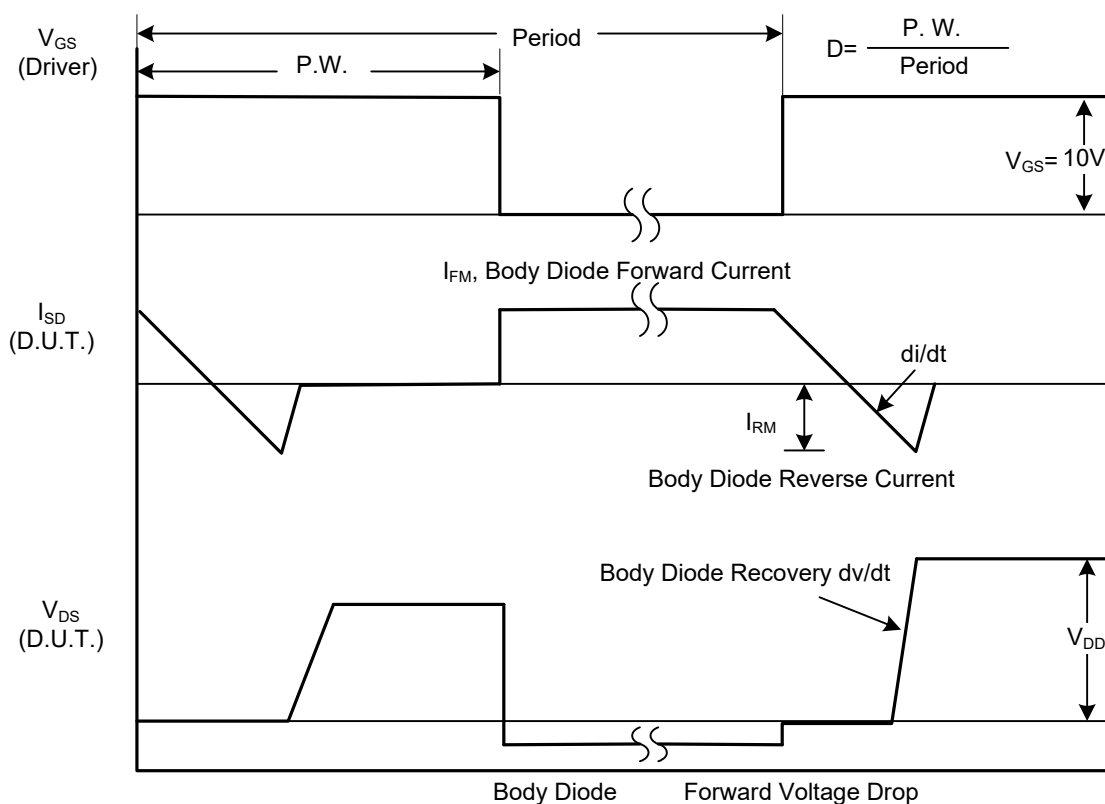
Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

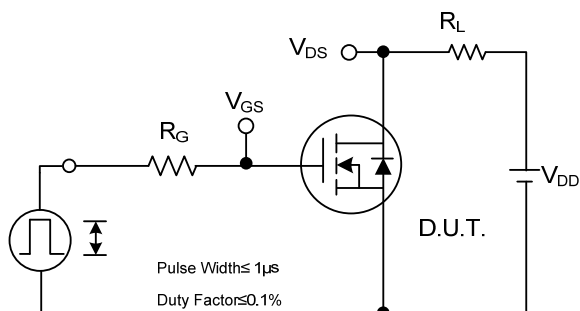


Peak Diode Recovery dv/dt Test Circuit

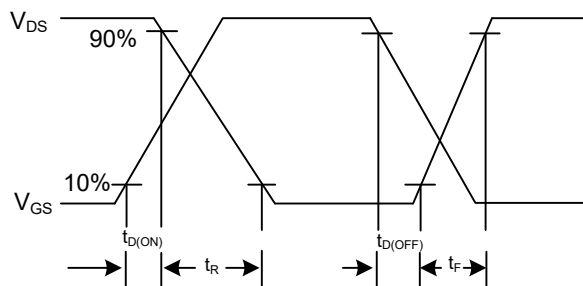


Peak Diode Recovery dv/dt Waveforms

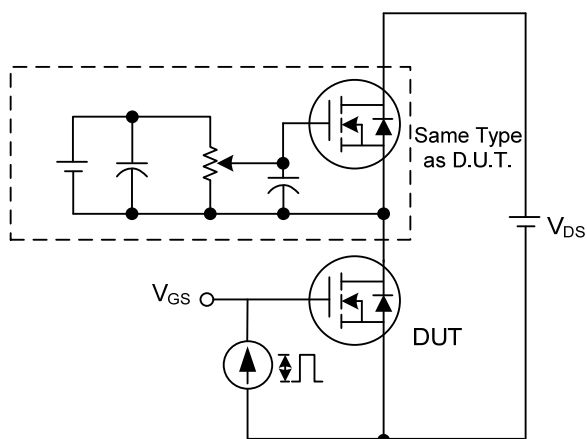
■ TEST CIRCUITS AND WAVEFORMS



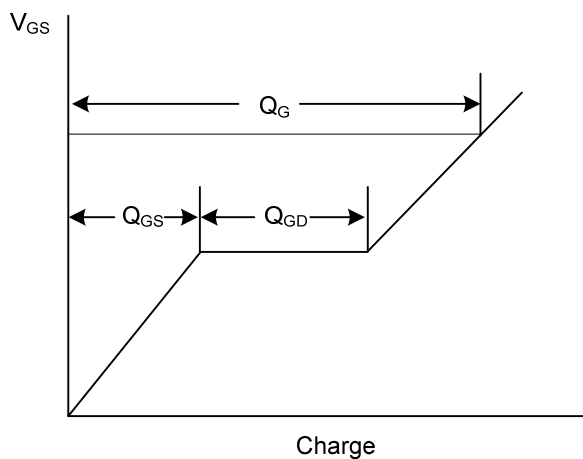
Switching Test Circuit



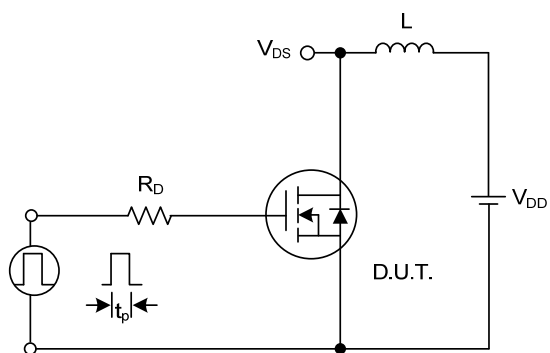
Switching Waveforms



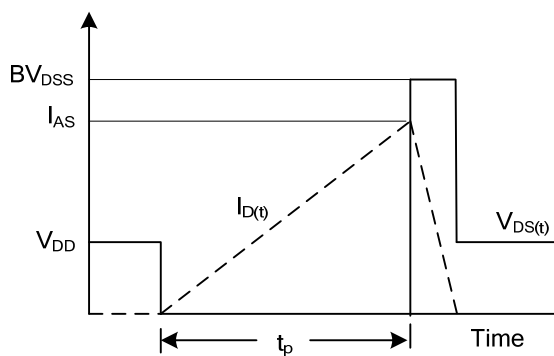
Gate Charge Test Circuit



Gate Charge Waveform

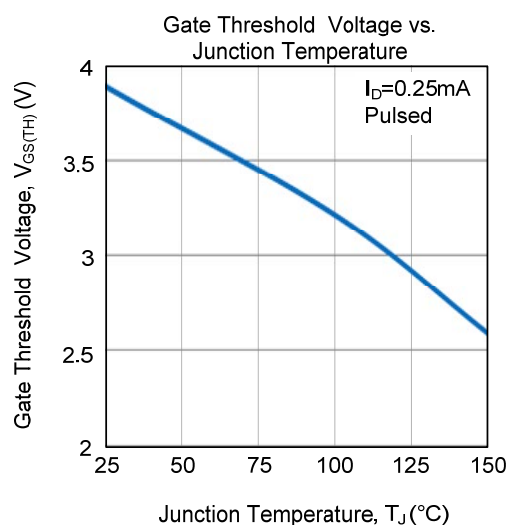
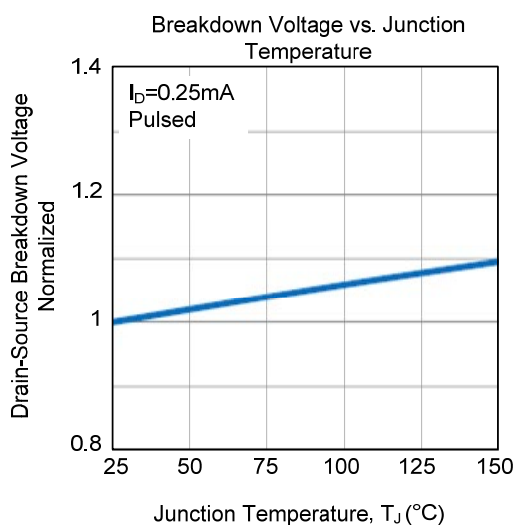
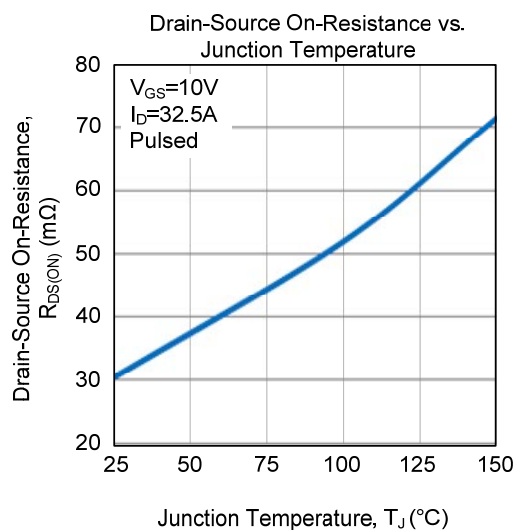
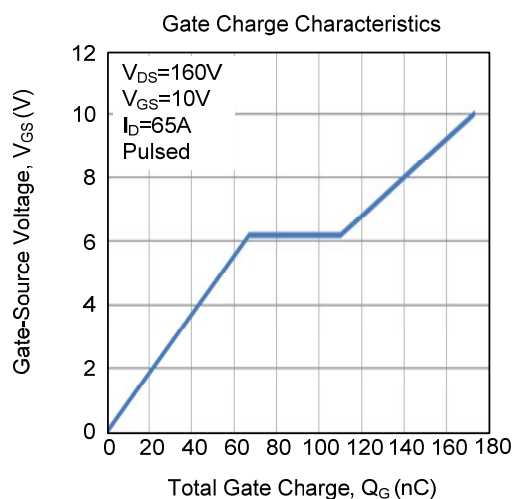
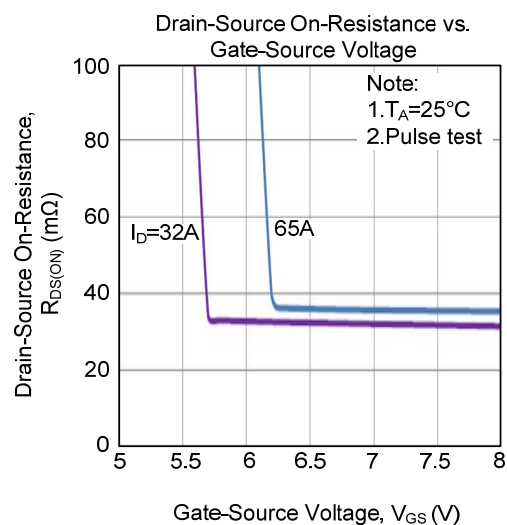
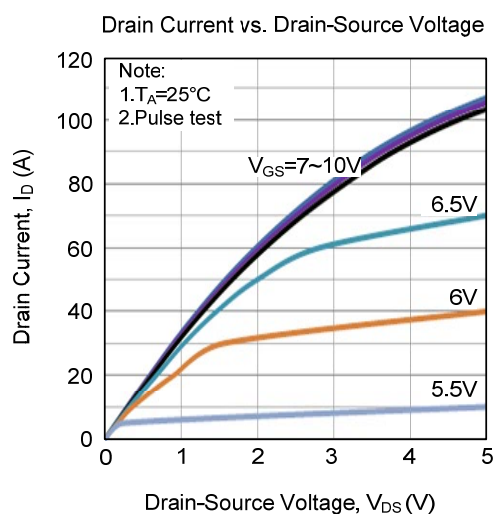


Unclamped Inductive Switching Test Circuit

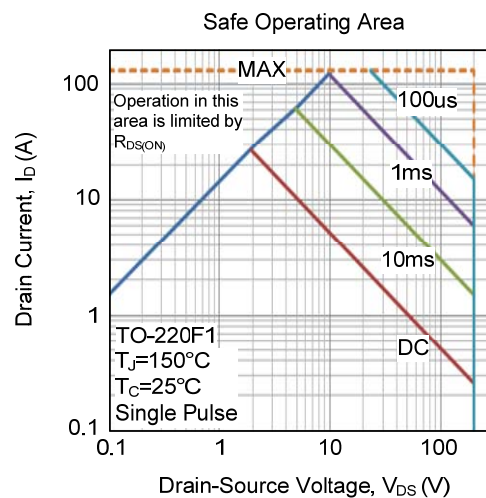
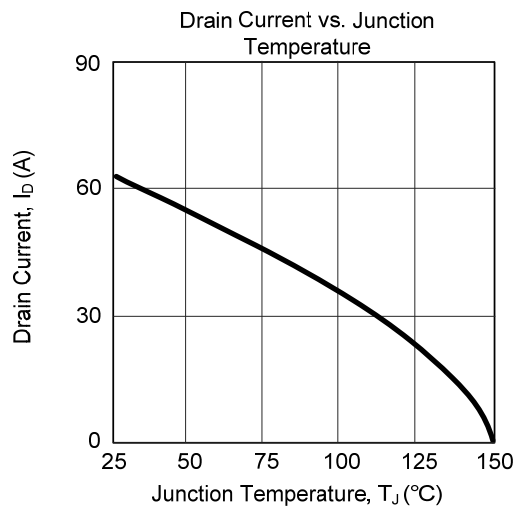
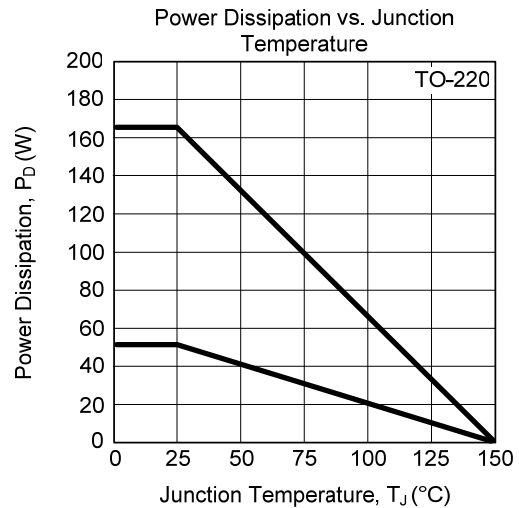
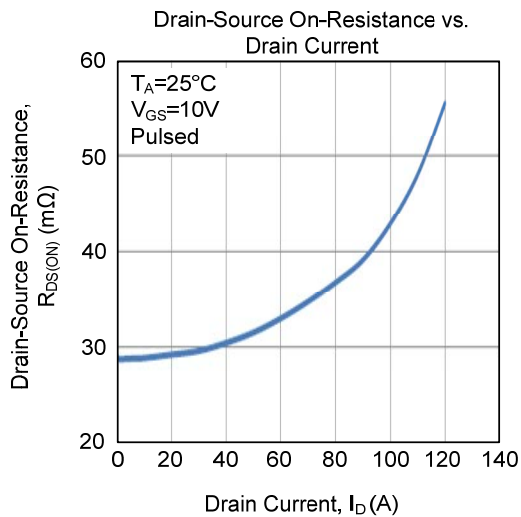
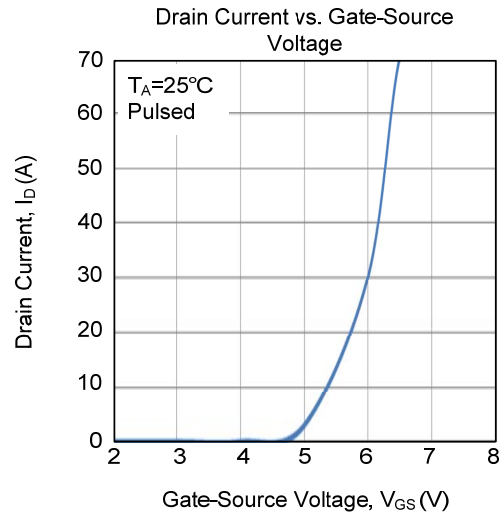
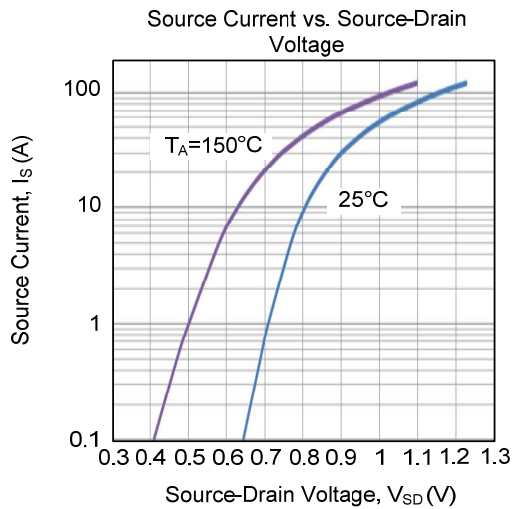


Unclamped Inductive Switching Waveforms

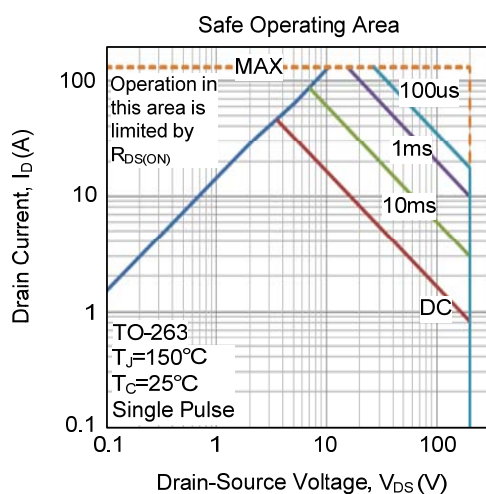
TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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