

## UT3NP06

Power MOSFET

DUAL ENHANCEMENT MODE  
(N-CHANNEL / P-CHANNEL)

## ■ DESCRIPTION

The UTC **UT3NP06** incorporates a N-channel MOSFET and a P-channel MOSFET, it uses UTC's advanced technology to provide customers a minimum on-state resistance, high switching speed, low gate charge and cost effectiveness.

The UTC **UT3NP06** is universally applied in low voltage applications.

## ■ FEATURES

\*N-CHANNEL: 3A, 60V

$R_{DS(on)} \leq 56 \text{ m}\Omega @ V_{GS}=10\text{V}, I_D=1.5\text{A}$

$R_{DS(on)} \leq 66 \text{ m}\Omega @ V_{GS}=4.5\text{V}, I_D=1.5\text{A}$

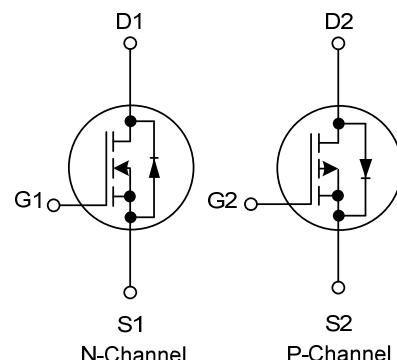
\*P-CHANNEL: -3A, -60V

$R_{DS(on)} \leq 95 \text{ m}\Omega @ V_{GS}=-10\text{V}, I_D=-1.5\text{A}$

$R_{DS(on)} \leq 120 \text{ m}\Omega @ V_{GS}=-4.5\text{V}, I_D=-1.5\text{A}$

\* High switching speed

## ■ SYMBOL



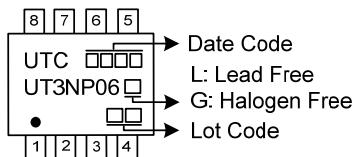
## ■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UT3NP06L-S08-R	UT3NP06G-S08-R	SOP-8	S1	G1	S2	G2	D2	D2	D1	D1	Tape Reel

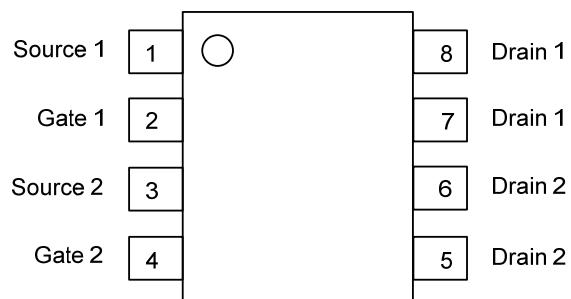
Note: Pin Assignment: G: Gate D: Drain S: Source

UT3NP06G-S08-R	(1)Packing Type	(1) R: Tape Reel
	(2)Package Type	(2) S08: SOP-8
	(3)Green Package	(3) G: Halogen Free and Lead Free, L: Lead Free

### ■ MARKING



### ■ PIN CONFIGURATION



■ ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS		UNIT	
		N-CHANNEL	P-CHANNEL		
Drain-Source Voltage	V <sub>DSS</sub>	60	-60	V	
Gate-Source Voltage	V <sub>GSS</sub>	±20	±20	V	
Drain Current	Continuous T <sub>A</sub> =25°C	I <sub>D</sub>	3	-3	A
	Pulsed (Note 1)	I <sub>DM</sub>	6	-6	A
Power Dissipation	T <sub>A</sub> =25°C	P <sub>D</sub>	1		W
Junction Temperature		T <sub>J</sub>	-55 ~ +150		°C
Storage Temperature Range		T <sub>STG</sub>	-55 ~ +150		°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS		UNIT
Junction to Ambient	θ <sub>JA</sub>	125 (Note)		°C/W

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub>=25°C, unless otherwise specified)

**N-Channel**

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =250μA, V <sub>GS</sub> =0V	60			V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C			1	μA
		V <sub>DS</sub> =48V, V <sub>GS</sub> =0V, T <sub>J</sub> =125°C			10	μA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =+20V			+100	nA
		V <sub>GS</sub> =-20V			-100	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.0		3.0	V
Static Drain-Source On-State Resistance (Note 2)	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =1.5A			56	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =1.5A			66	mΩ
<b>DYNAMIC PARAMETERS</b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1.0MHz		540		pF
Output Capacitance	C <sub>OSS</sub>			50		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			40		pF
<b>SWITCHING PARAMETERS</b>						
Total Gate Charge (Note 2)	Q <sub>G</sub>	V <sub>DS</sub> =48V, V <sub>GS</sub> =10V, I <sub>D</sub> =3A		22		nC
Gate to Source Charge	Q <sub>GS</sub>			3.6		nC
Gate to Drain Charge	Q <sub>GD</sub>			4		nC
Turn-ON Delay Time (Note 2)	t <sub>D(ON)</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =10V, I <sub>D</sub> =3A, R <sub>G</sub> =6Ω		4		ns
Rise Time	t <sub>R</sub>			16.5		ns
Turn-OFF Delay Time	t <sub>D(OFF)</sub>			21		ns
Fall-Time	t <sub>F</sub>			18		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Drain-Source Diode Forward Voltage (Note 2)	V <sub>SD</sub>	I <sub>S</sub> =3A, V <sub>GS</sub> =0V			1.4	V

## ■ ELECTRICAL CHARACTERISTICS (Cont.)

## P-Channel

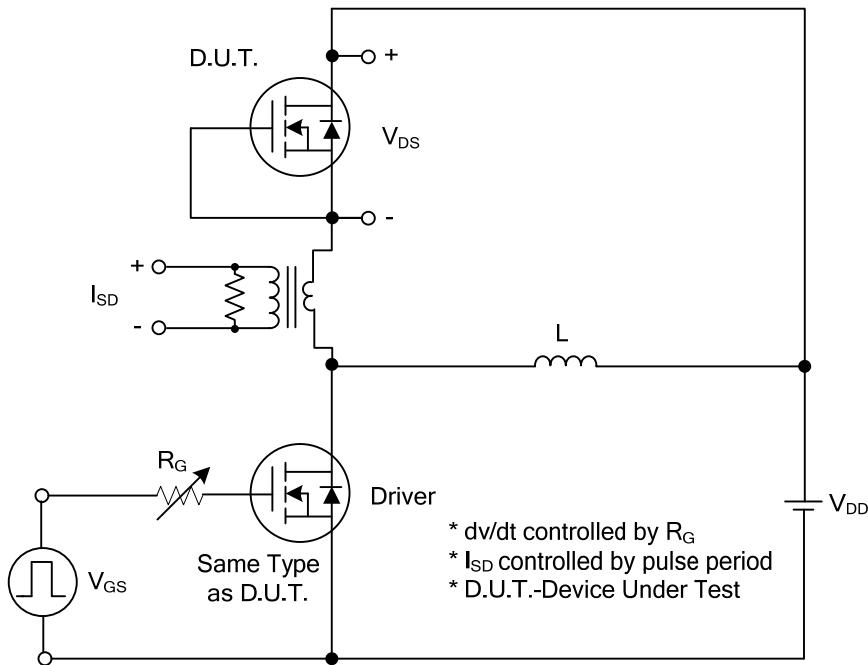
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$I_D=-250\mu A, V_{GS}=0V$	-60			V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS}=-60V, V_{GS}=0V, T_J=25^\circ C$			-1	$\mu A$
		$V_{DS}=-48V, V_{GS}=0V, T_J=125^\circ C$			-10	$\mu A$
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=+20V$			+100	nA
		$V_{GS}=-20V$			-100	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1.0		-3.0	V
Static Drain-Source On-State Resistance (Note 2)	$R_{DS(ON)}$	$V_{GS}=-10V, I_D=-1.5A$			95	$m\Omega$
		$V_{GS}=-4.5V, I_D=-1.5A$			120	$m\Omega$
<b>DYNAMIC PARAMETERS</b>						
Input Capacitance	$C_{iss}$	$V_{GS}=0V, V_{DS}=-25V, f=1.0MHz$		780		pF
Output Capacitance	$C_{oss}$			65		pF
Reverse Transfer Capacitance	$C_{rss}$			50		pF
<b>SWITCHING PARAMETERS</b>						
Total Gate Charge (Note 2)	$Q_G$	$V_{DS}=-48V, V_{GS}=-10V, I_D=-3A$		23		nC
Gate to Source Charge	$Q_{GS}$			4.8		nC
Gate to Drain Charge	$Q_{GD}$			4.6		nC
Turn-ON Delay Time (Note 2)	$t_{D(ON)}$	$V_{DS}=-30V, V_{GS}=-10V, I_D=-3A, R_G=6\Omega$		6		ns
Rise Time	$t_R$			17		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			38		ns
Fall-Time	$t_F$			22		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Drain-Source Diode Forward Voltage (Note 2)	$V_{SD}$	$I_S=-3A, V_{GS}=0V$			1.4	V

Notes: 1. Pulse width limited by maximum junction temperature

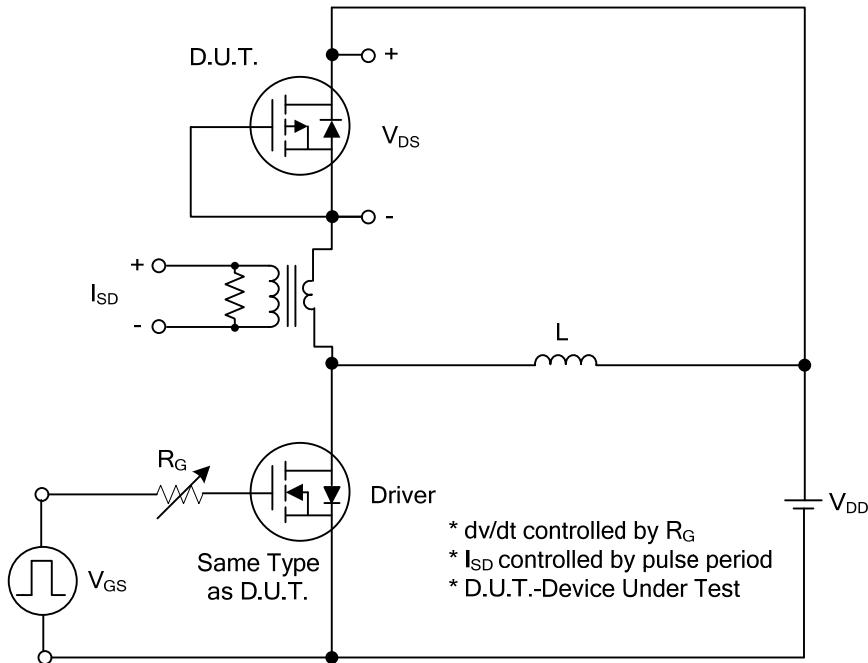
2. Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$

## ■ TEST CIRCUITS AND WAVEFORMS

## N-CHANNEL

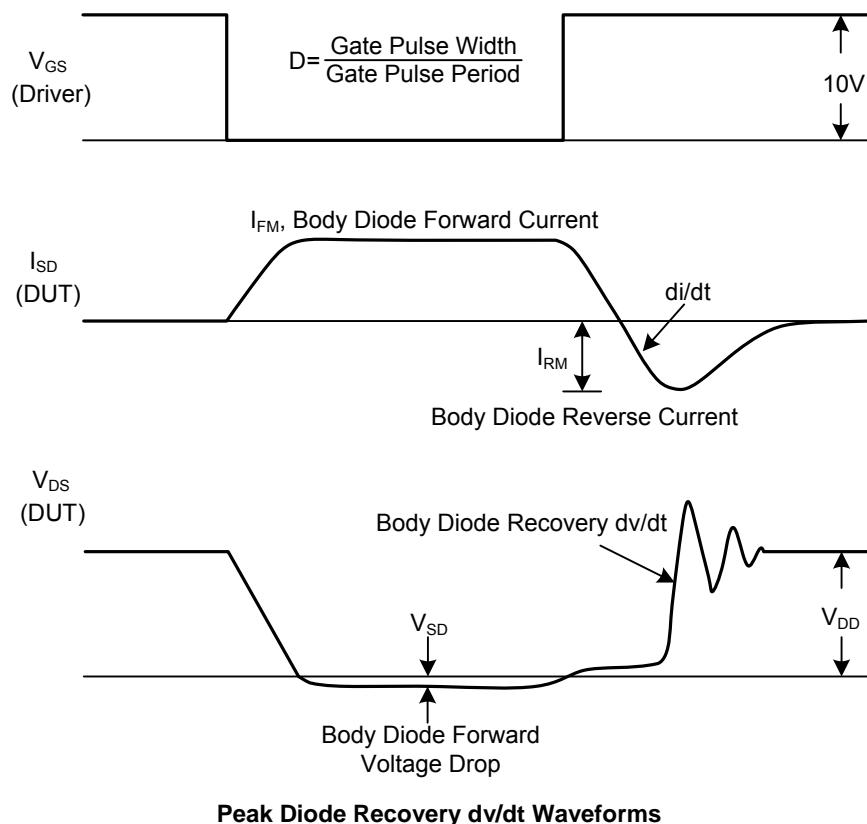


## P-CHANNEL



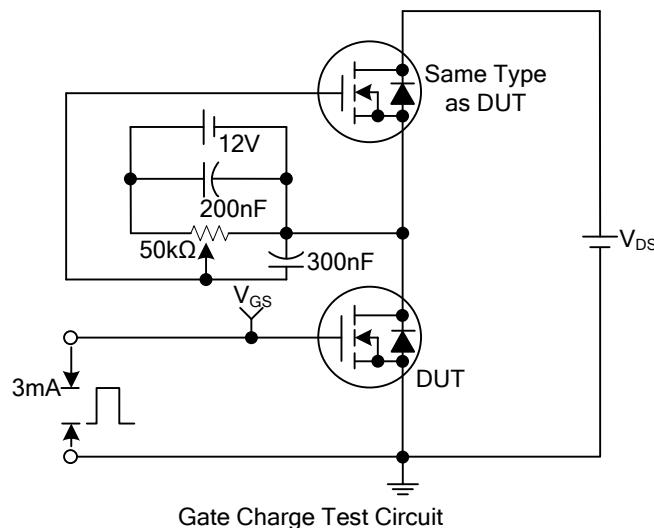
Peak Diode Recovery dv/dt Test Circuit

## ■ TEST CIRCUITS AND WAVEFORMS

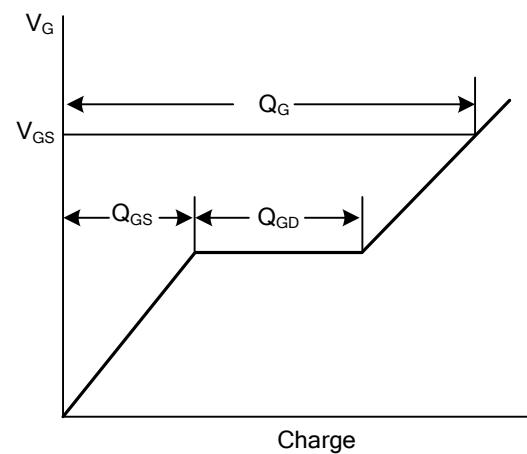


## ■ TEST CIRCUITS AND WAVEFORMS

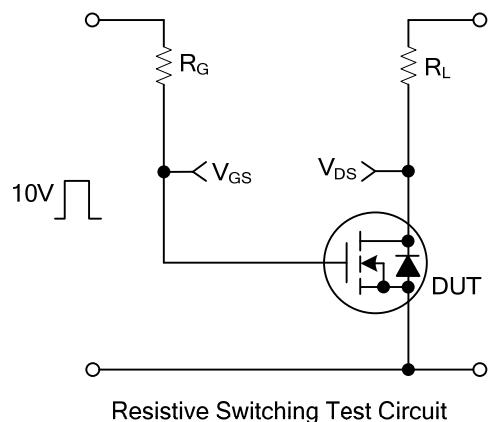
## N-CHANNEL



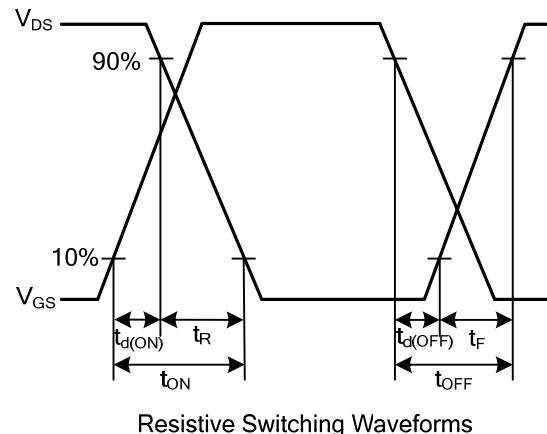
Gate Charge Test Circuit



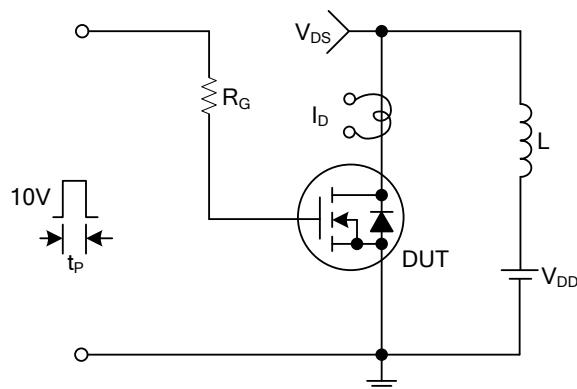
Gate Charge Waveforms



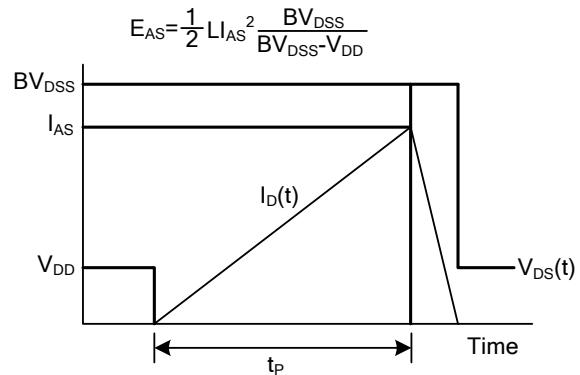
Resistive Switching Test Circuit



Resistive Switching Waveforms



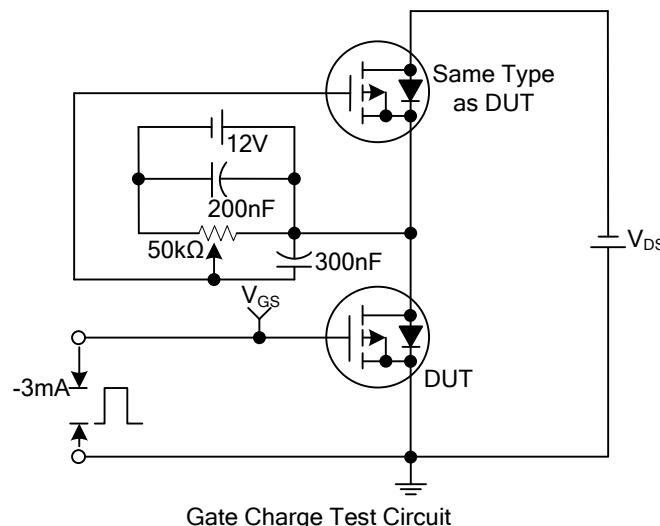
Unclamped Inductive Switching Test Circuit



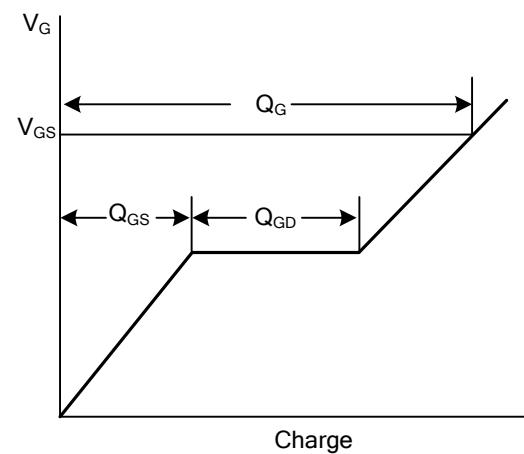
Unclamped Inductive Switching Waveforms

## ■ TEST CIRCUITS AND WAVEFORMS (Cont.)

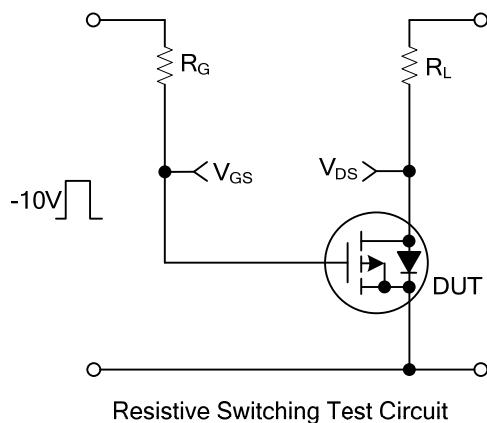
## P-CHANNEL



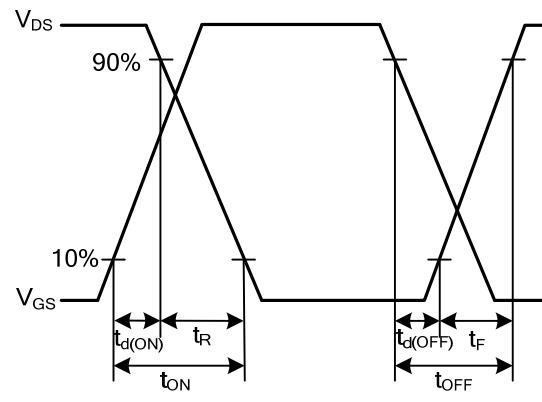
Gate Charge Test Circuit



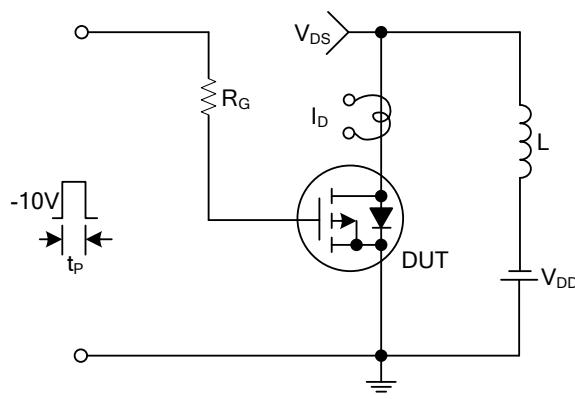
Gate Charge Waveforms



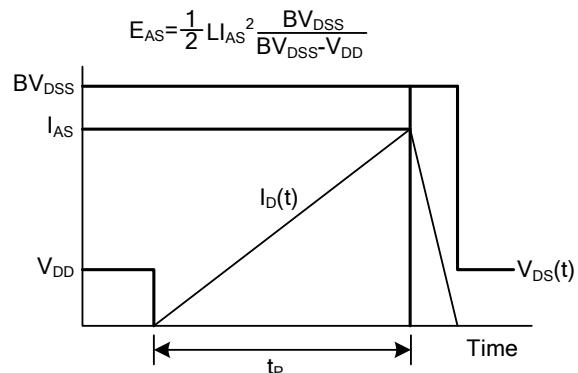
Resistive Switching Test Circuit



Resistive Switching Waveforms



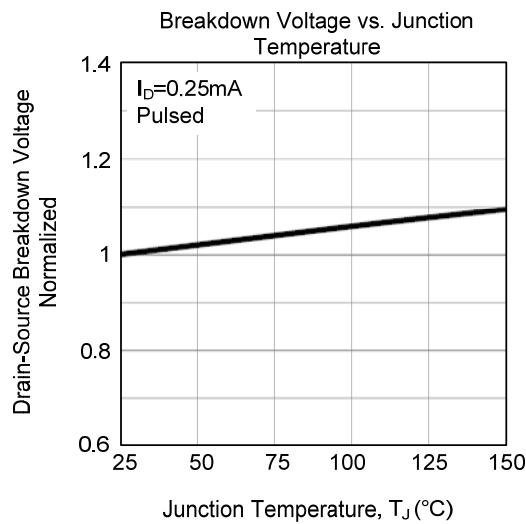
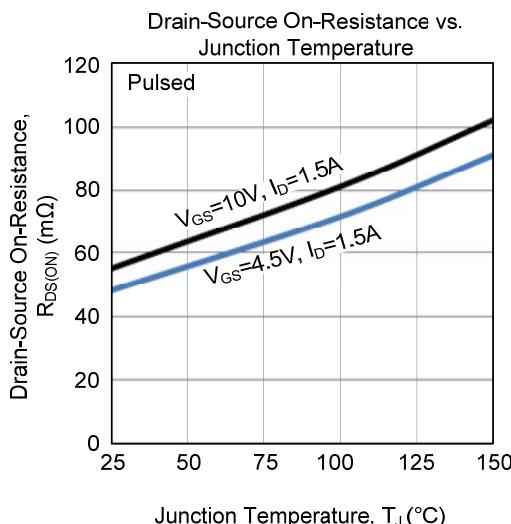
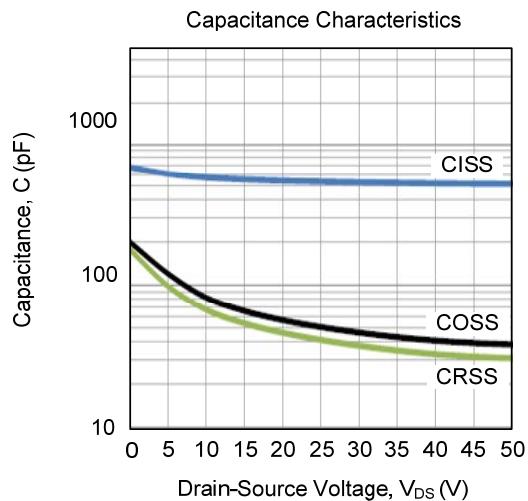
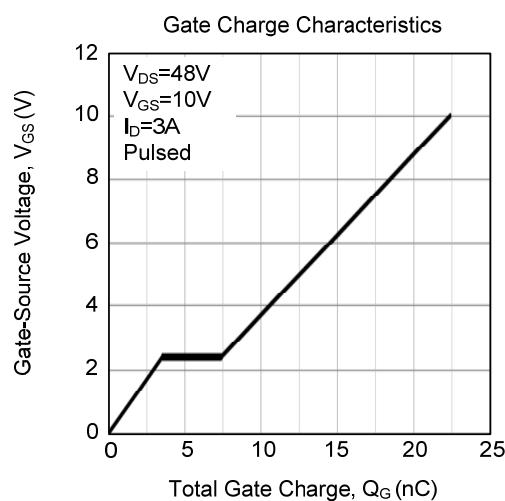
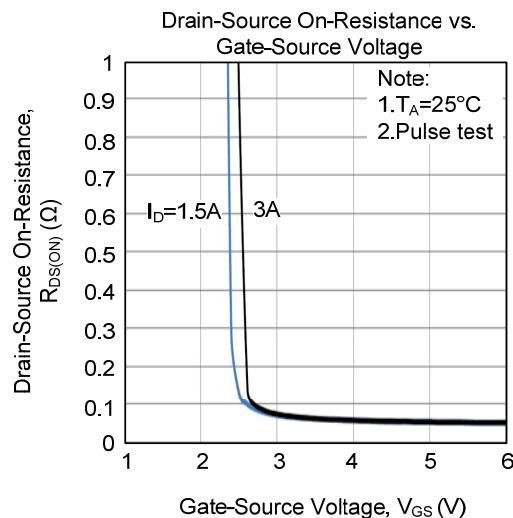
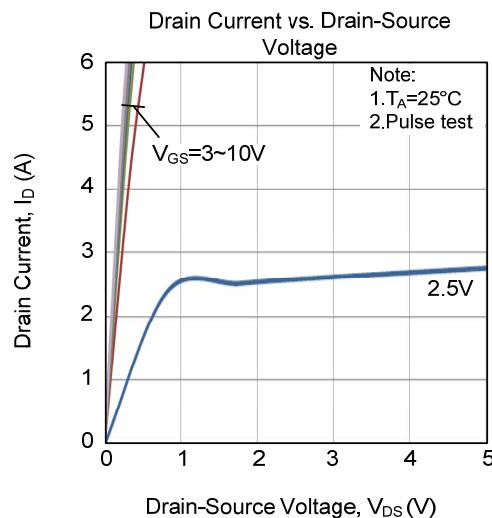
Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

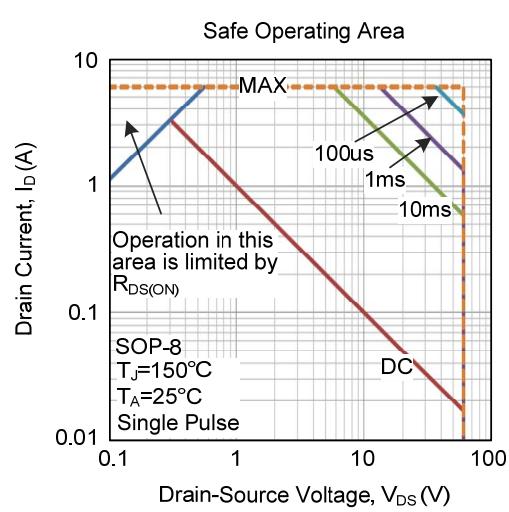
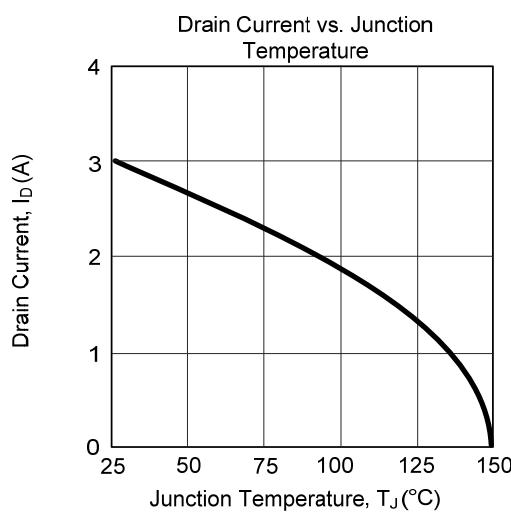
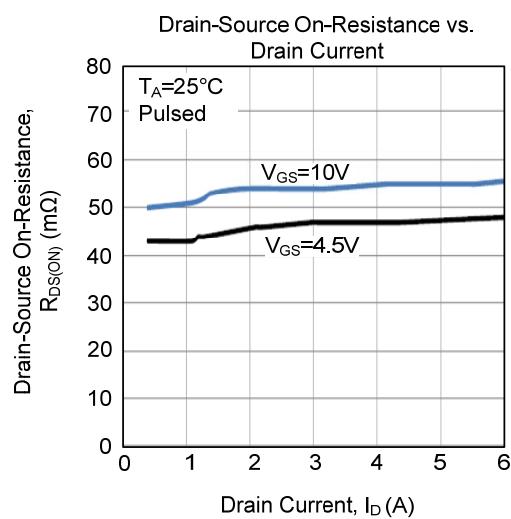
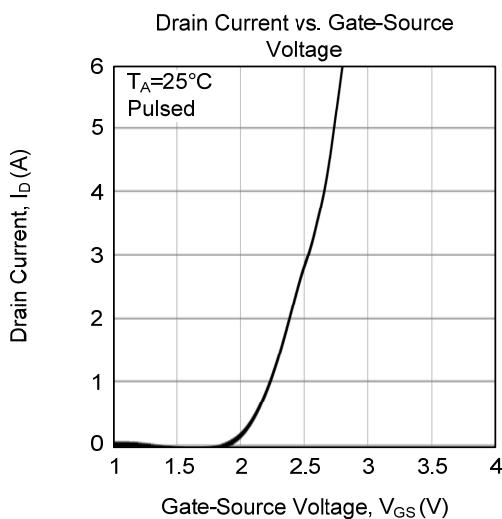
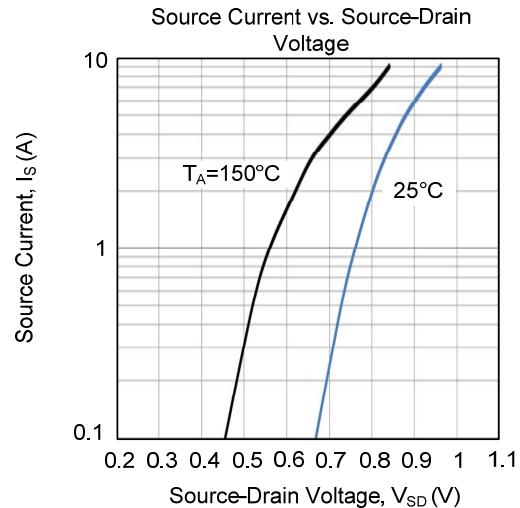
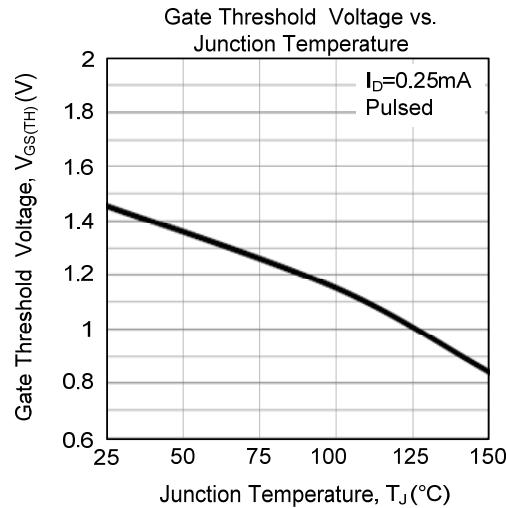
■ TYPICAL CHARACTERISTICS

N-CHANNEL



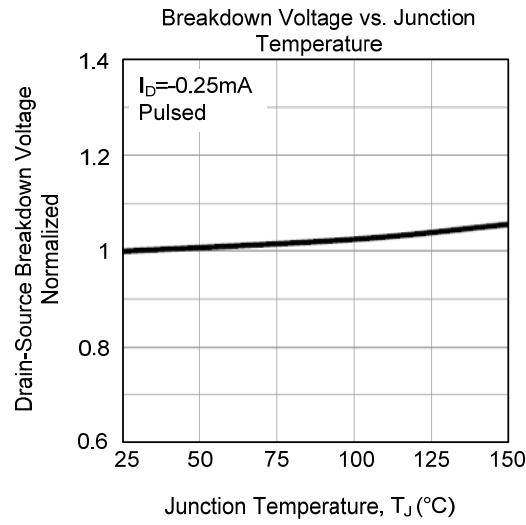
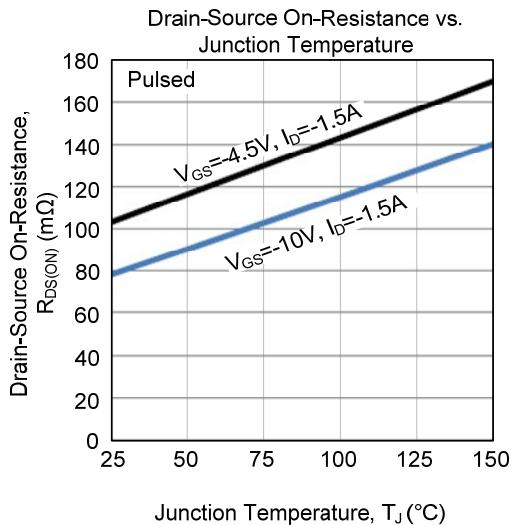
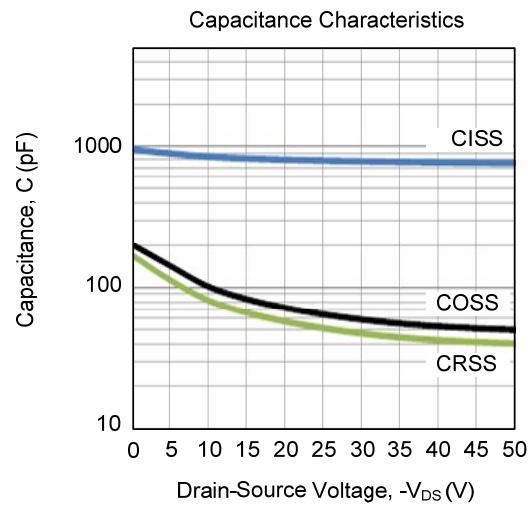
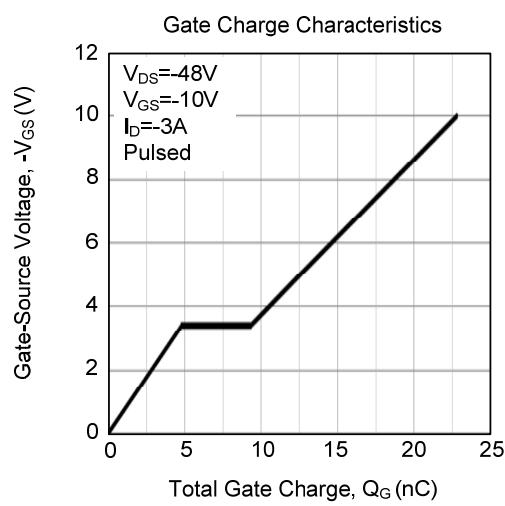
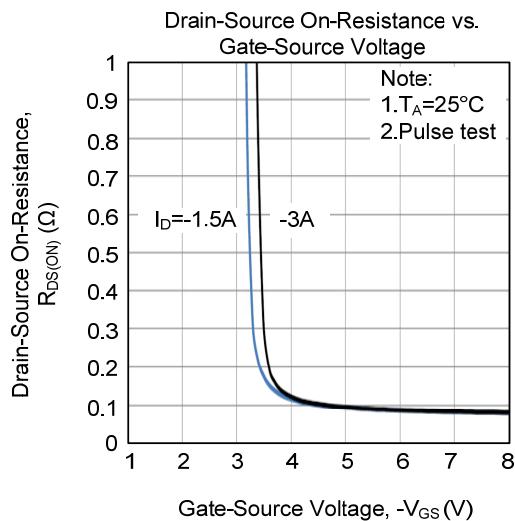
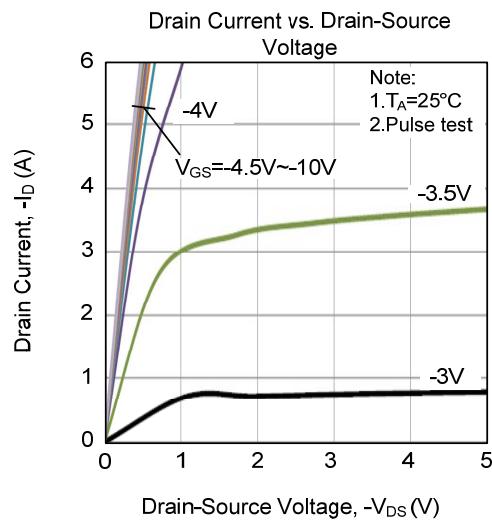
## ■ TYPICAL CHARACTERISTICS (Cont.)

## N-CHANNEL



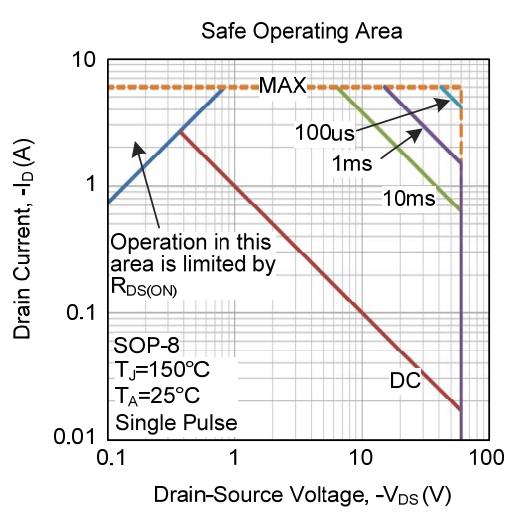
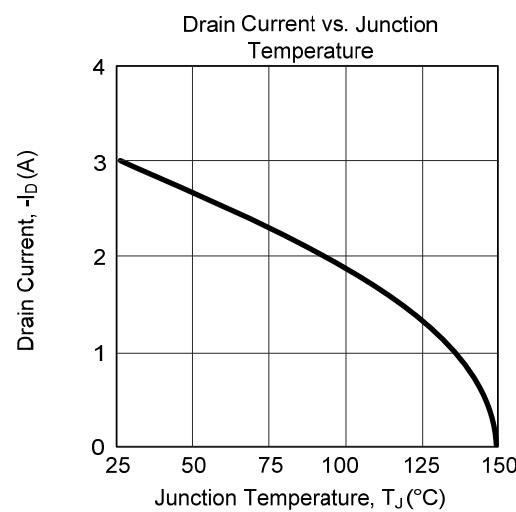
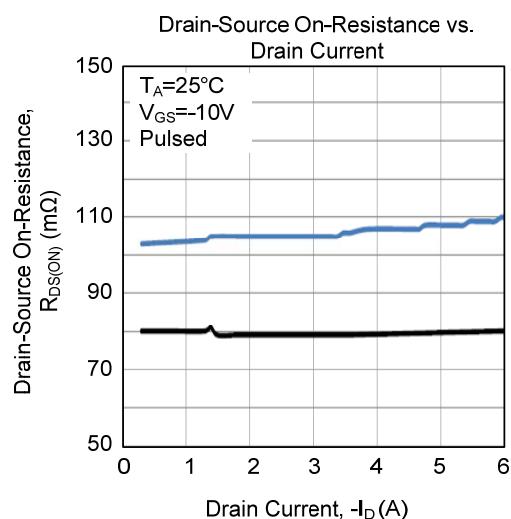
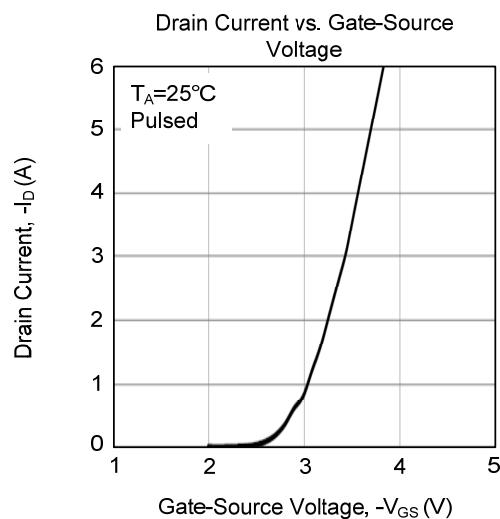
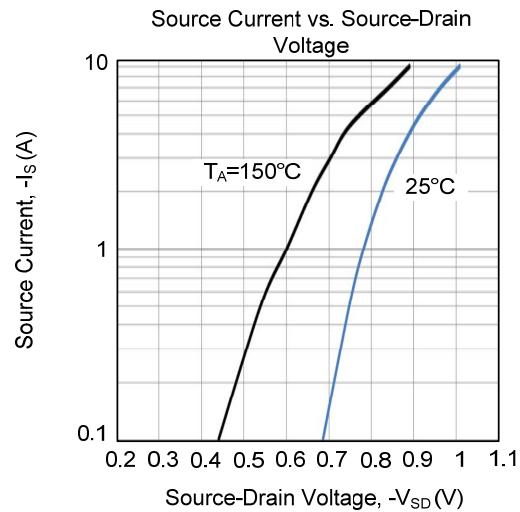
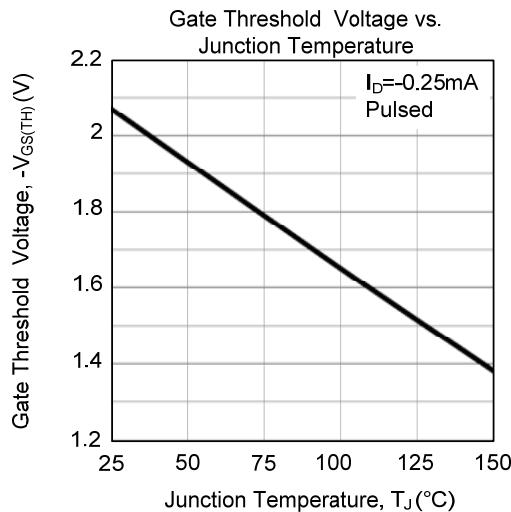
## ■ TYPICAL CHARACTERISTICS (Cont.)

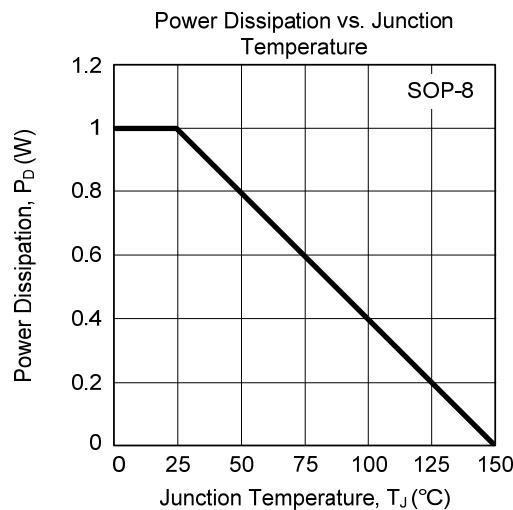
## P-CHANNEL



■ TYPICAL CHARACTERISTICS (Cont.)

P-CHANNEL



**■ TYPICAL CHARACTERISTICS (Cont.)**

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