

TGBR15S100

TRENCH MOS SCHOTTKY BARRIER RECTIFIER

DESCRIPTION

The UTC **TGBR15S100** is a trench mos schottky barrier rectifier, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

FEATURES

* Super low forward voltage drop

* High switching speed

SYMBOL



ORDERING INFORMATION

Ordering	Ordering Number		Pin Assignment			De elsia a	
Lead Free	Halogen Free	Раскаде	Package 1 2		3	Packing	
TGBR15S100L-TF1-T	TGBR15S100G-TF1-T	TO-220F1	Α	К	Α	Tube	

Note: Pin Assignment: A: Anode K: Cathode

TGBR15S100G-TF1-T			
↓	ing Type	(1) T: Tube	
(2)Pack	age Type	(2) TF1: TO-220F1	
(3)Gree	n Package	(3) G: Halogen Free and Lead Free, L: Lead Free	

MARKING





■ ABSOLUTE MAXIMUM RATINGS (PER LEG) (T_A=25°C, unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

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PARAMETER	SYMBOL	RATINGS	UNIT
DC Blocking Voltage	V _{RM}	100	V
Working Peak Reverse Voltage	V _{RWM}	100	V
Peak Repetitive Reverse Voltage	V _{RRM}	100	V
Average Rectified Output Current Per Device	lo	15	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	135	А
Operating Junction Temperature	TJ	-65 ~ +150	°C
Storage Temperature	T _{STG}	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS (PER LEG)

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ _{JA}	62.5	°C/W
Junction to Case	θ _{JC}	3.31	°C/W

■ ELECTRICAL CHARACTERISTICS (PER LEG) (T_A=25°C, unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	V _{(BR)R}	I _R =0.50mA	100			V
Forward Voltage Drop	VEM	I _F =15A, T _J =25°C			0.75	V
		I _F =15A, T _J =125°C			0.65	V
Leakage Current (Note 1)	DM	V _R =100V, T _J =25°C			200	μA
		V _R =100V, T _J =125°C			20	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.

2. Thermal resistance junction to case mounted on heatsink.



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TYPICAL CHARACTERISTICS



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