UNISONIC TECHNOLOGIES CO., LTD

8NM120 Power MOSFET

8.0A, 1200V N-CHANNEL SUPER-JUNCTION MOSFET

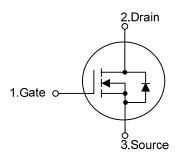
■ DESCRIPTION

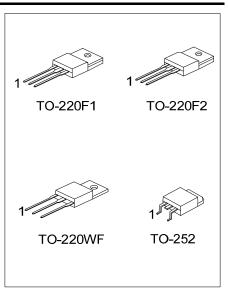
The UTC **8NM120** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

■ FEATURES

- * $R_{DS(ON)} \le 1.35 \Omega @ V_{GS} = 10V, I_D = 4.0A$
- * High Switching Speed

■ SYMBOL

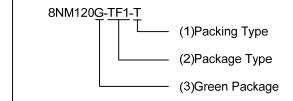




■ ORDERING INFORMATION

| Ordering Number | | Dealtons | Pin Assignment | | | Doolsins | |
|-----------------|---------------|----------|----------------|---|---|-----------|--|
| Lead Free | Halogen Free | Package | 1 | 2 | 3 | Packing | |
| 8NM120L-TF1-T | 8NM120G-TF1-T | TO-220F1 | G | D | S | Tube | |
| 8NM120L-TF2-T | 8NM120G-TF2-T | TO-220F2 | G | D | S | Tube | |
| 8NM120L-TW1-T | 8NM120G-TW1-T | TO-220WF | G | D | S | Tube | |
| 8NM120L-TN3-R | 8NM120G-TN3-R | TO-252 | G | D | S | Tape Reel | |

Note: Pin Assignment: G: Gate D: Drain S: Source

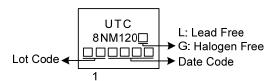


- (1) T: Tube, R: Tape Reel
- (2) TF1: TO-220F1, TF2: TO-220F2, TW1: TO-220WF

TN3: TO-252

(3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING



<u>www.unisonic.com.tw</u> 1 of 8

8NM120 Power MOSFET

■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

| PARAMETER | | SYMBOL | RATINGS | UNIT |
|------------------------------------|-------------------------------|------------------|------------|------|
| Drain-Source Voltage | | V_{DSS} | 1200 | V |
| Gate-Source Voltage | | V_{GSS} | ±30 | V |
| Continuous Drain Current | Continuous | I _D | 8 | Α |
| | Pulsed | I_{DM} | 24 | Α |
| Avalanche Energy | Single Pulsed (Note 3) | E _{AS} | 288 | mJ |
| Peak Diode Recovery dv/dt (Note 4) | | dv/dt | 1.6 | V/ns |
| Power Dissipation | TO-220F1/TO-220F2 TO-220WF | P_D | 29 | W |
| | TO-252 | | 42 | W |
| Junction Temperature | | T_J | +150 | °C |
| Storage Temperature | | T _{STG} | -55 ~ +150 | °C |

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L = 100mH, I_{AS} = 2.4A, V_{DD} = 50V, R_{G} = 25 Ω Starting T_{J} = 25°C
- 4. $I_{SD} \le 8.0$ A, di/dt ≤ 200 A/ μ s, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25$ °C

■ THERMAL DATA

| PARAMETER | | SYMBOL | RATING | UNIT |
|---------------------|-------------------------------|--------|-------------|------|
| Junction to Ambient | TO-220F1/TO-220F2 TO-220WF | θја | 62.5 | °C/W |
| | TO-252 | | 110 | °C/W |
| Junction to Case | TO-220F1/TO-220F2 TO-220WF | θυς | 4.31 | °C/W |
| | TO-252 |] | 2.97 (Note) | °C/W |

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

8NM120 Power MOSFET

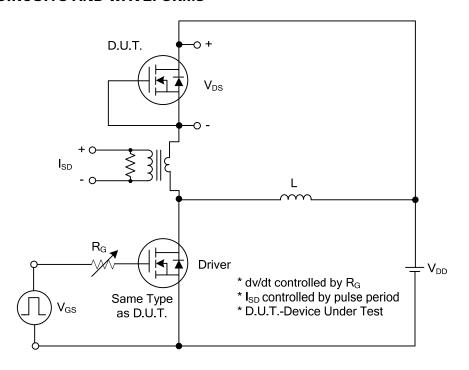
■ **ELECTRICAL CHARACTERISTICS** (T_J=25°C, unless otherwise specified)

| PARAMETER | | SYMBOL | TEST CONDITIONS | MIN | TYP | MAX | UNIT | |
|---|---------|---------------------|---|------|-----|------|------|--|
| OFF CHARACTERISTICS | | | | | | | | |
| Drain-Source Breakdown Voltage | | BV_{DSS} | I_D =250 μ A, V_{GS} =0 V | 1200 | | | V | |
| Drain-Source Leakage Current | | I_{DSS} | V _{DS} =1200V, V _{GS} =0V | | | 10 | μΑ | |
| Gate-Source Leakage Current | Forward | | V_{GS} =+30V, V_{DS} =0V | | | +100 | nA | |
| | Reverse | I _{GSS} | V_{GS} =-30V, V_{DS} =0V | | | -100 | nA | |
| ON CHARACTERISTICS | | | | | | | | |
| Gate Threshold Voltage | | $V_{GS(TH)}$ | $V_{DS}=V_{GS}$, $I_{D}=250\mu A$ | 2.5 | | 4.5 | V | |
| Static Drain-Source On-State Resistance | | R _{DS(ON)} | V _{GS} =10V, I _D =4.0A | | | 1.35 | Ω | |
| DYNAMIC PARAMETERS | | | | | - | | | |
| Input Capacitance | | C_{ISS} | | | 920 | | pF | |
| Output Capacitance | | Coss | V_{GS} =0V, V_{DS} =50V, f=1.0MHz | | 50 | | pF | |
| Reverse Transfer Capacitance | | C_{RSS} | | | 3 | | pF | |
| SWITCHING PARAMETERS | | | | | | | | |
| Total Gate Charge | | Q_G | V _{DS} =960V, V _{GS} =10V, I _D =8.0A | | 52 | | nC | |
| Gate to Source Charge | | Q_GS | (Note 1, 2) | | 12 | | nC | |
| Gate to Drain Charge | | Q_GD | (Note 1, 2) | | 19 | | nC | |
| Turn-ON Delay Time | | $t_{D(ON)}$ | | | 8 | | ns | |
| Rise Time | | t_R | V_{DD} =100V, V_{GS} =10V, I_{D} =8.0A, | | 18 | | ns | |
| Turn-OFF Delay Time | | $t_{D(OFF)}$ | R _G =25Ω (Note 1, 2) | | 39 | | ns | |
| Fall-Time | | t_{F} | | | 23 | | ns | |
| SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS | | | | | | | | |
| Maximum Body-Diode Continuous Current | | Is | | | | 8 | Α | |
| Maximum Body-Diode Pulsed Current | | I_{SM} | | | | 24 | Α | |
| Drain-Source Diode Forward Voltage | | V_{SD} | I _S =8.0A, V _{GS} =0V | | | 1.4 | V | |
| Body Diode Reverse Recovery Time | | t _{rr} | I _S =8.0A, V _{GS} =0V, | | 664 | | ns | |
| Reverse Recovery Charge | | Q_{rr} | dI _F /dt=100A/μs (Note 1) | | 9 | | μC | |

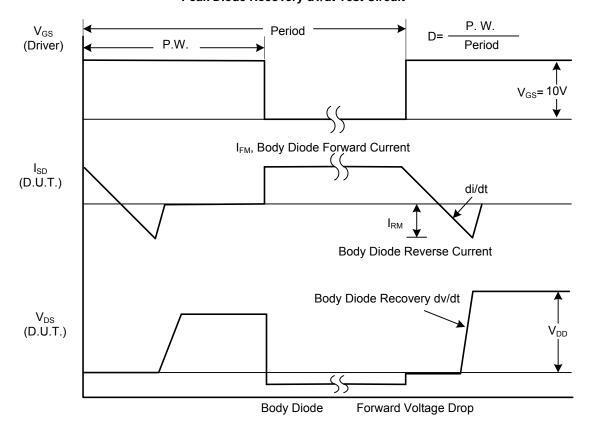
Notes: 1. Pulse Test: Pulse width ≤ 1200µs, Duty cycle ≤ 2%.

^{2.} Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS



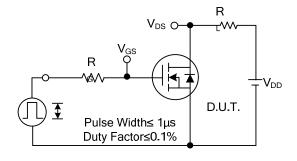
Peak Diode Recovery dv/dt Test Circuit



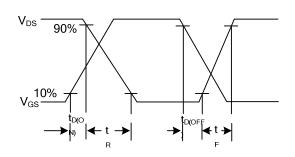
Peak Diode Recovery dv/dt Waveforms

8NM120 Power MOSFET

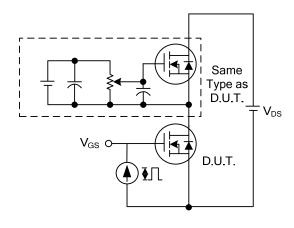
TEST CIRCUITS AND WAVEFORMS



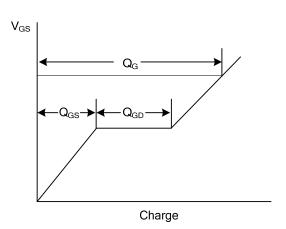
Switching Test Circuit



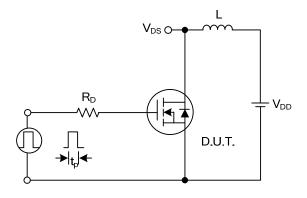
Switching Waveforms



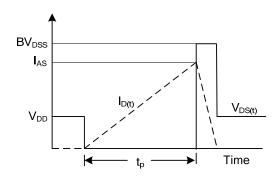
Gate Charge Test Circuit



Gate Charge Waveform

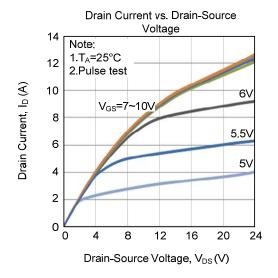


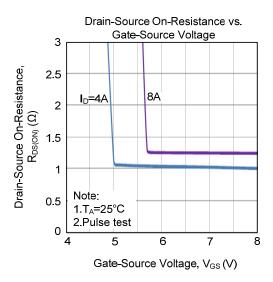
Unclamped Inductive Switching Test Circuit

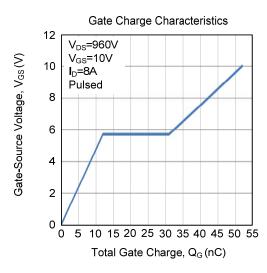


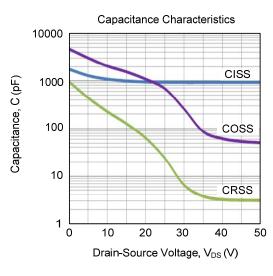
Unclamped Inductive Switching Waveforms

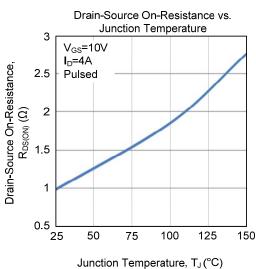
■ TYPICAL CHARACTERISTICS

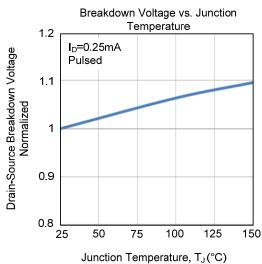




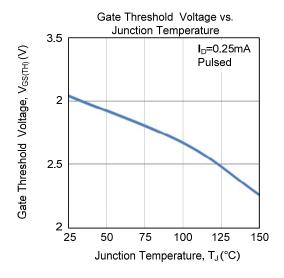


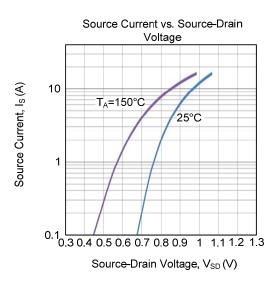


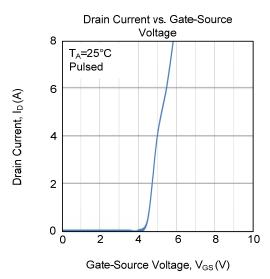


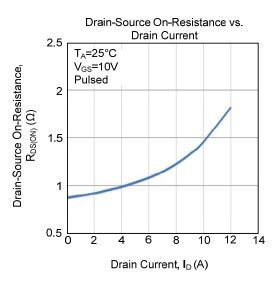


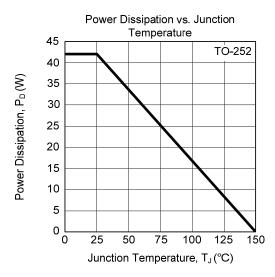
■ TYPICAL CHARACTERISTICS (Cont.)

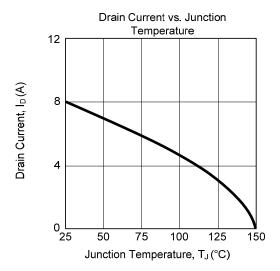






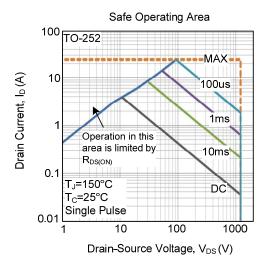






8NM120

TYPICAL CHARACTERISTICS (Cont.)



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