

UNISONIC TECHNOLOGIES CO., LTD

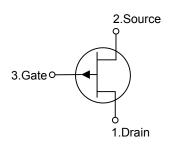
MMBFJ177 JFET

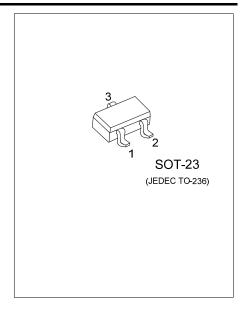
P-CHANNEL SWITCH

■ DESCRIPTION

The UTC **MMBFJ177** is designed for low level analog switching sample and hold circuits and chopper stabilized amplifiers.

■ SYMBOL

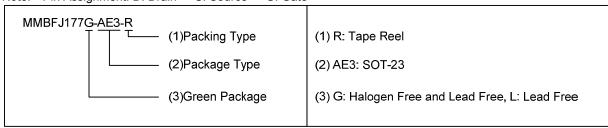




■ ORDERING INFORMATION

Ordering Number		Daalaasa	Pin Assignment			Deaking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
MMBFJ177G-AE3-R	MMBFJ177G-AE3-R	SOT-23	D	S	G	Tape Reel	

Note: Pin Assignment: D: Drain S: Source G: Gate



MARKING



MMBFJ177 JFET

■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATING	UNIT
Drain-Gate Voltage	V_{DG}	-30	V
Gate-Source Voltage	V_{GS}	30	V
Forward Gate Current	I_{GF}	50	mA
Power Dissipation	P _D	225	mW
Junction Temperature	TJ	-55 ~ + 150	°C
Storage Temperature	T _{STG}	-55 ~ + 150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT	
Junction to Ambient	θ_{JA}	556	°C/W	

Note: Device mounted on FR-4 PCB 36mm × 18mm × 1.5mm, mounting pad for the collector lead minimum 6cm².

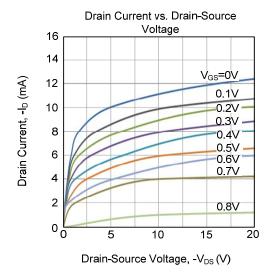
■ ELECTRICAL CHARACTERISTICS (T_A=25°C, unless otherwise specified)

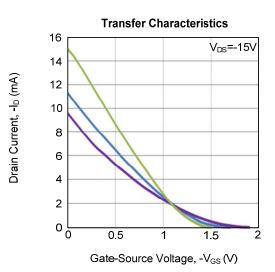
PARAMETER	SYMBOL	TEST CONDITIONS		TYP	MAX	UNIT	
OFF CHARACTERISTICS							
Gate-Source Breakdown Voltage	$V_{(BR)GSS}$	I _G =1.0μA, V _{DS} =0	30			V	
Gate Reverse Current (Note)	I_{GSS}	V _{GS} =20V, V _{DS} =0			1.0	nA	
Gate-Source Cut-Off Voltage	$V_{GS(OFF)}$	V _{DS} =-15V, I _D =-10nA	0.8		2.5	V	
ON CHARACTERISTICS							
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} =-15V, V _{GS} =0	-1.5		-20	mA	
Drain-Source On Resistance	$R_{DS(ON)}$	$V_{DS} \le 0.1V$, $V_{GS}=0$			300	Ω	

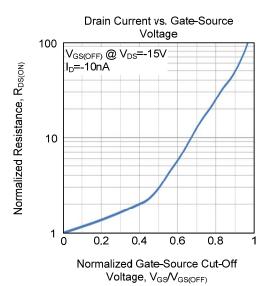
Note: Pulse test: pulse width \leq 300 µs, duty cycle \leq 2%.

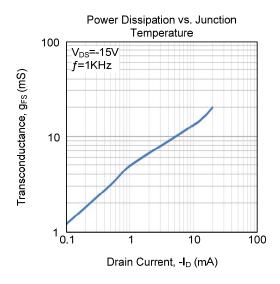
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■ TYPICAL CHARACTERISTICS









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