UNISONIC TECHNOLOGIES CO., LTD

UTG75N65-S

Insulated Gate Bipolar Transistor

650V TRENCH GATE FIELD-STOP IGBT

DESCRIPTION

The UTC UTG75N65-S is an Trench Field-Stop Insulated Gate Bipolar Transistor. it uses UTC's advanced technology to provide customers with high switching speed, low saturation voltage and low switching loss, etc.

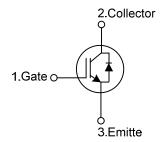
The UTC UTG75N65-S is suitable for the resonant or soft switching applications.

TO-247

FEATURES

- * High switching speed
- * High avalanche ruggedness
- * Low saturation voltage: VCE(SAT).Typ.=1.65V @ IC=75A, VGE=15V $(T_C = 25^{\circ}C)$

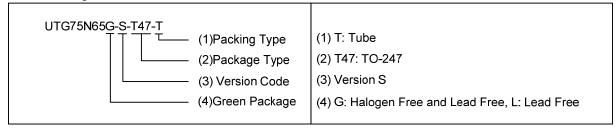
SYMBOL



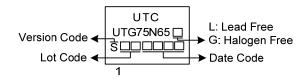
ORDERING INFORMATION

Ordering Number		Daalaana	Pin Assignment			Daakina	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UTG75N65L-S-T47-T	UTG75N65G-S-T47-T	TO-247	G	С	E	Tube	

C: Collector Note: Pin Assignment: G: Gate E: Emitter



MARKING



www.unisonic.com.tw 1 of 6

■ **ABSOLUTE MAXIMUM RATINGS** (T_A=25°C, unless otherwise noted)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector-Emitter Voltage	V _{CES}	650	V
Gate-Emitter Voltage		±20	V
Transient Gate-emitter voltage (tp < 5 ms)	V_{GES}	±25	V
T _C =25°C		150	Α
Continuous Collector Current T _C =100°C	Ic	75	Α
Collector Current Pulsed (Note 1)	I _{CM}	300	Α
T _C =25°C	l _F	150	Α
Diode Forward Current T _C =100°C		75	Α
Short Circuit Withstand Time	tsc		
$V_{GE} = 15V, V_{CC} \le 200V$			μs
Allowed number of short circuits < 1000		3	
Time between short circuits: ≥1.0s			
T_{VJ} = 25°C			
Power Dissipation (T _C =25°C)	P _D	310	W
Operating Junction Temperature	TJ	-40 ~ +150	°C
Storage Temperature Range	T _{STG}	-55 ~ + 150	°C

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

■ THERMAL DATA

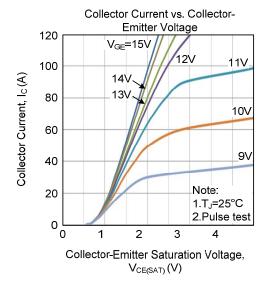
PARAMETER	SYMBOL	RATING	UNIT
Junction to Case	θјς	0.4	°C/W

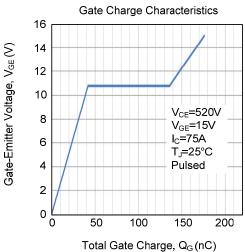
^{2.} Pulse width limited by maximum junction temperature.

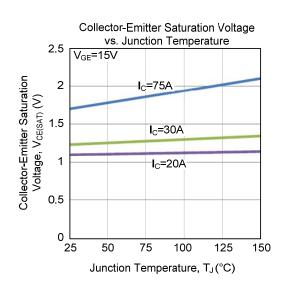
■ **ELECTRICAL CHARACTERISTICS** (T_C=25°C, unless otherwise noted)

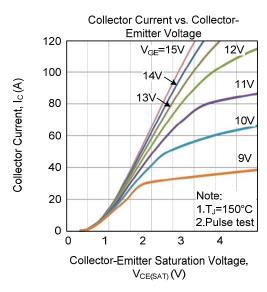
PARAMETER	SYMBOL	TEST CONDITIONS			TYP	MAX	UNIT	
Off Characteristics				·				
Collector-Emitter Breakdown Voltage	BV _{CES}			650			V	
Collector Cut-Off Current	I _{CES}	V _{CE} =650V, V _{GE} =0V				5	μΑ	
G-E Leakage Current	I _{GES}	V _{CE} =0V, V _{GE} =±20V				±400	nA	
On Characteristics								
Gate to Emitter Threshold Voltage	$V_{GE(TH)}$	I _C =250μA, V _{CE} =V _{GE}		4.5		6.5	V	
Collector to Emitter Saturation Voltage	V _{CE(SAT)}	I _C =75A, V _{GE} =15V	T _C =25°C		1.65	2.1	V	
			T _C =125°C		2.0		V	
Dynamic Characteristics								
Input Capacitance	C _{IES}			4240		pF		
Output Capacitance	C _{OES}	V _{CE} =25V, V _{GE} =0V, f=1		370		pF		
Reverse Transfer Capacitance	C _{RES}				70		pF	
Switching Characteristics								
Total Gate Charge	Q_G				180		nC	
Gate-Emitter Charge	Q_GE	V _{CE} =520V, I _C =75A, V _{GE} =15V			41		nC	
Gate-Collector Charge	Q_{GC}				97		nC	
Turn-On Delay Time	t _{DON)}	V _{CC} =400V, I _C =75A, R _G =5Ω, V _{GE} =0~15V, L=100μH			25		ns	
Rise Time	t_R				100		ns	
Turn-Off Delay Time	t _{DOFF)}				125		ns	
Fall Time	t_{F}				65		ns	
Turn-On Switching Loss	Eon				4.27		mJ	
Turn-Off Switching Loss	E _{OFF}]			2.37		mJ	
SOURCE- DRAIN DIODE RATINGS AN	D CHARACTE	RISTICS			ā.			
Forward Voltage Drop	V _F	I _F =75A			1.6	2.0	V	
Reverse Recovery Time	t _{rr}	1 75A JUH 400A (-0) / 400Y			52		ns	
Reverse Recovery Charge	Qrr	I _F =75A, dI/dt=100A/µS, V _{CC} =400V			629		nC	

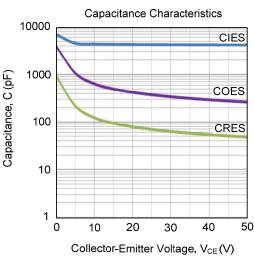
■ TYPICAL CHARACTERISTICS

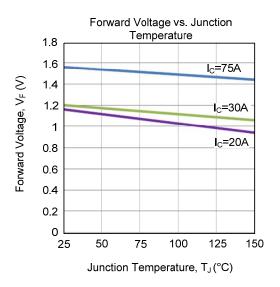




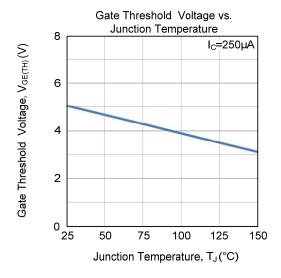


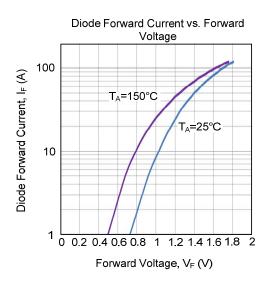


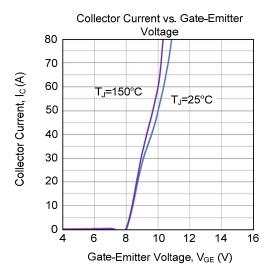


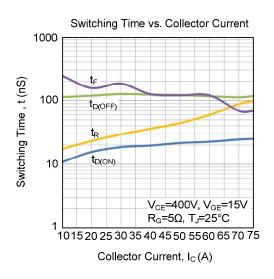


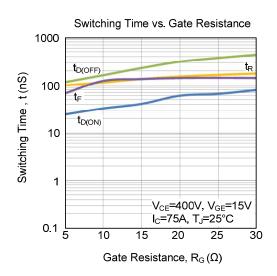
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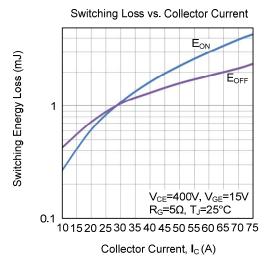




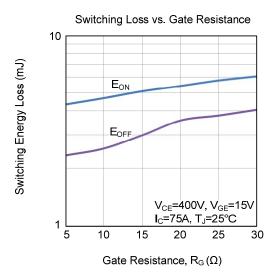


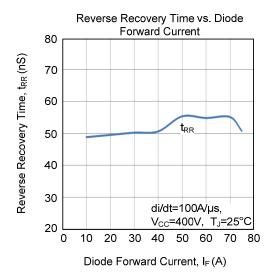


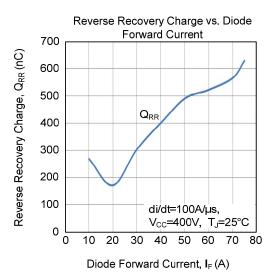


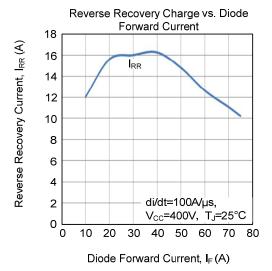


■ TYPICAL CHARACTERISTICS (Cont.)









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