



# 78DXXAS

## LINEAR INTEGRATED CIRCUIT

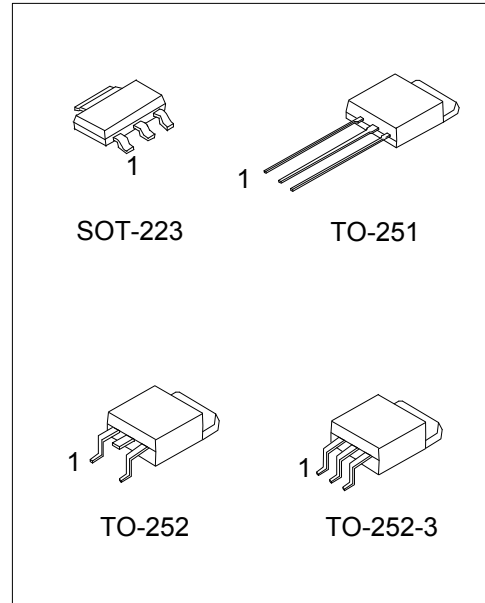
### 3-TERMINALS 1A POSITIVE VOLTAGE REGULATOR

#### DESCRIPTION

The UTC **78DXXAS** family is monolithic fixed voltage regulator integrated circuit. They are suitable for applications that required supply current up to 1 A.

#### FEATURES

- \* Peak output current up to 1A.
- \* Fixed output voltage of 5V, 6V, 7V, 8V, 9V, 10V, 12V, 15V, 18V, 20V and 24V available.
- \* Thermal overload shutdown protection.
- \* Short circuit current limiting.
- \* Output transistor SOA protection.



#### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
78DXXASL-AA3-R	78DXXASG-AA3-R	SOT-223	I	G	O	Tape Reel
78DXXASL-TM3-T	78DXXASG-TM3-T	TO-251	I	G	O	Tube
78DXXASL-TN3-R	78DXXASG-TN3-R	TO-252	I	G	O	Tape Reel
78DXXASL-TNA-R	78DXXASG-TNA-R	TO-252-3	I	G	O	Tape Reel

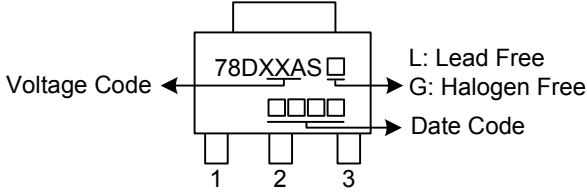
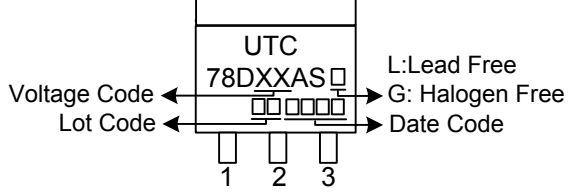
Note: Pin Code: I: Input G: GND O: Output

<p>78DXXASG-AA3-R</p>	<p>(1) R: Tape Reel, T: Tube</p> <p>(2) AA3: SOT-223, TM3: TO-251, TN3: TO-252, TNA: TO-252-3</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p> <p>(4) XX: refer to Marking Information</p>
-----------------------	--

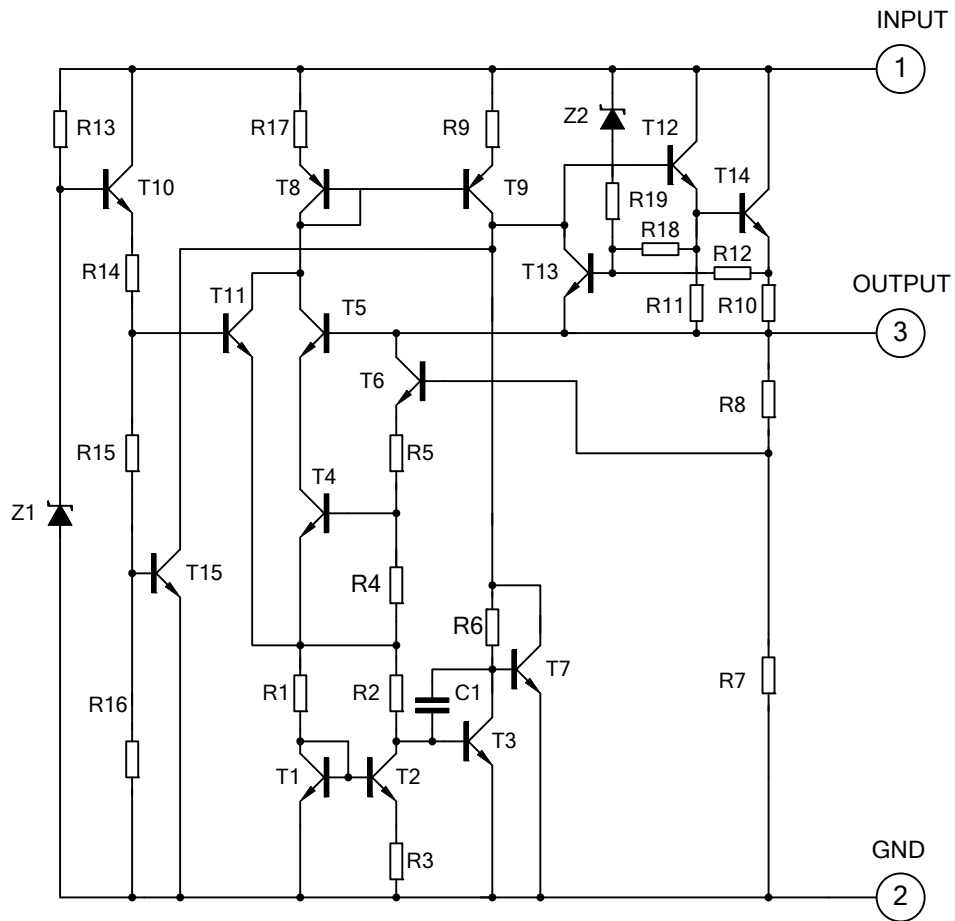
# 78DXXAS

## LINEAR INTEGRATED CIRCUIT

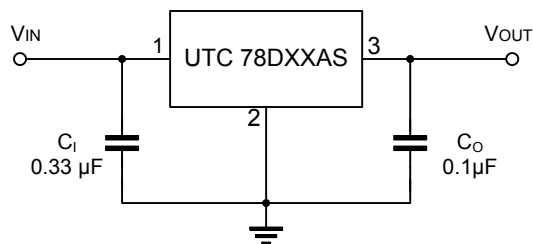
### MARKING INFORMATION

PACKAGE	VOLTAGE CODE	MARKING
SOT-223	05: 5V 06: 6V 07: 7V 08: 8V 09: 9V 10: 10V	 <p>Diagram of SOT-223 package marking. The package is a small surface-mount component with three pins labeled 1, 2, and 3. The top surface is marked with '78DXXAS' followed by a small square. To the left of the package, an arrow points to the 'X' characters, labeled 'Voltage Code'. To the right, an arrow points to the 'AS' characters, labeled 'L: Lead Free' and 'G: Halogen Free'. Below the package, an arrow points to the 'XX' characters, labeled 'Date Code'.</p>
TO-251 TO-252 TO-252-3	12: 12V 15: 15V 18: 18V 20: 20V 24: 24V	 <p>Diagram of TO-251/252/252-3 package marking. The package is a larger component with three pins labeled 1, 2, and 3. The top surface is marked with 'UTC' above '78DXXAS' followed by a small square. To the left, an arrow points to the 'X' characters, labeled 'Voltage Code'. To the right, an arrow points to the 'AS' characters, labeled 'L: Lead Free' and 'G: Halogen Free'. Below the package, an arrow points to the 'XX' characters, labeled 'Date Code'. To the left of the package, an arrow points to the '78D' characters, labeled 'Lot Code'.</p>

■ BLOCK DIAGRAM



■ APPLICATION CIRCUIT



Note: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.

■ ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT	
Input Voltage	$V_{IN}$	$V_{OUT}=5\sim 20\text{V}$	35	V
		$V_{OUT}=24\text{V}$	40	V
Output Current	$I_{OUT}$	1	A	
Power Dissipation ( $T_C=25^\circ\text{C}$ )	$P_D$	SOT-223	0.67	W
		TO-251/TO-252	0.89	W
		TO-252-3		
Junction Temperature	$T_J$	+150	$^\circ\text{C}$	
Operating Temperature	$T_{OPR}$	-40 ~ +125	$^\circ\text{C}$	
Storage Temperature	$T_{STG}$	-55 ~ +150	$^\circ\text{C}$	

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

The device could be damaged beyond Absolute maximum ratings.

2. The maximum steady state usable output current are dependent on input voltage, heat sinking, lead length of the package and copper pattern of PCB. The data are showed as electrical characteristics table represents pulse test conditions with junction temperatures specified at the initiation of test.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	$\theta_{JA}$	SOT-223	150	$^\circ\text{C/W}$
		TO-251/TO-252	112	$^\circ\text{C/W}$
		TO-252-3		
Junction to Case	$\theta_{JC}$	SOT-223	15	$^\circ\text{C/W}$
		TO-251/TO-252	12.5	$^\circ\text{C/W}$
		TO-252-3		

■ ELECTRICAL CHARACTERISTICS

( $T_J=25^\circ\text{C}$ ,  $C_i=0.33\mu\text{F}$ ,  $C_o=0.1\mu\text{F}$ , unless otherwise specified)

For 78D05AS ( $V_{IN}=10\text{V}$ ,  $I_{OUT}=1.0\text{A}$ )

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	$V_{OUT}$	$I_{OUT}=5\text{mA}\sim 1.0\text{A}$	4.90	5.0	5.10	V
		$V_{IN}=7.5\sim 20\text{V}$ , $I_{OUT}=5\text{mA}\sim 1.0\text{A}$	4.85		5.15	V
Load Regulation	$\Delta V_{OUT}$	$I_{OUT}=5\text{mA}\sim 1.0\text{A}$			50	mV
		$I_{OUT}=0.25\text{A}\sim 0.75\text{A}$			25	mV
Line Regulation	$\Delta V_{OUT}$	$V_{IN}=7\sim 25\text{V}$			50	mV
		$V_{IN}=7.5\sim 20\text{V}$ , $I_{OUT}=1.0\text{A}$			50	mV
Quiescent Current	$I_Q$	$I_{OUT}\leq 1.0\text{A}$			8.0	mA
Quiescent Current Change	$\Delta I_Q$	$V_{IN}=7.5\sim 20\text{V}$			1.0	mA
		$I_{OUT}=5\text{mA}\sim 1.0\text{A}$			0.5	mA
Output Noise Voltage	eN	$10\text{Hz}\leq f\leq 100\text{kHz}$		40		$\mu\text{V}$
Ripple Rejection	RR	$V_{IN}=8\sim 18\text{V}$ , $f=120\text{Hz}$	45	55		dB
Peak Output Current	$I_{PEAK}$			1.8		A
Short-Circuit Current	$I_{SC}$	$V_{IN}=35\text{V}$		250		mA
Dropout Voltage	$V_D$			2.0		V

■ ELECTRICAL CHARACTERISTICS(Cont.)

For 78D06AS ( $V_{IN}=11V$ ,  $I_{OUT}=1.0A$ )

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	$V_{OUT}$	$I_{OUT}=5mA\sim 1.0A$	5.88	6.0	6.12	V
		$V_{IN}=8.5\sim 21V$ , $I_{OUT}=5mA\sim 1.0A$	5.82		6.18	V
Load Regulation	$\Delta V_{OUT}$	$I_{OUT}=5mA\sim 1.0A$			60	mV
		$I_{OUT}=0.25A\sim 0.75A$			30	mV
Line Regulation	$\Delta V_{OUT}$	$V_{IN}=8\sim 25V$			60	mV
		$V_{IN}=8.5\sim 21V$ , $I_{OUT}=1.0A$			60	mV
Quiescent Current	$I_Q$	$I_{OUT}\leq 1.0A$			8.0	mA
Quiescent Current Change	$\Delta I_Q$	$V_{IN}=8.5\sim 21V$			1.0	mA
		$I_{OUT}=5mA\sim 1.0A$			0.5	mA
Output Noise Voltage	eN	$10Hz\leq f\leq 100kHz$		45		$\mu V$
Ripple Rejection	RR	$V_{IN}=9\sim 19V$ , $f=120Hz$	45	55		dB
Peak Output Current	$I_{PEAK}$			1.8		A
Short-Circuit Current	$I_{SC}$	$V_{IN}=35V$		250		mA
Dropout Voltage	$V_D$			2.0		V

For 78D07AS ( $V_{IN}=13V$ ,  $I_{OUT}=1.0A$ )

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	$V_{OUT}$	$I_{OUT}=5mA\sim 1.0A$	6.86	7.0	7.14	V
		$V_{IN}=9.5\sim 22V$ , $I_{OUT}=5mA\sim 1.0A$	6.79		7.21	V
Load Regulation	$\Delta V_{OUT}$	$I_{OUT}=5mA\sim 1.0A$			70	mV
		$I_{OUT}=0.25A\sim 0.75A$			35	mV
Line Regulation	$\Delta V_{OUT}$	$V_{IN}=9\sim 25V$			70	mV
		$V_{IN}=9.5\sim 22V$ , $I_{OUT}=1.0A$			70	mV
Quiescent Current	$I_Q$	$I_{OUT}\leq 1.0A$			8.0	mA
Quiescent Current Change	$\Delta I_Q$	$V_{IN}=9.5\sim 22V$			1.0	mA
		$I_{OUT}=5mA\sim 1.0A$			0.5	mA
Output Noise Voltage	eN	$10Hz\leq f\leq 100kHz$		50		$\mu V$
Ripple Rejection	RR	$V_{IN}=10.5V\sim 20.5V$ , $f=120Hz$	45	55		dB
Peak Output Current	$I_{PEAK}$			1.7		A
Short-Circuit Current	$I_{SC}$	$V_{IN}=35V$		250		mA
Dropout Voltage	$V_D$			2.0		V

For 78D08AS ( $V_{IN}=14V$ ,  $I_{OUT}=1.0A$ )

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	$V_{OUT}$	$I_{OUT}=5mA\sim 1.0A$	7.84	8.0	8.16	V
		$V_{IN}=10.5\sim 23V$ , $I_{OUT}=5mA\sim 1.0A$	7.76		8.24	V
Load Regulation	$\Delta V_{OUT}$	$I_{OUT}=5mA\sim 1.0A$			80	mV
		$I_{OUT}=0.25A\sim 0.75A$			40	mV
Line Regulation	$\Delta V_{OUT}$	$V_{IN}=10.5\sim 25V$			80	mV
		$V_{IN}=10.5\sim 23V$ , $I_{OUT}=1.0A$			80	mV
Quiescent Current	$I_Q$	$I_{OUT}\leq 1.0A$			8.0	mA
Quiescent Current Change	$\Delta I_Q$	$V_{IN}=10.5\sim 23V$			1.0	mA
		$I_{OUT}=5mA\sim 1.0A$			0.5	mA
Output Noise Voltage	eN	$10Hz\leq f\leq 100kHz$		58		$\mu V$
Ripple Rejection	RR	$V_{IN}=11.5\sim 21.5V$ , $f=120Hz$	45	55		dB
Peak Output Current	$I_{PEAK}$			1.8		A
Short-Circuit Current	$I_{SC}$	$V_{IN}=35V$		250		mA
Dropout Voltage	$V_D$			2.0		V

■ ELECTRICAL CHARACTERISTICS(Cont.)

For 78D09AS ( $V_{IN}=15V$ ,  $I_{OUT}=1.0A$ )

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	$V_{OUT}$	$I_{OUT}=5mA\sim 1.0A$	8.82	9.0	9.18	V
		$V_{IN}=11.5\sim 24V$ , $I_{OUT}=5mA\sim 1.0A$	8.73		9.27	V
Load Regulation	$\Delta V_{OUT}$	$I_{OUT}=5mA\sim 1.0A$			90	mV
		$I_{OUT}=0.25A\sim 0.75A$			45	mV
Line Regulation	$\Delta V_{OUT}$	$V_{IN}=11.5\sim 25V$			90	mV
		$V_{IN}=11.5\sim 24V$ , $I_{OUT}=1.0A$			90	mV
Quiescent Current	$I_Q$	$I_{OUT}\leq 1.0A$			8.0	mA
Quiescent Current Change	$\Delta I_Q$	$V_{IN}=11.5\sim 24V$			1.0	mA
		$I_{OUT}=5mA\sim 1.0A$			0.5	mA
Output Noise Voltage	eN	$10Hz\leq f\leq 100kHz$		58		$\mu V$
Ripple Rejection	RR	$V_{IN}=12.5\sim 22.5V$ , $f=120Hz$	45	55		dB
Peak Output Current	$I_{PEAK}$			1.8		A
Short-Circuit Current	$I_{SC}$	$V_{IN}=35V$		250		mA
Dropout Voltage	$V_D$			2.0		V

For 78D10AS ( $V_{IN}=16V$ ,  $I_{OUT}=1.0A$ )

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	$V_{OUT}$	$I_{OUT}=5mA\sim 1.0A$	9.8	10	10.2	V
		$V_{IN}=12.5\sim 25V$ , $I_{OUT}=5mA\sim 1.0A$	9.7		10.3	V
Load Regulation	$\Delta V_{OUT}$	$I_{OUT}=5mA\sim 1.0A$			100	mV
		$I_{OUT}=0.25A\sim 0.75A$			50	mV
Line Regulation	$\Delta V_{OUT}$	$V_{IN}=13\sim 25V$			100	mV
		$V_{IN}=13\sim 25V$ , $I_{OUT}=1.0A$			100	mV
Quiescent Current	$I_Q$	$I_{OUT}\leq 1.0A$			8.0	mA
Quiescent Current Change	$\Delta I_Q$	$V_{IN}=12.6V\sim 25V$			1.0	mA
		$I_{OUT}=5mA\sim 1.0A$			0.5	mA
Output Noise Voltage	eN	$10Hz\leq f\leq 100kHz$		58		$\mu V$
Ripple Rejection	RR	$V_{IN}=13\sim 23V$ , $f=120Hz$	45	55		dB
Peak Output Current	$I_{PEAK}$			1.8		A
Short-Circuit Current	$I_{SC}$	$V_{IN}=35V$		250		mA
Dropout Voltage	$V_D$			2.0		V

For 78D12AS ( $V_{IN}=19V$ ,  $I_{OUT}=1.0A$ )

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	$V_{OUT}$	$I_{OUT}=5mA\sim 1.0A$	11.76	12	12.24	V
		$V_{IN}=14.5\sim 27V$ , $I_{OUT}=5mA\sim 1.0A$	11.64		12.36	V
Load Regulation	$\Delta V_{OUT}$	$I_{OUT}=5mA\sim 1.0A$			120	mV
		$I_{OUT}=0.25A\sim 0.75A$			60	mV
Line Regulation	$\Delta V_{OUT}$	$V_{IN}=14.5\sim 30V$			120	mV
		$V_{IN}=14.6\sim 27V$ , $I_{OUT}=1.0A$			120	mV
Quiescent Current	$I_Q$	$I_{OUT}\leq 1.0A$			8.0	mA
Quiescent Current Change	$\Delta I_Q$	$V_{IN}=14.5\sim 30V$			1.0	mA
		$I_{OUT}=5mA\sim 1.0A$			0.5	mA
Output Noise Voltage	eN	$10Hz\leq f\leq 100kHz$		75		$\mu V$
Ripple Rejection	RR	$V_{IN}=15\sim 25V$ , $f=120Hz$	45	55		dB
Peak Output Current	$I_{PEAK}$			1.8		A
Short-Circuit Current	$I_{SC}$	$V_{IN}=35V$		250		mA
Dropout Voltage	$V_D$			2.0		V

■ ELECTRICAL CHARACTERISTICS(Cont.)

For 78D15AS ( $V_{IN}=23V$ ,  $I_{OUT}=1.0A$ )

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	$V_{OUT}$	$I_{OUT}=5mA\sim 1.0A$	14.70	15	15.30	V
		$V_{IN}=17.5\sim 30V$ , $I_{OUT}=5mA\sim 1.0A$	14.55		15.45	V
Load Regulation	$\Delta V_{OUT}$	$I_{OUT}=5mA\sim 1.0A$			150	mV
		$I_{OUT}=0.25A\sim 0.75A$			75	mV
Line Regulation	$\Delta V_{OUT}$	$V_{IN}=18.5\sim 30V$			150	mV
		$V_{IN}=17.7\sim 30V$ , $I_{OUT}=1.0A$			150	mV
Quiescent Current	$I_Q$	$I_{OUT}\leq 1.0A$			8.0	mA
Quiescent Current Change	$\Delta I_Q$	$V_{IN}=17.5\sim 30V$			1.0	mA
		$I_{OUT}=5mA\sim 1.0A$			0.5	mA
Output Noise Voltage	eN	$10Hz\leq f\leq 100kHz$		90		$\mu V$
Ripple Rejection	RR	$V_{IN}=18.5\sim 28.5V$ , $f=120Hz$	45	55		dB
Peak Output Current	$I_{PEAK}$			1.8		A
Short-Circuit Current	$I_{SC}$	$V_{IN}=35V$		250		mA
Dropout Voltage	$V_D$			2.0		V

For 78D18AS ( $V_{IN}=27V$ ,  $I_{OUT}=1.0A$ )

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	$V_{OUT}$	$I_{OUT}=5mA\sim 1.0A$	17.64	18	18.36	V
		$V_{IN}=21\sim 33V$ , $I_{OUT}=5mA\sim 1.0A$	17.46		18.54	V
Load Regulation	$\Delta V_{OUT}$	$I_{OUT}=5mA\sim 1.0A$			180	mV
		$I_{OUT}=0.25A\sim 0.75A$			90	mV
Line Regulation	$\Delta V_{OUT}$	$V_{IN}=21\sim 33V$			180	mV
		$V_{IN}=21\sim 33V$ , $I_{OUT}=1.0A$			180	mV
Quiescent Current	$I_Q$	$I_{OUT}\leq 1.0A$			8.0	mA
Quiescent Current Change	$\Delta I_Q$	$V_{IN}=21.5\sim 33V$			1.0	mA
		$I_{OUT}=5mA\sim 1.0A$			0.5	mA
Output Noise Voltage	eN	$10Hz\leq f\leq 100kHz$		110		$\mu V$
Ripple Rejection	RR	$V_{IN}=22\sim 32V$ , $f=120Hz$	45	55		dB
Peak Output Current	$I_{PEAK}$			1.8		A
Short-Circuit Current	$I_{SC}$	$V_{IN}=35V$		250		mA
Dropout Voltage	$V_D$			2.0		V

For 78D20AS ( $V_{IN}=29V$ ,  $I_{OUT}=1.0A$ )

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	$V_{OUT}$	$I_{OUT}=5mA\sim 1.0A$	19.6	20	20.4	V
		$V_{IN}=23\sim 35V$ , $I_{OUT}=5mA\sim 1.0A$	19.4		20.6	V
Load Regulation	$\Delta V_{OUT}$	$I_{OUT}=5mA\sim 1.0A$			200	mV
		$I_{OUT}=0.25A\sim 0.75A$			100	mV
Line Regulation	$\Delta V_{OUT}$	$V_{IN}=23\sim 35V$			200	mV
		$V_{IN}=23\sim 35V$ , $I_{OUT}=1.0A$			200	mV
Quiescent Current	$I_Q$	$I_{OUT}\leq 1.0A$			8.0	mA
Quiescent Current Change	$\Delta I_Q$	$V_{IN}=23.5\sim 35V$			1.0	mA
		$I_{OUT}=5mA\sim 1.0A$			0.5	mA
Output Noise Voltage	eN	$10Hz\leq f\leq 100kHz$		130		$\mu V$
Ripple Rejection	RR	$V_{IN}=24\sim 34V$ , $f=120Hz$	45	55		dB
Peak Output Current	$I_{PEAK}$			1.8		A
Short-Circuit Current	$I_{SC}$	$V_{IN}=35V$		250		mA
Dropout Voltage	$V_D$			2.0		V

## ■ ELECTRICAL CHARACTERISTICS(Cont.)

For 78D24AS ( $V_{IN}=33V$ ,  $I_{OUT}=1.0A$ )

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	$V_{OUT}$	$I_{OUT}=5mA\sim 1.0A$	23.52	24	24.48	V
		$V_{IN}=27\sim 38V$ , $I_{OUT}=5mA\sim 1.0A$	23.28		24.72	V
Load Regulation	$\Delta V_{OUT}$	$I_{OUT}=5mA\sim 1.0A$			240	mV
		$I_{OUT}=0.25A\sim 0.75A$			120	mV
Line Regulation	$\Delta V_{OUT}$	$V_{IN}=27\sim 38V$			240	mV
		$V_{IN}=27\sim 38V$ , $I_{OUT}=1.0A$			240	mV
Quiescent Current	$I_Q$	$I_{OUT}\leq 1.0A$			8.0	mA
Quiescent Current Change	$\Delta I_Q$	$V_{IN}=28\sim 38V$			1.0	mA
		$I_{OUT}=5mA\sim 1.0A$			0.5	mA
Output Noise Voltage	eN	$10Hz\leq f\leq 100kHz$		170		$\mu V$
Ripple Rejection	RR	$V_{IN}=28\sim 38V$ , $f=120Hz$	45	55		dB
Peak Output Current	$I_{PEAK}$			1.8		A
Short-Circuit Current	$I_{SC}$	$V_{IN}=35V$		250		mA
Dropout Voltage	$V_D$			2.0		V



UTC assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all UTC products described or contained herein. UTC products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. UTC reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.