

9NM95Z

Power MOSFET

9.0A, 950V N-CHANNEL SUPER-JUNCTION MOSFET

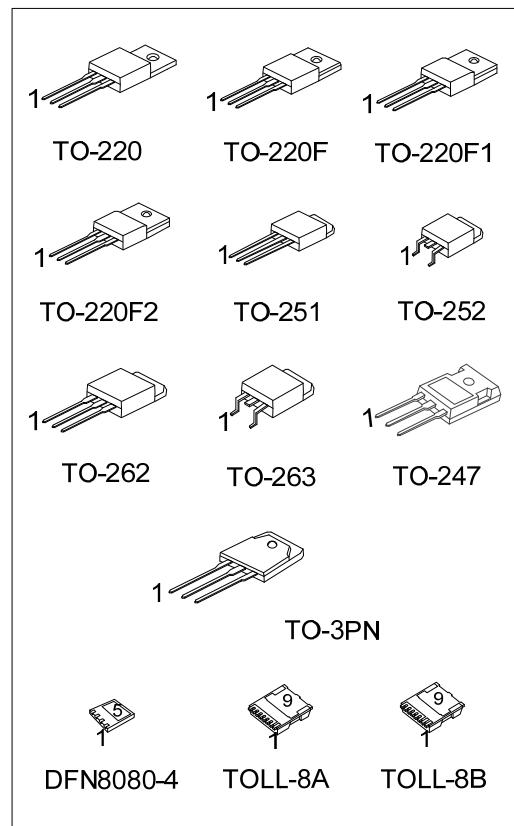
■ DESCRIPTION

The UTC **9NM95Z** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

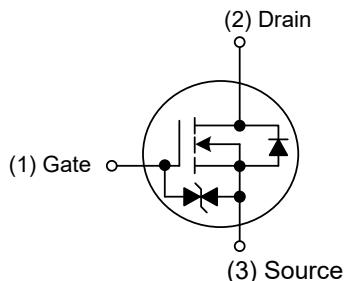
■ FEATURES

* $R_{DS(ON)} \leq 1.4 \Omega$ @ $V_{GS}=10V$, $I_D=4.5A$

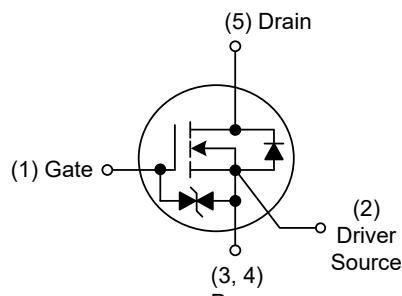
* High Switching Speed



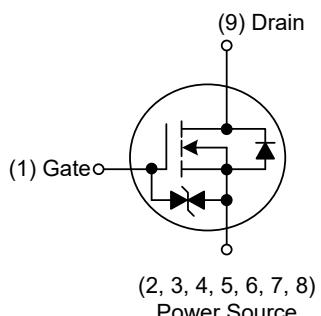
■ SYMBOL



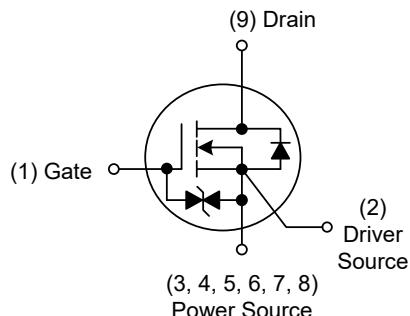
TO-220 / TO-220F / TO-220F1
TO-220F2 / TO-251 / TO-252
TO-247 / TO-262 / TO-263 / TO-3PN



DFN8080-4



TOLL-8A



TOLL-8B

■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment									Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	9	
9NM95ZL-TA3-T	9NM95ZG-TA3-T	TO-220	G	D	S	-	-	-	-	-	-	Tube
9NM95ZL-TF1-T	9NM95ZG-TF1-T	TO-220F1	G	D	S	-	-	-	-	-	-	Tube
9NM95ZL-TF2-T	9NM95ZG-TF2-T	TO-220F2	G	D	S	-	-	-	-	-	-	Tube
9NM95ZL-TF3-T	9NM95ZG-TF3-T	TO-220F	G	D	S	-	-	-	-	-	-	Tube
9NM95ZL-TM3-T	9NM95ZG-TM3-T	TO-251	G	D	S	-	-	-	-	-	-	Tube
9NM95ZL-TN3-R	9NM95ZG-TN3-R	TO-252	G	D	S	-	-	-	-	-	-	Tape Reel
9NM95ZL-T2Q-T	9NM95ZG-T2Q-T	TO-262	G	D	S	-	-	-	-	-	-	Tube
9NM95ZL-TQ2-T	9NM95ZG-TQ2-T	TO-263	G	D	S	-	-	-	-	-	-	Tube
9NM95ZL-TQ2-R	9NM95ZG-TQ2-R	TO-263	G	D	S	-	-	-	-	-	-	Tape Reel
9NM95ZL-T47-T	9NM95ZG-T47-T	TO-247	G	D	S	-	-	-	-	-	-	Tube
9NM95Z L-T3N-T	9NM95Z G-T3N-T	TO-3PN	G	D	S	-	-	-	-	-	-	Tube
9NM95ZL-K04-8080-R	9NM95ZG-K04-8080-R	DFN8080-4	G	S	S	S	D	-	-	-	-	Tape Reel
9NM95ZL-T8A-R	9NM95ZG-T8A-R	TOLL-8A	G	S	S	S	S	S	S	S	D	Tape Reel
9NM95ZL-T8B-R	9NM95ZG-T8B-R	TOLL-8B	G	S	S	S	S	S	S	S	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

 (1)Packing Type (2)Package Type (3)Green Package	(1) T: Tube, R: Tape Reel (2) TA3: TO-220, TF1: TO-220F1, TF2: TO-220F2, TF3: TO-220F, TM3: TO-251, TN3: TO-252, T2Q: TO-262, TQ2: TO-263, T3N: TO-3PN, T47: TO-247, T8A: TOLL-8A, T8B: TOLL-8B, K04-8080: DFN8080-4 (3) G: Halogen Free and Lead Free L: Lead Free
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■ MARKING

TO-220 / TO-220F / TO-220F1 TO-220F2 / TO-251 / TO-252 TO-247 / TO-262 / TO-263 / TO-3PN	DFN8080-4
 Lot Code ← 1 → Date Code	 Lot Code ← → Date Code
TOLL-8A / TOLL-8B	-
 Lot Code ← 1 → Date Code	-

■ ABSOLUTE MAXIMUM RATINGS ($T_c=25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	950	V
Gate-Source Voltage		V_{GSS}	± 20	V
Drain Current	Continuous	I_D	9	A
	Pulsed (Note 2)	I_{DM}	27	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	225	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	1	V/ns
Power Dissipation	TO-220/TO-262	P_D	70	W
	TO-263			
	TO-220F/TO-220F1			
	TO-220F2			
	TO-3PN			
	TO-247			
	TO-251/TO-252			
	DFN8080-4			
	TOLL-8A/TOLL-8A			
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 100\text{mH}$, $I_{AS} = 2.1\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\Omega$ Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 9.0\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT			
Junction to Ambient	TO-220/TO-220F	θ_{JA}	62.5	$^\circ\text{C/W}$			
	TO-220F1/TO-220F2						
	TO-262/TO-263						
	TO-3PN						
	TO-247						
	TO-251/TO-252						
Junction to Case	DFN8080-4/TOLL-8A/TOLL-8A	θ_{JC}	35	$^\circ\text{C/W}$			
	TO-220/TO-262/TO-263						
	TO-220F/TO-220F1						
	TO-220F2						
	TO-3PN						
	TO-247						
	TO-251/TO-252						
DFN8080-4							
TOLL-8A/TOLL-8A							

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

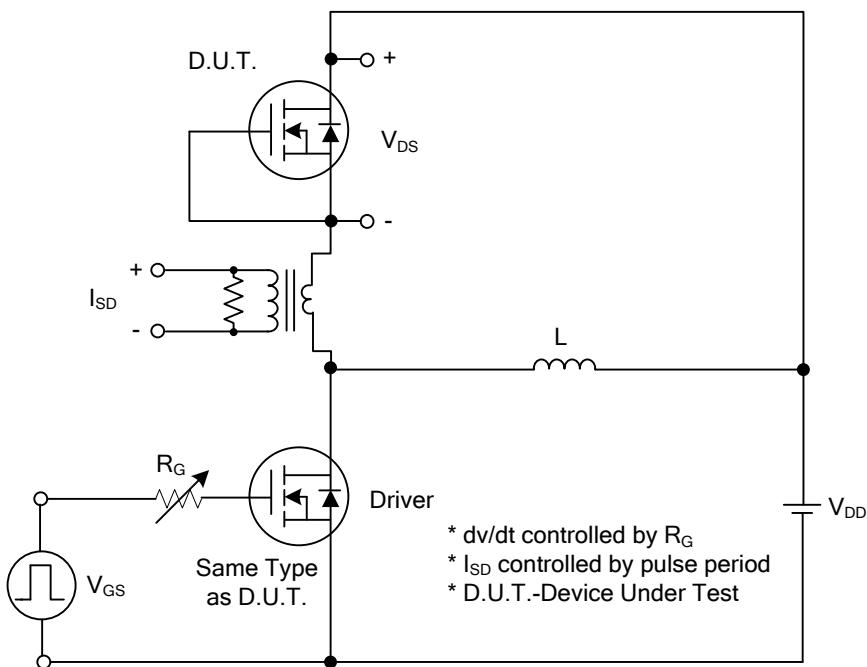
■ ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	950			V
Drain-Source Leakage Current	I_{DSS}	$\text{V}_{\text{DS}}=950\text{V}, \text{V}_{\text{GS}}=0\text{V}$		10		μA
Gate-Source Leakage Current	Forward	$\text{V}_{\text{GS}}=20\text{V}, \text{V}_{\text{DS}}=0\text{V}$		10		μA
	Reverse	$\text{V}_{\text{GS}}=-20\text{V}, \text{V}_{\text{DS}}=0\text{V}$		-10		μA
ON CHARACTERISTICS						
Gate Threshold Voltage	$\text{V}_{\text{GS(TH)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	2.5		4.5	V
Static Drain-Source On-State Resistance	$\text{R}_{\text{DS(ON)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=4.5\text{A}$			1.4	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{ISS}	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=50\text{V}, f=1.0 \text{ MHz}$		676		pF
Output Capacitance	C_{OSS}			54		pF
Reverse Transfer Capacitance	C_{RSS}			2		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge (Note 1)	Q_G	$\text{V}_{\text{DS}}=760\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{I}_D=9.0\text{A}$ (Note 1, 2)		33		nC
Gatesource Charge	Q_{GS}			8		nC
Gate-Drain Charge	Q_{GD}			13		nC
Turn-on Delay Time (Note 1)	$t_{\text{D(ON)}}$	$\text{V}_{\text{DS}}=100\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{I}_D=9.0\text{A},$ $R_G=25\Omega$ (Note 1, 2)		11		ns
Rise Time	t_R			22		ns
Turn-off Delay Time	$t_{\text{D(OFF)}}$			79		ns
Fall-Time	t_F			30		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				9	A
Drain-Source Diode Forward Voltage (Note 1)	V_{SD}	$\text{I}_S=9.0\text{A}, \text{V}_{\text{GS}}=0\text{V}$			1.4	V
Reverse Recovery Time (Note 1)	t_{rr}	$\text{I}_S=9.0\text{A}, \text{V}_{\text{GS}}=0\text{V}$		544		ns
Reverse Recovery Charge	Q_{rr}	$d\text{I}_F/dt=100\text{A}/\mu\text{s}$ (Note 1)		8.7		μC

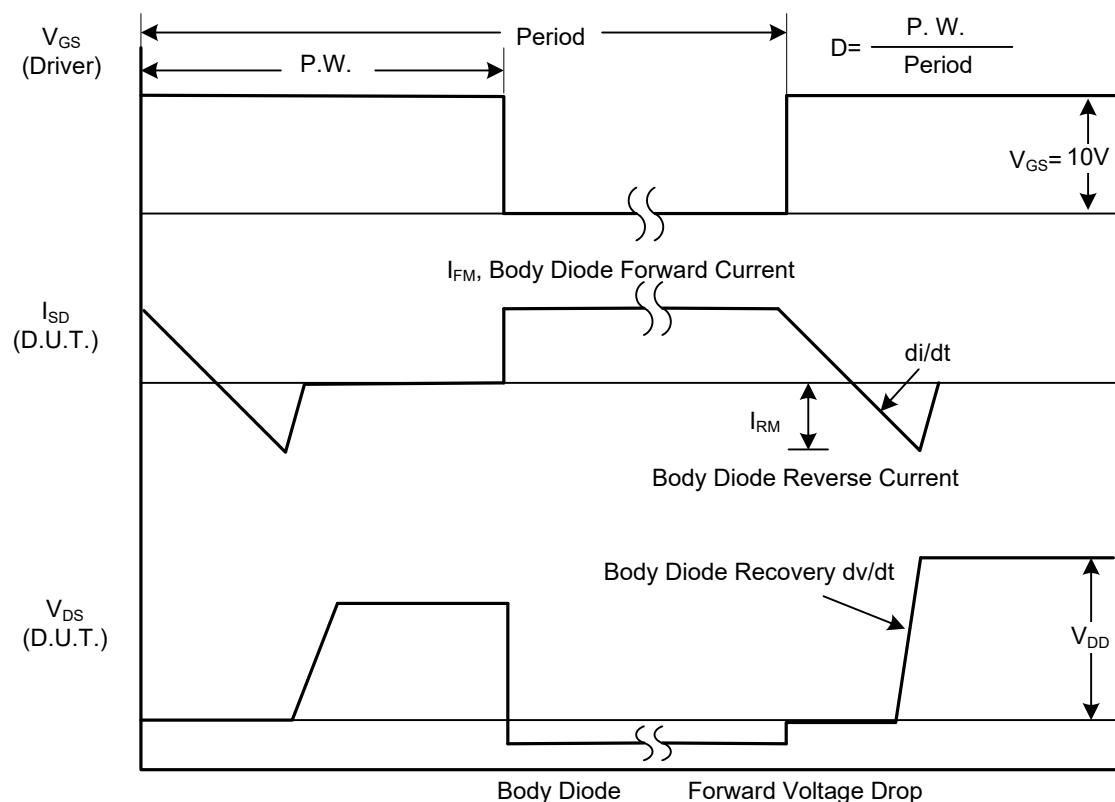
Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

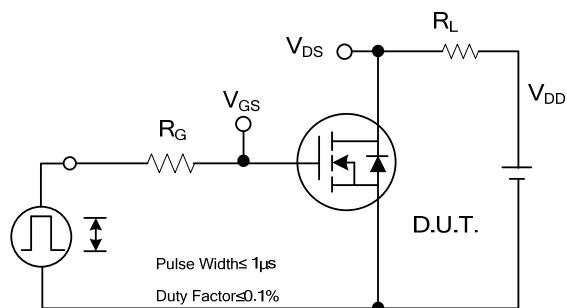


Peak Diode Recovery dv/dt Test Circuit

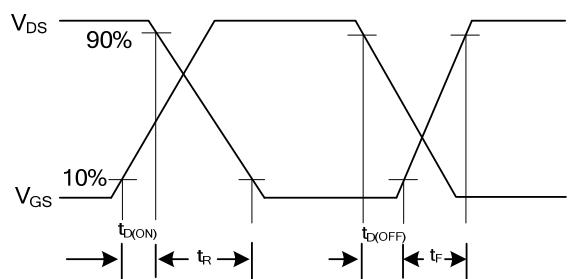


Peak Diode Recovery dv/dt Waveforms

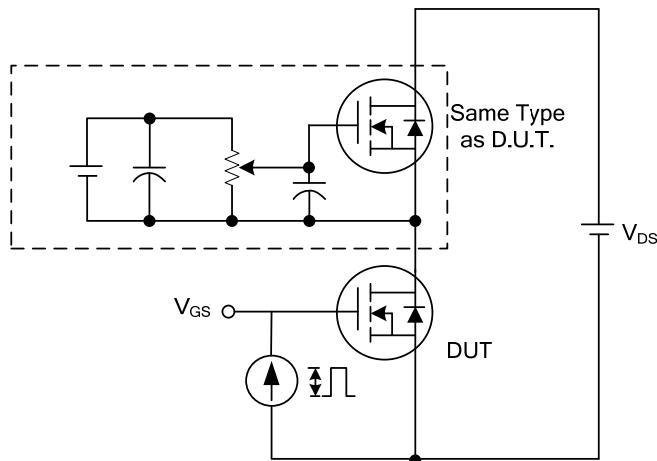
■ TEST CIRCUITS AND WAVEFORMS



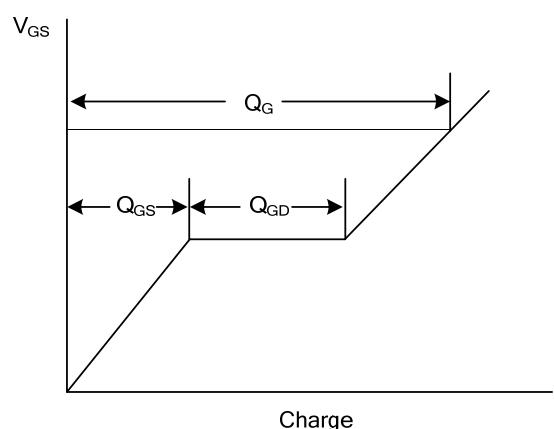
Switching Test Circuit



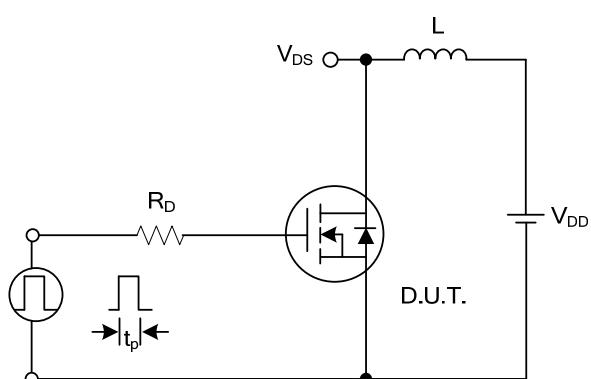
Switching Waveforms



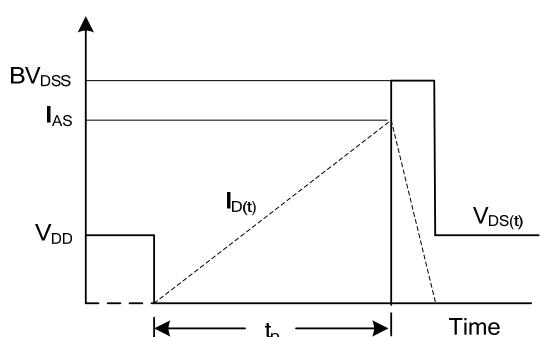
Gate Charge Test Circuit



Gate Charge Waveform

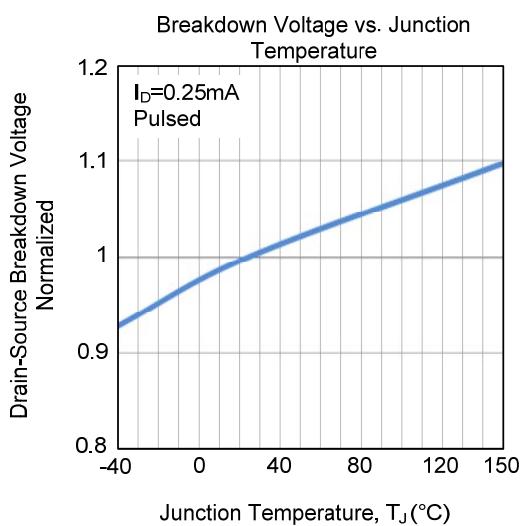
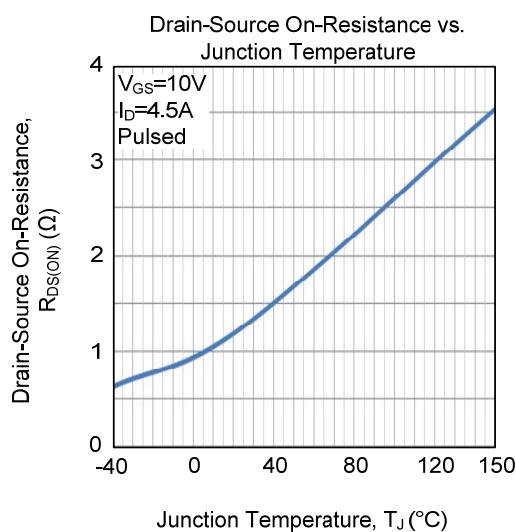
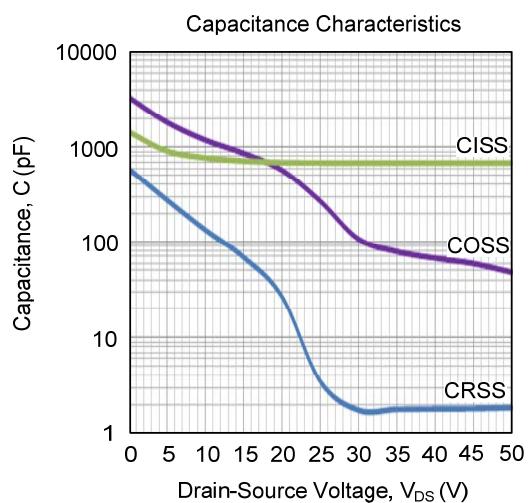
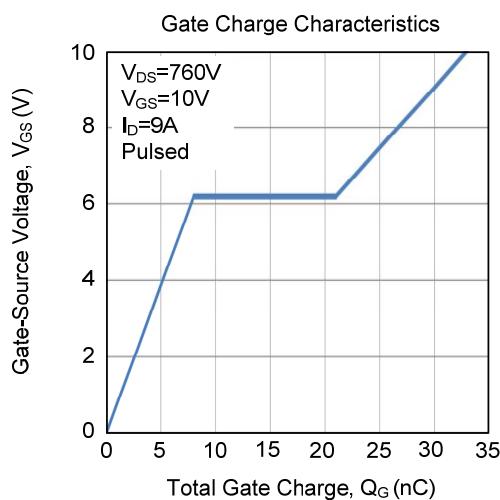
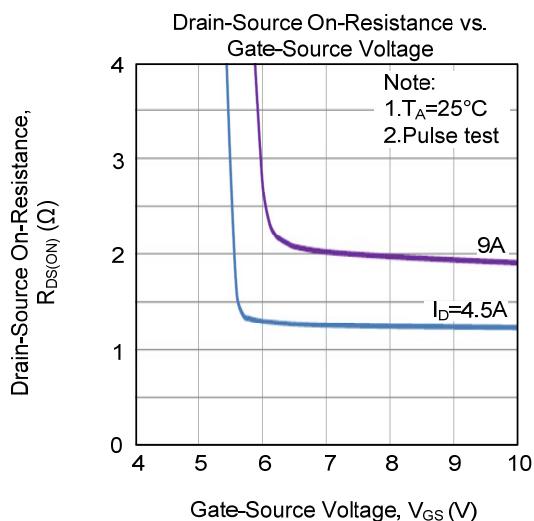
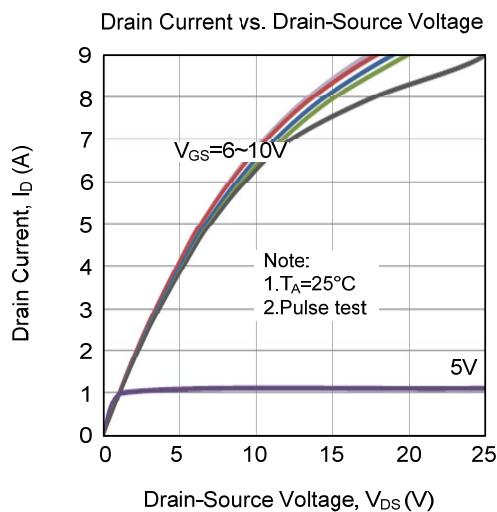


Unclamped Inductive Switching Test Circuit

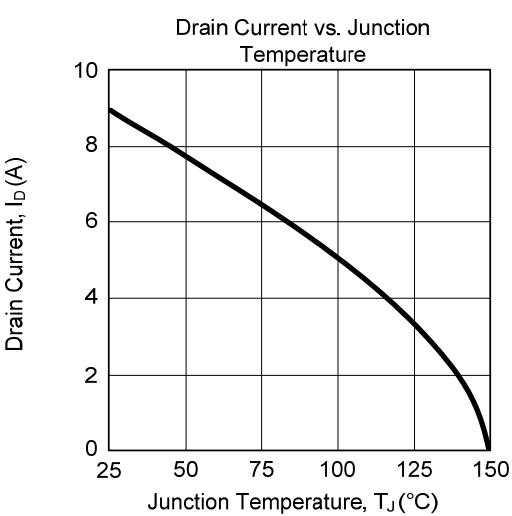
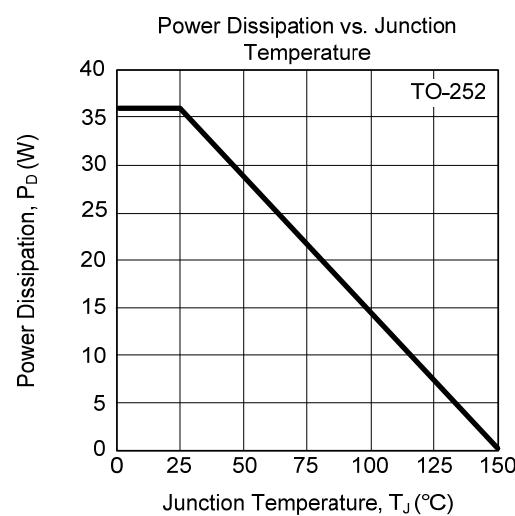
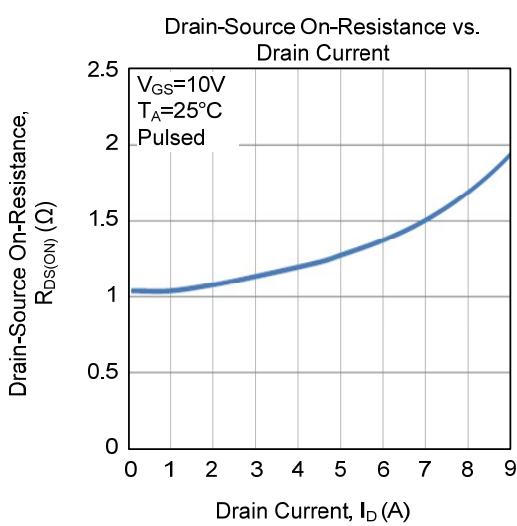
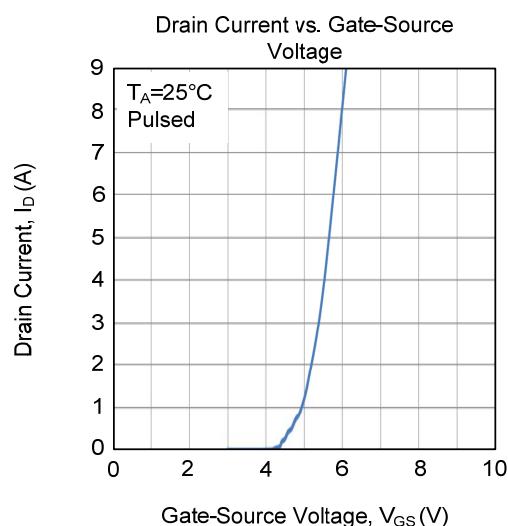
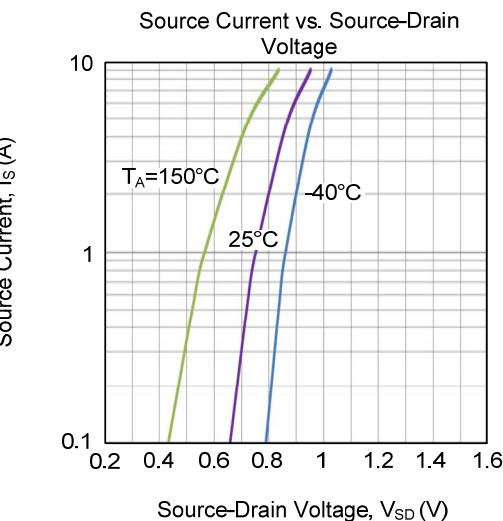
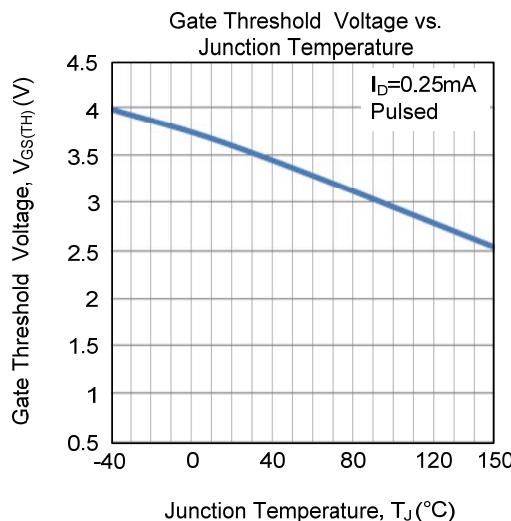


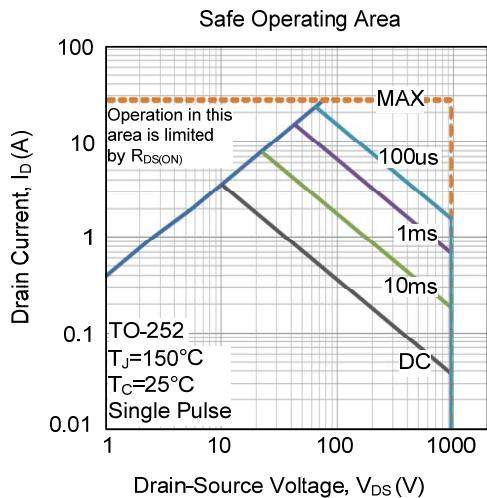
Unclamped Inductive Switching Waveforms

■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)

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