UNISONIC TECHNOLOGIES CO., LTD

UTG10N120

Preliminary

Insulated Gate Bipolar Transistor

1200V, SMPS N-CHANNEL IGBT

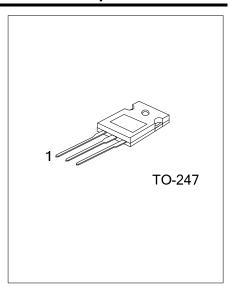
DESCRIPTION

The UTC **UTG10N120** is a N-channel IGBT. it uses UTC's advanced technology to provide customers with high input impedance, high switching speed and low conduction loss, etc.

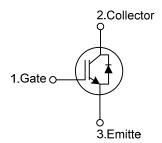
The UTC **UTG10N120** is suitable for high voltage switching, high frequency switch mode power supplies.

■ FEATURES

- * High switching speed
- * High avalanche ruggedness
- * Low saturation voltage: $V_{CE(SAT).Typ.} = 1.5V @ I_C=10A$, $V_{GE}=15V (T_C=25^{\circ}C)$



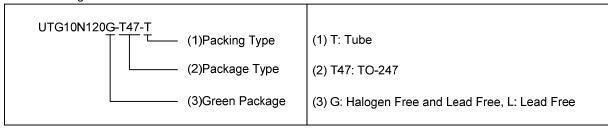
■ SYMBOL



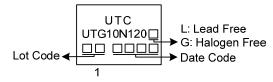
■ ORDERING INFORMATION

Ordering Number		Dankana	Pin Assignment			Dli	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UTG10N120L-S-T47-T	UTG10N120G-S-T47-T	TO-247	G	С	Е	Tube	

Note: Pin Assignment: G: Gate C: Collector E: Emitter



■ MARKING



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■ **ABSOLUTE MAXIMUM RATINGS** (T_A=25°C, unless otherwise noted)

PARAMETER		SYMBOL	RATINGS	UNIT	
Collector-Emitter Voltage		V _{CES}	1200	V	
Gate-Emitter Voltage		.,	±20	V	
Transient Gate-emitter voltage (tp < 5 ms)		V_{GES}	±25	V	
Continuous Collector Current	T _C =25°C	Ic	20	Α	
	T _C =100°C		10	Α	
Collector Current Pulsed (Note 1)		I _{CM}	40	Α	
Diode Forward Current	T _C =25°C	I _F	20	Α	
	T _C =100°C		10	Α	
Short Circuit Withstand Time $V_{GE} = 15V, V_{CC} \le 200V$					
				μs	
Allowed number of short circuits < 1000		t _{sc}	10		
Time between short circuits: ≥ 1.0s					
T _{VJ} = 25°C					
Power Dissipation (T _C =25°C)		P _D	255	W	
Operating Junction Temperature		T_J	-40 ~ +150	°C	
Storage Temperature Range		T _{STG}	-55 ~ +150	°C	

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT	
Junction to Case	θ.ιс	0.49	°C/W	

^{2.} Pulse width limited by maximum junction temperature.

■ **ELECTRICAL CHARACTERISTICS** (T_C=25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT	
Off Characteristics								
Collector-Emitter Breakdown Voltage	BV _{CES}			1200			V	
Collector Cut-Off Current	I _{CES}	V _{CE} =1200V, V _{GE} =0V				5	μΑ	
G-E Leakage Current	I_{GES}	V _{CE} =0V, V _{GE} =±20V				±250	nA	
On Characteristics								
Gate to Emitter Threshold Voltage	$V_{GE(TH)}$	$I_C=250\mu A,\ V_{CE}=V_{GE}$		4.5		7.5	V	
Collector to Emitter Saturation Voltage	V _{CE(SAT)}	I _C =10A, V _{GE} =15V	T _C =25°C		1.5	2.1	V	
			T _C =125°C		2.2		V	
Dynamic Characteristics								
Input Capacitance	CIES	V _{CE} =25V, V _{GE} =0V, f=1MHz			1000		pF	
Output Capacitance	C _{OES}				58.7		рF	
Reverse Transfer Capacitance	C _{RES}				30.7		рF	
Switching Characteristics								
Total Gate Charge	Q_{G}	V _{CE} =600V, I _C =10A, V _{GE} =15V			83.6		nC	
Gate-Emitter Charge	Q_GE				13.7		nC	
Gate-Collector Charge	Q_{GC}				49.7		nC	
Turn-On Delay Time	t _{DON)}				8		ns	
Rise Time	t_{R}		1		13.6		ns	
Turn-Off Delay Time	t _{DOFF)}	V _{CC} =600V, I _C =10A, R _G =5Ω, V _{GE} =0~15V, L=500μH			101		ns	
Fall Time	t_{F}				308		ns	
Turn-On Switching Loss	Eon				0.622		mJ	
Turn-Off Switching Loss	E _{OFF}			1.096		mJ		
SOURCE- DRAIN DIODE RATINGS A	ND CHARAC	TERISTICS			-			
Forward Voltage Drop	V_{F}	I _F =10A				2.5	V	
Reverse Recovery Time	t _{rr}	-I _F =10A, dI/dt=100A/μS, V _{CC} =400V			51		ns	
Reverse Recovery Charge	Qrr				0.65		μC	

TEST CIRCUIT AND WAVEFORMS

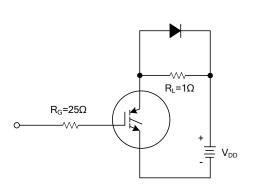


Fig 1. INDUCTIVE SWITCHING TEST CIRCUIT

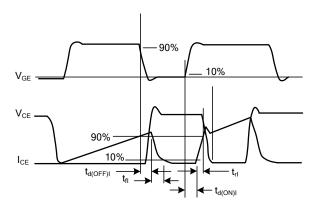


Fig 2. SWITCHING TEST WAVEFORMS

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