UNISONIC TECHNOLOGIES CO., LTD

UTG40N120WT

Preliminary

Insulated Gate Bipolar Transistor

1200V, SMPS N-CHANNEL IGBT

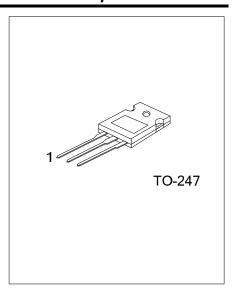
■ DESCRIPTION

The UTC **UTG40N120WT** is a N-channel IGBT. it uses UTC's advanced technology to provide customers with high input impedance, high switching speed and low conduction loss, etc.

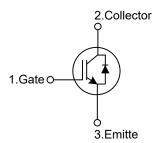
The UTC **UTG40N120WT** is suitable for high voltage switching, high frequency switch mode power supplies.

■ FEATURES

- * High switching speed
- * High avalanche ruggedness
- * Low saturation voltage: V_{CE(SAT).Typ.} =1.8V @ I_C=40A, V_{GE}=15V (T_C =25°C)



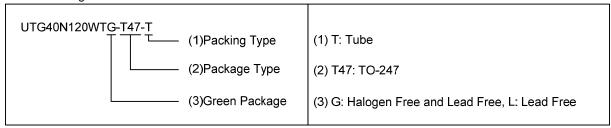
■ SYMBOL



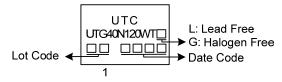
■ ORDERING INFORMATION

Ordering Number		Daalaaaa	Pin Assignment			Da alsinan	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UTG40N120WTL-T47-T	UTG40N120WTG-T47-T	TO-247	G	С	Е	Tube	

Note: Pin Assignment: G: Gate C: Collector E: Emitter



■ MARKING



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■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C, unless otherwise noted)

PARAMETER		SYMBOL	RATINGS	UNIT	
Collector-Emitter Voltage		V _{CES}	1200	V	
Gate-Emitter Voltage		.,	±20	V	
Transient Gate-emitter voltage (tp < 5 ms)		V_{GES}	±25	V	
Continuous Collector Current	T _C =25°C	Ic	80	Α	
	T _C =100°C		40	Α	
Collector Current Pulsed (Note 1)		Ісм	160	Α	
Diode Forward Current	T _C =25°C	I _F	80	Α	
	T _C =100°C		40	Α	
Short Circuit Withstand Time		tsc			
$V_{\text{GE}} = 15\text{V}, V_{\text{CC}} \le 200\text{V}$				μs	
Allowed number of short circuits < 1000			5		
Time between short circuits: ≥ 1.0s					
$T_{VJ} = 25^{\circ}\text{C}$					
Power Dissipation (T _C =25°C)		P _D	285	W	
Operating Junction Temperature		T_J	-40 ~ +150	Ŝ	
Storage Temperature Range		T _{STG}	-55 ~ + 150	°C	

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT
Junction to Case	θјς	0.44	°C/W

^{2.} Pulse width limited by maximum junction temperature.

■ **ELECTRICAL CHARACTERISTICS** (T_C=25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Off Characteristics							
Collector-Emitter Breakdown Voltage	BV _{CES}			1200			V
Collector Cut-Off Current	I _{CES}	V _{CE} =1200V, V _{GE} =0V				5	μΑ
G-E Leakage Current	I _{GES}	V _{CE} =0V, V _{GE} =±20V				±400	nA
On Characteristics							
Gate to Emitter Threshold Voltage	$V_{GE(TH)}$	I _C =250μA, V _{CE} =V _{GE}		4.5		7.5	V
Collector to Emitter Saturation Voltage	V _{CE(SAT)}		T _C =25°C		1.8	2.3	V
			T _C =125°C		2.2		V
Dynamic Characteristics					_		
Input Capacitance	CIES	V _{CE} =25V, V _{GE} =0V, f=1MHz			3490		pF
Output Capacitance	Coes				129		рF
Reverse Transfer Capacitance	C _{RES}				32.5		pF
Switching Characteristics							
Total Gate Charge	Q_{G}	V _{CE} =600V, I _C =40A, V _{GE} =15V			152.4		nC
Gate-Emitter Charge	Q_GE				36.6		nC
Gate-Collector Charge	Q_GC				70.8		nC
Turn-On Delay Time	t _{DON)}				20		ns
Rise Time	t_R				42.4		ns
Turn-Off Delay Time	t _{DOFF)}	V _{CC} =600V, I _C =40A, R _G =5Ω, V _{GE} =0~15V, L=500μH			122		ns
Fall Time	t_{F}				254		ns
Turn-On Switching Loss	Eon				3.186		mJ
Turn-Off Switching Loss	E _{OFF}			3.02		mJ	
SOURCE- DRAIN DIODE RATINGS A	ND CHARAC	TERISTICS					
Forward Voltage Drop	V_{F}	I _F =40A				2.5	V
Reverse Recovery Time	t _{rr}	-I _F =40A, dI/dt=100A/μS, V _{CC} =400V			68.2		ns
Reverse Recovery Charge	Qrr				2.74		μC

TEST CIRCUIT AND WAVEFORMS

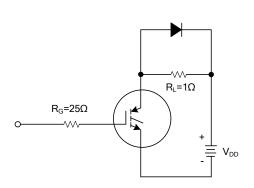


Fig 1. INDUCTIVE SWITCHING TEST CIRCUIT

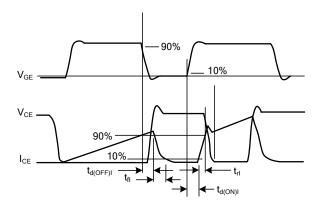


Fig 2. SWITCHING TEST WAVEFORMS

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