## UNISONIC TECHNOLOGIES CO., LTD

UTG30N135-G2

**Preliminary** 

Insulated Gate Bipolar Transistor

# 1350V TRENCH GATE FIELD-STOP IGBT

#### ■ DESCRIPTION

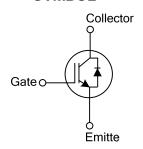
The UTC **UTG30N135-G2** is an Trench Field-Stop Insulated Gate Bipolar Transistor. it uses UTC's advanced technology to provide customers with high switching speed, low saturation voltage and low switching loss, etc.

The UTC **UTG30N135-G2** is suitable for the resonant or soft switching applications.

## ■ FEATURES

- \* High switching speed
- \* High avalanche ruggedness
- \* Low saturation voltage: V<sub>CE(sat), typ.</sub> = 1.7V @ I<sub>C</sub>=30A (T<sub>C</sub> =25°C)
- \* Low switching loss:  $E_{OFF, typ.} = 2.82 \text{mJ} \otimes I_{C} = 30 \text{A} (T_{C} = 25 ^{\circ}\text{C})$





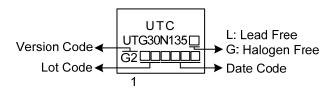
#### ■ ORDERING INFORMATION

Ordering Number		Doolsogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UTG30N135L-G2-T47-T	UTG30N135G-G2-T47-T	TO-247	G	С	Е	Tube	
Note: Pin Assignment: G: Ga	ate C: Collector E: Emit	ter				_	

Note: Pin Assignment: G: Gate C: Collector E: Emitter

UTG30N135G-G2-T47-T (1)Packing Type (2)Package Type (2) T47: TO-247 (3)Version Code (4)Green Package (4) G: Halogen Free and Lead Free, L: Lead Free

#### ■ MARKING



1 TO-247

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#### ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT	
Collector-Emitter Voltage		$V_{CES}$	1350	V	
Gate-Emitter Voltage		$V_{GES}$	±20	V	
Camtinua Callantar Cumant	T <sub>C</sub> =25°C T <sub>C</sub> =100°C	_	60	Α	
Continuous Collector Current		Ic	30	Α	
Collector Current Pulsed (Note 1)		I <sub>CM</sub>	100	Α	
Die de Fernand Ormand	T <sub>C</sub> =25°C		60	Α	
Diode Forward Current	T <sub>C</sub> =100°C		30	Α	
Short Circuit Withstand Time					
$V_{GE} = 15V, V_{CC} \le 200V$					
Allowed number of short circuits < 1000		t <sub>sc</sub>	10	μs	
Time between short circuits: ≥1.0s					
$T_{\text{VJ}} = 25^{\circ}\text{C}$					
Power Dissipation	T <sub>C</sub> =25°C	$P_D$	260	W	
Operating Junction Temperature		$T_J$	-40 ~ +150	°C	
Storage Temperature Range		$T_{STG}$	-55 ~ +150	°C	

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

#### **■ THERMAL DATA**

PARAMETER	SYMBOL	RATING	UNIT	
Junction to Case	θ.ιс	0.48	°C/W	

<sup>2.</sup> Pulse width limited by maximum junction temperature.

### ■ ELECTRICAL CHARACTERISTICS (Tc=25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Collector-Emitter Breakdown Voltage	BV <sub>CES</sub>		1350			V
Collector Cut-Off Current	I <sub>CES</sub>	V <sub>CE</sub> =V <sub>CES</sub> , V <sub>GE</sub> =0V			5	μΑ
G-E Leakage Current	I <sub>GES</sub>	V <sub>GE</sub> =V <sub>GES</sub> , V <sub>CE</sub> =0V			±250	mA
On Characteristics						
Gate to Emitter Threshold Voltage	$V_{GE(TH)}$	I <sub>C</sub> =250μA, V <sub>CE</sub> =V <sub>GE</sub>	4.5		7.5	V
Collector to Emitter Saturation Voltage	.,	I <sub>C</sub> =30A, V <sub>GE</sub> =15V		1.7	2.1	V
	$V_{CE(SAT)}$	I <sub>C</sub> =30A, V <sub>GE</sub> =15V, T <sub>C</sub> =125°C		2.1		V
DYNAMIC CHARACTERISTICS						
Input Capacitance	CIES			1950		рF
Output Capacitance	C <sub>OES</sub>	V <sub>CE</sub> =25V, V <sub>GE</sub> =0V, f=1MHz		81.4		рF
Reverse Transfer Capacitance	C <sub>RES</sub>	]		47.2		рF
SWITCHING CHARACTERISTICS						
Total Gate Charge	$Q_{\mathrm{G}}$			142		nC
Gate-Emitter Charge	$Q_GE$	V <sub>CE</sub> =600V, IC=30A, V <sub>GE</sub> =15V		17.2		nC
Gate-Collector Charge	Q <sub>GC</sub>	]		88		nC
Turn-On Delay Time	t <sub>DON)</sub>			14.6		ns
Rise Time	t <sub>R</sub>	]		23.2		ns
Turn-Off Delay Time	t <sub>DOFF)</sub>	Vcc=600V, Ic=30A, R <sub>G</sub> =5Ω,		156		ns
Fall Time	t <sub>F</sub>	V <sub>GE</sub> =0~15V, L=500uH		271		ns
Turn-On Switching Loss	Eon	]		2.12		mJ
Turn-Off Switching Loss	E <sub>OFF</sub>	]		2.82		mJ
SOURCE- DRAIN DIODE RATINGS AN	D CHARACTE	RISTICS	_			
Forward Voltage Drop	V <sub>FM</sub>	I <sub>F</sub> =30A			3.0	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =30A,		54		ns
Reverse Recovery Charge	Qrr	dl/dt=100A/μS		1.31		μC

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