

UNISONIC TECHNOLOGIES CO., LTD

UT35N02

Preliminary

35A, 20V N-CHANNEL POWER MOSFET

DESCRIPTION

The UTC UT35N02 is a N-channel enhancement MOSFET using UTC's advanced technology to provide the customers with perfect $R_{\text{DS}(\text{ON})}$ and high switching speed.

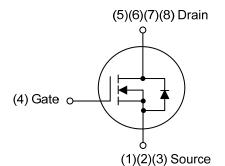
FEATURES

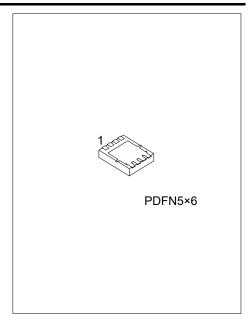
* $R_{DS(ON)} \le 4.5 \text{ m}\Omega @ V_{GS}=10V$, $I_D=20A$

 $R_{DS(ON)} \le 5.0 \text{ m}\Omega @ V_{GS}=4.5V, I_D=10A$

* High Switching Speed

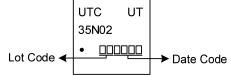
SYMBOL





ORDERING INFORMATION

Ordering Number			Pin Assignment									
Lead Free	Halogen Free	Package	1	2	3	4	5	6	- Pa		Packing	
UT35N02L-P5060-R	PDFN5×6	S	S	S	G	D	D	D	D	Tape Reel		
Note: Pin Assignment: S: Source G: Gate D: Drain												
UT35N02G-P5060-R (1)Packing Type (2)Package Type (3)Green Package		(1) R: Tape Reel (2) P5060: PDFN5×6 (3) G: Halogen Free and Lead Free, K: Lead Free										



■ ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	20	V
Gate-Source Voltage		V _{GSS}	±20	V
Drain Current	Continuous (V _{GS} =10V)	lo	35	А
	Pulsed(Note 2)	Ідм	70	А
Avalanche Energy	che Energy Single Pulsed (Note 3)		121	mJ
Power Dissipation		PD	34	W
Junction Temperature		TJ	+150	°C
Storage Temperature		Tstg	-55 ~ +175	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L = 1mH, I_{AS} = 15.6A, V_{DD} = 90V, R_G = 25 Ω , Starting T_J = 25°C

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT		
Junction to Ambient	θ _{JA}	65	°C/W		
Junction to Case	θις	3.68	°C/W		

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

■ **ELECTRICAL CHARACTERISTICS** (T_J=25°C, unless otherwise specified)

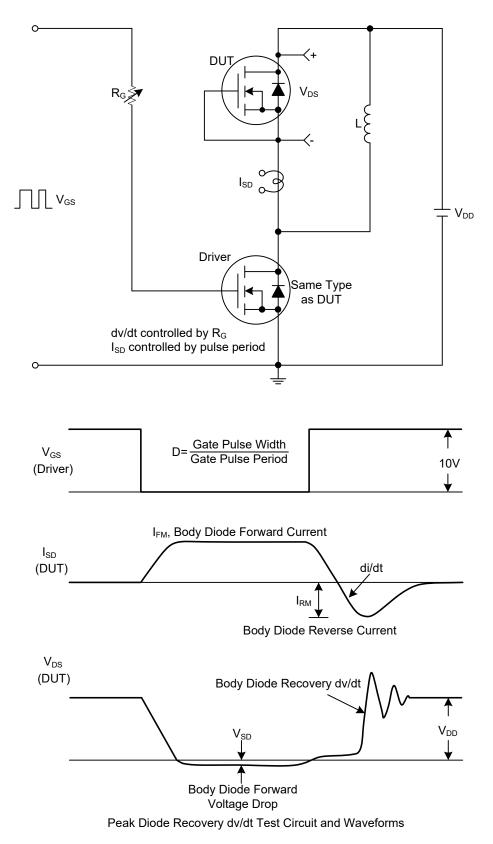
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V, I _D =250µA	20			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =20V, V _{GS} =0V			1	μA
Gate- Source Leakage Current	Forward		V _{GS} =+20V, V _{DS} =0V			+100	nA
	Reverse	I _{GSS}	V _{GS} =-20V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250µA	1.0		3.0	V
Static Drain-Source On-State R	:	RDS(ON)	V _{GS} =10V, I _D =20A			4.5	mΩ
Static Drain-Source On-State Re	esistance		V _{GS} =4.5V, I _D =10A			5.0	mΩ
DYNAMIC PARAMETERS							
nput Capacitance		CISS			2743		pF
Output Capacitance		Coss	V _{GS} =0V, V _{DS} =15V, f=1.0MHz		770		pF
Reverse Transfer Capacitance		Crss			674		рF
SWITCHING PARAMETERS							
Total Gate Charge		Q _G			76		nC
Gate to Source Charge		Q _{GS}	V _{DS} =16V, V _{GS} =10V, I _D =35A (Note 1, 2)		7		nC
Gate to Drain Charge		Q _{GD}	(Note 1, 2)		29		nC
Turn-ON Delay Time		t _{D(ON)}			12		ns
Rise Time		t _R	V _{DD} =10V, V _{GS} =10V, I _D =35A,		19		ns
Turn-OFF Delay Time		t _{D(OFF)}	R _G =3.3Ω, (Note 1, 2)		72		ns
Fall-Time		t⊧			67		ns
SOURCE- DRAIN DIODE RATI	NGS AND CH		STICS				
Maximum Continuous Drain-Source Diode Forward Current		1.				35	^
		ls				35	A
Maximum Pulsed Drain-Source Diode		lsм				70	А
Forward Current		ISM				10	~
Drain-Source Diode Forward Voltage		Vsd	Is=35A,V _{GS} =0V			1.4	V
Notes: 1 Pulse Test: Pulse widt	$h < 300 \mu c$ D	$v_{\rm tv} c_{\rm v} c_{\rm l} < 2$	00/				

Notes: 1. Pulse Test: Pulse width \leq 300µs, Duty cycle \leq 2%.

2. Essentially independent of operating ambient temperature.

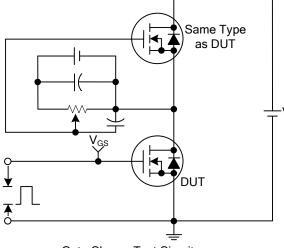


TEST CIRCUITS AND WAVEFORMS

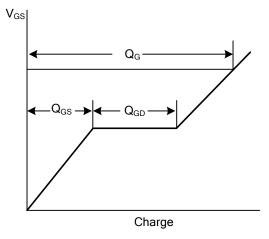


V_{DS}

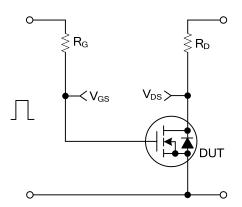
TEST CIRCUITS AND WAVEFORMS



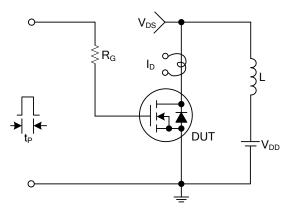




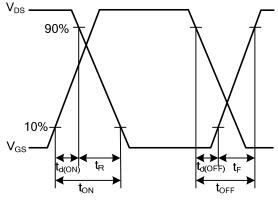




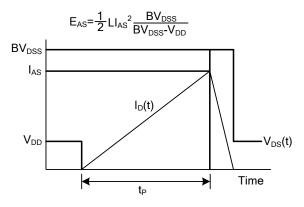
Resistive Switching Test Circuit



Unclamped Inductive Switching Test Circuit



Resistive Switching Waveforms



Unclamped Inductive Switching Waveforms



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