



UTG10N120LLS1

Preliminary

Insulated Gate Bipolar Transistor

1200V, SMPS N-CHANNEL IGBT

DESCRIPTION

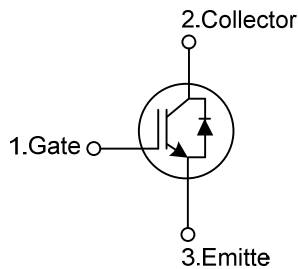
The UTC **UTG10N120LLS1** is a N-channel IGBT. it uses UTC's advanced technology to provide customers with high input impedance, high switching speed and low conduction loss, etc.

The UTC **UTG10N120LLS1** is suitable for high voltage switching, high frequency switch mode power supplies.

FEATURES

- * High switching speed
- * High avalanche ruggedness
- * Low saturation voltage: $V_{CE(SAT), Typ.} = 1.45V @ I_C=10A, V_{GE}=15V$ ($T_C=25^{\circ}C$)

SYMBOL

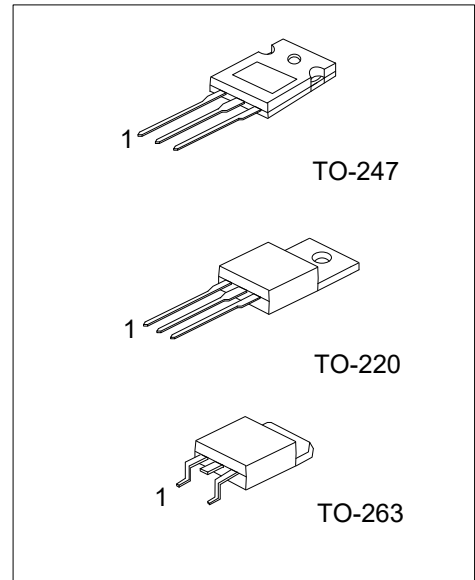


ORDERING INFORMATION

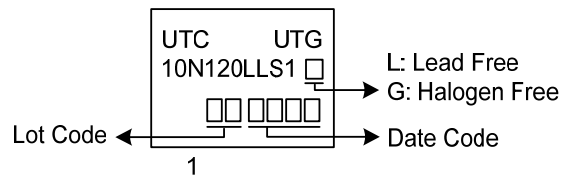
Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UTG10N120LLS1L-TA3-T	UTG10N120LLS1G-TA3-T	TO-220	G	C	E	Tube
UTG10N120LLS1L-TQ2-T	UTG10N120LLS1G-TQ2-T	TO-263	G	C	E	Tube
UTG10N120LLS1L-TQ2-R	UTG10N120LLS1G-TQ2-R	TO-263	G	C	E	Tape Reel
UTG10N120LLS1L-T47-T	UTG10N120LLS1G-T47-T	TO-247	G	C	E	Tube

Note: Pin Assignment: G: Gate C: Collector E: Emitter

UTG10N120LLS1G-TA3-T	(1)Packing Type	(1) T: Tube, R: Tape Reel
	(2)Package Type	(2) TA3: TO-220, TQ2: TO-263, T47: TO-247
	(3)Green Package	(3) G: Halogen Free and Lead Free, L: Lead Free



■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_A=25^{\circ}\text{C}$, unless otherwise noted)

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Emitter Voltage		V_{CES}	1200	V
Gate-Emitter Voltage		V_{GES}	± 20	V
Transient Gate-emitter voltage ($t_p < 5\text{ ms}$)			± 25	V
Continuous Collector Current	$T_C=25^{\circ}\text{C}$	I_C	20	A
	$T_C=100^{\circ}\text{C}$		10	A
Collector Current Pulsed (Note 1)		I_{CM}	40	A
Diode Forward Current	$T_C=25^{\circ}\text{C}$	I_F	20	A
	$T_C=100^{\circ}\text{C}$		10	A
Short Circuit Withstand Time $V_{GE} = 15\text{V}$, $V_{CC} \leq 200\text{V}$ Allowed number of short circuits < 1000 Time between short circuits: $\geq 1.0\text{s}$ $T_{VJ} = 25^{\circ}\text{C}$		t_{SC}	8	μs
Power Dissipation ($T_C=25^{\circ}\text{C}$)	TO-220	P_D	96	W
	TO-263		245	W
	TO-247			
Operating Junction Temperature		T_J	$-40 \sim +150$	$^{\circ}\text{C}$
Storage Temperature Range		T_{STG}	$-55 \sim +150$	$^{\circ}\text{C}$

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

Absolute maximum ratings are those values beyond which the device could be permanently damaged.

2. Pulse width limited by maximum junction temperature.

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Case	TO-220	θ_{JC}	1.3	$^{\circ}\text{C/W}$
	TO-263		0.51	$^{\circ}\text{C/W}$
	TO-247			

■ ELECTRICAL CHARACTERISTICS (T_C=25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Collector-Emitter Breakdown Voltage	BV _{CES}			1200			V
Collector Cut-Off Current	I _{CES}	V _{CE} =1200V, V _{GE} =0V				5	μA
G-E Leakage Current	I _{GES}	V _{CE} =0V, V _{GE} =±20V				±250	nA
ON CHARACTERISTICS							
Gate to Emitter Threshold Voltage	V _{GE(TH)}	I _C =250μA, V _{CE} =V _{GE}		4.5		7.6	V
Collector to Emitter Saturation Voltage	V _{CE(SAT)}	I _C =10A, V _{GE} =15V	T _C =25°C		1.45	2.1	V
			T _C =125°C		2.2		V
DYNAMIC CHARACTERISTICS							
Input Capacitance	C _{IES}	V _{CE} =25V, V _{GE} =0V, f=1MHz			1000		pF
Output Capacitance	C _{OES}				58.7		pF
Reverse Transfer Capacitance	C _{RES}				30.7		pF
SWITCHING CHARACTERISTICS							
Total Gate Charge	Q _G	V _{CE} =600V, I _C =10A, V _{GE} =15V			83.6		nC
Gate-Emitter Charge	Q _{GE}				13.7		nC
Gate-Collector Charge	Q _{GC}				49.7		nC
Turn-On Delay Time	t _{DON}	V _{CC} =600V, I _C =10A, R _G =5Ω, V _{GE} =0~15V, L=500μH			7.6		ns
Rise Time	t _R				13.5		ns
Turn-Off Delay Time	t _{DOFF}				100.6		ns
Fall Time	t _F				307.9		ns
Turn-On Switching Loss	E _{ON}				0.62		mJ
Turn-Off Switching Loss	E _{OFF}				1.1		mJ
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Forward Voltage Drop	V _F	I _F =10A				2.5	V
Reverse Recovery Time	t _{rr}	I _F =10A, dI/dt=100A/μS, V _{CC} =400V			51		ns
Reverse Recovery Charge	Q _{rr}				0.65		μC

■ TEST CIRCUIT AND WAVEFORMS

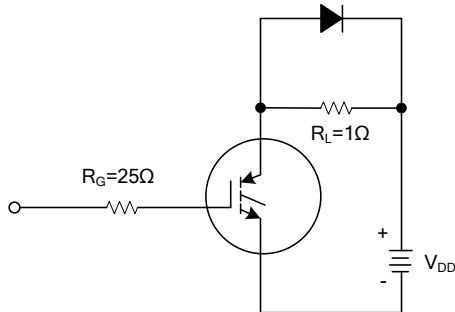


Fig 1. INDUCTIVE SWITCHING TEST CIRCUIT

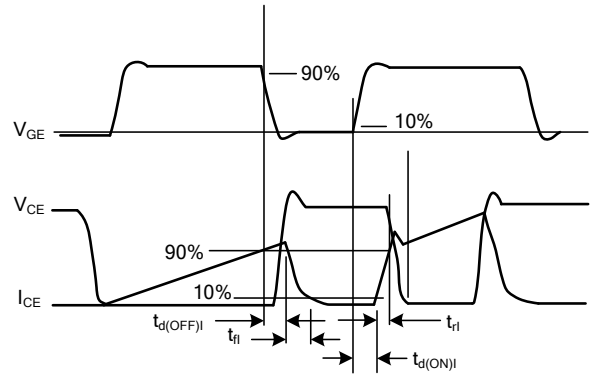


Fig 2. SWITCHING TEST WAVEFORMS

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