# UNISONIC TECHNOLOGIES CO., LTD

UTG75N65-SQ

**Preliminary** 

Insulated Gate Bipolar Transistor

## 650V TRENCH GATE FIELD-STOP IGBT

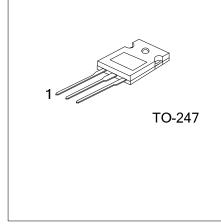
#### DESCRIPTION

The UTC UTG75N65-SQ is an Trench Field-Stop Insulated Gate Bipolar Transistor, it uses UTC's advanced technology to provide customers with high switching speed, low saturation voltage and low switching loss, etc.

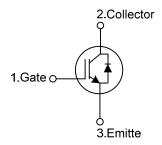
The UTC UTG75N65-SQ is suitable for the resonant or soft switching applications.

#### **FEATURES**

- \* High switching speed
- \* High avalanche ruggedness
- \* Low saturation voltage: VCE(SAT).Typ.=1.35V @ IC=75A, VGE=15V  $(T_C = 25^{\circ}C)$



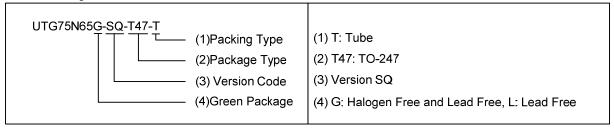
#### **SYMBOL**



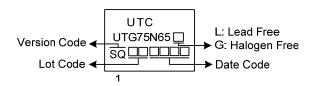
#### **ORDERING INFORMATION**

Ordering Number		DI	Pin Assignment			Da alsina	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UTG75N65L-SQ-T47-T	UTG75N65G-SQ-T47-T	TO-247	G	С	E	Tube	

Note: Pin Assignment: G: Gate C: Collector E: Emitter



#### **MARKING**



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#### ■ ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C, unless otherwise noted)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector-Emitter Voltage	V <sub>CES</sub>	650	V
Gate-Emitter Voltage	\/	±20	V
Transient Gate-emitter voltage (tp < 5 ms)	V <sub>GES</sub>	±25	V
T <sub>C</sub> =25°C		150	Α
Continuous Collector Current  Tc=100°C	Ic	75	Α
Collector Current Pulsed (Note 1)	I <sub>CM</sub>	300	Α
Diode Forward Current		150	Α
T <sub>C</sub> =100°C	) I <sub>F</sub>	75	Α
Short Circuit Withstand Time			
$V_{GE} = 15V, V_{CC} \le 200V$		3	μs
Allowed number of short circuits < 1000	t <sub>SC</sub>		
Time between short circuits: ≥1.0s			
T <sub>VJ</sub> = 25°C			
Power Dissipation (T <sub>C</sub> =25°C)	P <sub>D</sub>	291	W
Operating Junction Temperature	TJ	-40 ~ +150	°C
Storage Temperature Range	T <sub>STG</sub>	-55 ~ <b>+</b> 150	°C

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

#### **■ THERMAL DATA**

PARAMETER	SYMBOL	RATING	UNIT
Junction to Case	θјς	0.43	°C/W

<sup>2.</sup> Pulse width limited by maximum junction temperature.

### ■ **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub>=25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS			TYP	MAX	UNIT	
OFF CHARACTERISTICS				•	•			
Collector-Emitter Breakdown Voltage	BV <sub>CES</sub>			650			V	
Collector Cut-Off Current	I <sub>CES</sub>	V <sub>CE</sub> =650V, V <sub>GE</sub> =0V				5	μA	
G-E Leakage Current	I <sub>GES</sub>	V <sub>CE</sub> =0V, V <sub>GE</sub> =±20V				±400	nA	
ON CHARACTERISTICS								
Gate to Emitter Threshold Voltage	$V_{GE(TH)}$	I <sub>C</sub> =250μA, V <sub>CE</sub> =V <sub>GE</sub>		3.0		7.2	V	
Collector to Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	I <sub>C</sub> =75A, V <sub>GE</sub> =15V	T <sub>C</sub> =25°C		1.35	2.1	V	
			T <sub>C</sub> =125°C		2.0		V	
DYNAMIC CHARACTERISTICS								
Input Capacitance	C <sub>IES</sub>			7590		pF		
Output Capacitance	C <sub>OES</sub>	V <sub>CE</sub> =25V, V <sub>GE</sub> =0V, f=1		306.4		pF		
Reverse Transfer Capacitance	C <sub>RES</sub>			132.5		pF		
SWITCHING CHARACTERISTICS								
Total Gate Charge	$Q_G$				377		nC	
Gate-Emitter Charge	$Q_GE$	V <sub>CE</sub> =520V, I <sub>C</sub> =75A, V <sub>GE</sub> =15V			71		nC	
Gate-Collector Charge	$Q_GC$				209		nC	
Turn-On Delay Time	t <sub>DON)</sub>	V <sub>CC</sub> =400V, I <sub>C</sub> =75A, R <sub>G</sub> =5Ω, V <sub>GE</sub> =0~15V, L=100μH			51.1		ns	
Rise Time	$t_{R}$				3.7		ns	
Turn-Off Delay Time	t <sub>DOFF)</sub>				255.9		ns	
Fall Time	$t_{F}$				50.7		ns	
Turn-On Switching Loss	Eon				3.79		mJ	
Turn-Off Switching Loss	E <sub>OFF</sub>				2.8		mJ	
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS								
Forward Voltage Drop	VF	I <sub>F</sub> =75A			1.6	2.0	V	
Reverse Recovery Time	t <sub>rr</sub>	-I <sub>F</sub> =75A, dI/dt=100A/μS, V <sub>CC</sub> =400V			39.5		ns	
Reverse Recovery Charge	Qrr				543		nC	

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