UNISONIC TECHNOLOGIES CO., LTD

UTG15N120LNS1

Preliminary

Insulated Gate Bipolar Transistor

1200V, SMPS N-CHANNEL IGBT

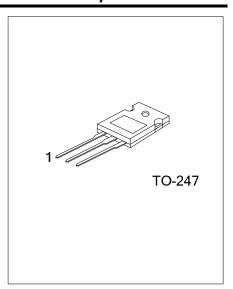
DESCRIPTION

The UTC **UTG15N120LNS1** is a N-channel IGBT. it uses UTC's advanced technology to provide customers with high input impedance, high switching speed and low conduction loss, etc.

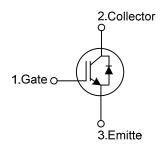
The UTC **UTG15N120LNS1** is suitable for high voltage switching, high frequency switch mode power supplies.

■ FEATURES

- * High switching speed
- * High avalanche ruggedness
- * Low saturation voltage: $V_{CE(SAT).Typ.} = 1.61V @ I_{C}=15A, V_{GE}=15V (T_{C}=25^{\circ}C)$



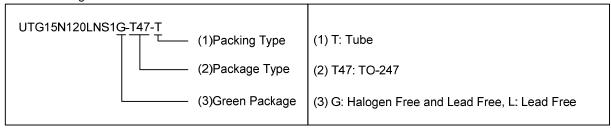
■ SYMBOL



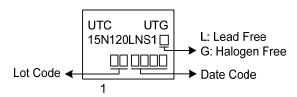
ORDERING INFORMATION

Ordering Number		Deelsene	Pin Assignment			D. alda a	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UTG15N120LNS1L-T47-T	UTG15N120LNS1G-T47-T	TO-247	G	С	Е	Tube	

Note: Pin Assignment: G: Gate C: Collector E: Emitter



MARKING



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■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Emitter Voltage		V _{CES}	1200	V
Gate-Emitter Voltage		.,	±20	V
Transient Gate-Emitter Voltage (tp < 5 ms)		V_{GES}	±25	V
Continuous Collector Current	T _C =25°C		30	Α
	T _C =100°C	Ic	15	Α
Collector Current Pulsed (Note 1)		I _{CM}	60	Α
Diode Forward Current	T _C =25°C		30	Α
	T _C =100°C	l _F	15	Α
Power Dissipation (T _C =25°C)		P _D	278	W
Operating Junction Temperature		TJ	-40 ~ +150	°C
Storage Temperature Range		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT	
Junction to Case	θις	0.45	°C/W	

■ ELECTRICAL CHARACTERISTICS (Tc=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Collector-Emitter Breakdown Voltage	BV _{CES}			1200			V
Collector Cut-Off Current	Ices	V _{CE} =1200V, V _{GE} =0V				5	μΑ
G-E Leakage Current	I _{GES}	V _{CE} =0V, V _{GE} =±20V				±250	nA
ON CHARACTERISTICS							
Gate to Emitter Threshold Voltage	$V_{GE(TH)}$	Ic=250µA, Vce=Vge	4.0		7.5	V	
Collector to Emitter Saturation Voltage	V _{CE(SAT)}	I _C =15A, V _{GE} =15V	T _C =25°C		1.61	2.3	V
			T _C =125°C		2.2		V
DYNAMIC CHARACTERISTICS							
Input Capacitance	CIES			863		рF	
Output Capacitance	Coes	V _{CE} =25V, V _{GE} =0V, f=1MHz			86		рF
Reverse Transfer Capacitance	C _{RES}			49		pF	
SWITCHING CHARACTERISTICS							
Total Gate Charge	Q_{G}	V _{CE} =600V, I _C =15A, V _{GE} =15V			63		nC
Gate-Emitter Charge	Q_GE				20		nC
Gate-Collector Charge	Q_{GC}				28		nC
Turn-On Delay Time	t _{DON)}	V _{CC} =600V, I _C =15A, R _G =5Ω, V _{GE} =0~15V, L=500μH			11		ns
Rise Time	t_{R}				21		ns
Turn-Off Delay Time	t _{DOFF)}				47		ns
Fall Time	t⊧				275		ns
Turn-On Switching Loss	Eon				1.08		mJ
Turn-Off Switching Loss	E _{OFF}			1.16		mJ	
SOURCE- DRAIN DIODE RATINGS A	ND CHARAC	TERISTICS		-		-	
Forward Voltage Drop	V_{F}	I _F =15A				2.5	V
Reverse Recovery Time	t _{rr}	I _F =15A, dI/dt=100A/μS, V _{CC} =400V			57		ns
Reverse Recovery Charge	Q_{rr}				1828		μC

^{2.} Pulse width limited by maximum junction temperature.

■ TEST CIRCUIT AND WAVEFORMS

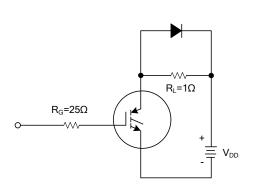


Fig 1. INDUCTIVE SWITCHING TEST CIRCUIT

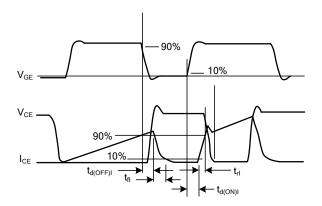


Fig 2. SWITCHING TEST WAVEFORMS

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