



UTG80N65LLS2

Preliminary

Insulated Gate Bipolar Transistor

650V, TRENCH GATE FIELD-STOP IGBT

DESCRIPTION

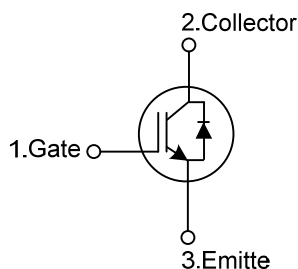
The UTC **UTG80N65LLS2** is an Trench Field-Stop Insulated Gate Bipolar Transistor. it uses UTC's advanced technology to provide customers with high switching speed, low saturation voltage and low switching loss, etc.

The UTC **UTG80N65LLS2** is suitable for the resonant or soft switching applications.

FEATURES

- * High switching speed
- * High avalanche ruggedness
- * Low saturation voltage: $V_{CE(SAT), Typ.} = 1.83V @ I_C=80A, V_{GE}=15V$ ($T_C = 25^\circ C$)

SYMBOL

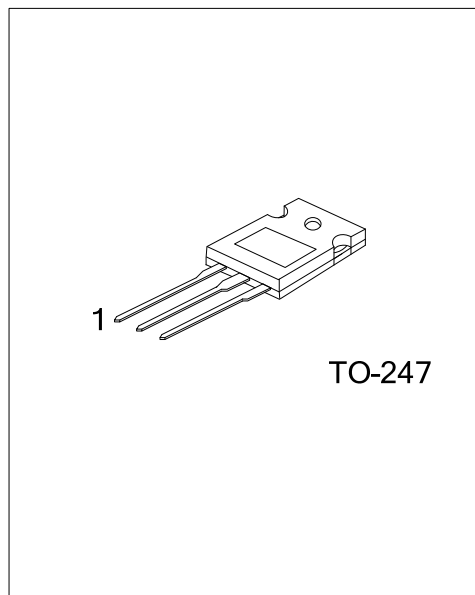


ORDERING INFORMATION

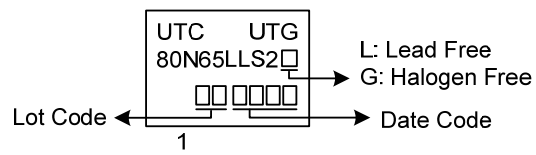
Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UTG80N65LLS2L-T47-T	UTG80N65LLS2G-T47-T	TO-247	G	C	E	Tube

Note: Pin Assignment: G: Gate C: Collector E: Emitter

UTG80N65LLS2G-T47-T		(1)Packing Type	(1) T: Tube
		(2)Package Type	(2) T47: TO-247
		(3)Green Package	(3) G: Halogen Free and Lead Free, L: Lead Free



■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector-Emitter Voltage	V_{CES}	650	V
Gate-Emitter Voltage	V_{GES}	± 20	V
Transient Gate-Emitter Voltage ($t_p < 5 \text{ ms}$)		± 25	V
Continuous Collector Current	I_C	$T_C=25^\circ\text{C}$	A
		$T_C=100^\circ\text{C}$	A
Collector Current Pulsed (Note 1)	I_{CM}	320	A
Diode Forward Current	I_F	$T_C=25^\circ\text{C}$	A
		$T_C=100^\circ\text{C}$	A
Short Circuit Withstand Time $V_{GE} = 15\text{V}$, $V_{CC} \leq 200\text{V}$ Allowed number of short circuits < 1000 Time between short circuits: $\geq 1.0\text{s}$ $T_{VJ} = 25^\circ\text{C}$	t_{SC}	8	μs
Power Dissipation ($T_C=25^\circ\text{C}$)	P_D	285	W
Operating Junction Temperature	T_J	$-40 \sim +150$	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	$-55 \sim +150$	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
Absolute maximum ratings are those values beyond which the device could be permanently damaged.
2. Pulse width limited by maximum junction temperature.

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT
Junction to Case	θ_{JC}	0.44	$^\circ\text{C/W}$

■ ELECTRICAL CHARACTERISTICS (T_C=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Off Characteristics						
Collector-Emitter Breakdown Voltage	BV_{CES}		650			V
Collector Cut-Off Current	I_{CES}	$V_{CE}=650V, V_{GE}=0V$			5	μA
G-E Leakage Current	I_{GES}	$V_{CE}=0V, V_{GE}=\pm 20V$			± 400	nA
On Characteristics						
Gate to Emitter Threshold Voltage	$V_{GE(TH)}$	$I_C=250\mu A, V_{CE}=V_{GE}$	4.0		6.5	V
Collector to Emitter Saturation Voltage	$V_{CE(SAT)}$	$I_C=80A, V_{GE}=15V$	$T_C=25^{\circ}C$	1.83	2.6	V
			$T_C=125^{\circ}C$	2.5		V
Dynamic Characteristics						
Input Capacitance	C_{IES}	$V_{CE}=25V, V_{GE}=0V, f=1MHz$		6020		pF
Output Capacitance	C_{OES}			375		pF
Reverse Transfer Capacitance	C_{RES}			223		pF
Switching Characteristics						
Total Gate Charge	Q_G	$V_{CE}=520V, I_C=80A, V_{GE}=15V$		488		nC
Gate-Emitter Charge	Q_{GE}			47		nC
Gate-Collector Charge	Q_{GC}			337		nC
Turn-On Delay Time	t_{DON}	$V_{CC}=400V, I_C=80A, R_G=5\Omega, V_{GE}=0\sim 15V, L=500\mu H$		34		ns
Rise Time	t_R			51		ns
Turn-Off Delay Time	t_{DOFF}			234		ns
Fall Time	t_F			110		ns
Turn-On Switching Loss	E_{ON}			5.88		mJ
Turn-Off Switching Loss	E_{OFF}			3.67		mJ
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Forward Voltage Drop	V_F	$I_F=80A$			2.6	V
Reverse Recovery Time	t_{rr}	$I_F=80A, dI/dt=100A/\mu S,$		52		ns
Reverse Recovery Charge	Q_{rr}	$V_{CC}=400V$		739		nC

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