

**48NM65-U2**

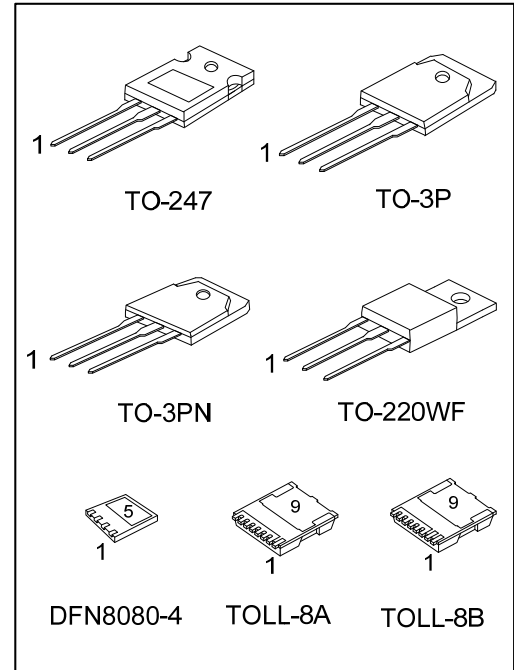
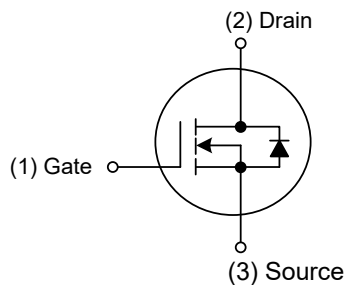
Preliminary

*Power MOSFET***48A, 650V N-CHANNEL
SUPER-JUNCTION MOSFET****■ DESCRIPTION**

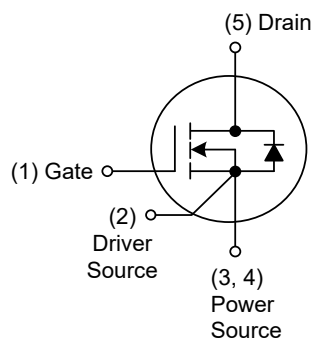
The **UTC 48NM65-U2** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

■ FEATURES

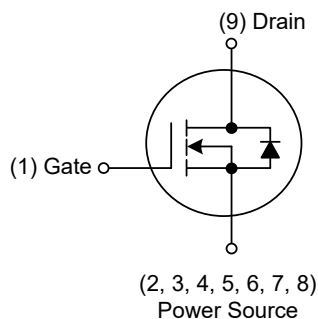
- * $R_{DS(ON)} \leq 66 \text{ m}\Omega$ @ $V_{GS}=10\text{V}$, $I_D=12\text{A}$
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness

**■ SYMBOL**

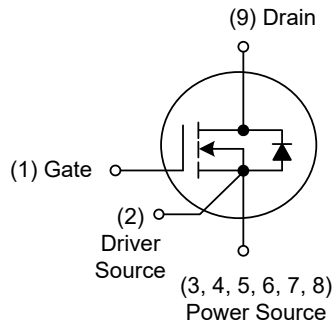
TO-220WF
TO-247 / TO-3P / TO-3PN



DFN8080-4



TOLL-8A



TOLL-8B

■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment									Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	9	
48NM65L-U2-TW1-T	48NM65G-U2-TW1-T	TO-220WF	G	D	S	-	-	-	-	-	-	Tube
48NM65L-U2-T47-T	48NM65G-U2-T47-T	TO-247	G	D	S	-	-	-	-	-	-	Tube
48NM65L-U2-T3P-T	48NM65G-U2-T3P-T	TO-3P	G	D	S	-	-	-	-	-	-	Tube
48NM65L-U2-T3N-T	48NM65G-U2-T3N-T	TO-3PN	G	D	S	-	-	-	-	-	-	Tube
48NM65L-U2-K04-8080-R	48NM65G-U2-K04-8080-R	DFN8080-4	G	S	S	S	D	-	-	-	-	Tape Reel
48NM65L-U2-T8A-R	48NM65G-U2-T8A-R	TOLL-8A	G	S	S	S	S	S	S	S	D	Tape Reel
48NM65L-U2-T8B-R	48NM65G-U2-T8B-R	TOLL-8B	G	S	S	S	S	S	S	S	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

48NM65G-U2-TW1-T (1) Packing Type (2) Package Type (3) Version Code (4) Green Package	(1) T: Tube, R: Tape Reel (2) TW1: TO-220WF, T47: TO-247, T3P: TO-3P T3N: TO-3PN, T8A: TOLL-8A, T8B: TOLL-8B K04-8080: DFN8080-4 (3) Version U2 (4) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING

TO-220WF / TO-247 / TO-3P / TO-3PN	DFN8080-4
UTC 48NM65 U2 □ □ □ □ □ □ Version Code ← Lot Code → L: Lead Free G: Halogen Free Date Code 1	UTC 48NM65 U2 □ □ □ □ □ □ Version Code ← Lot Code → Date Code 1
TOLL-8A / TOLL-8B	-
UTC 48NM65 U2 □ □ □ □ □ □ Version Code ← Lot Code → L: Lead Free G: Halogen Free Date Code 1	-

■ **ABSOLUTE MAXIMUM RATINGS** ($T_C=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DS}	650	V
Gate-Source Voltage		V_{GS}	± 30	V
Drain Current	Continuous	I_D	48	A
	Pulsed (Note 2)	I_{DM}	144	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	2500	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	6.4	V/ns
Power Dissipation	TO-220WF	P_D	44	W
	TO-247		210	W
	TO-3P/TO-3PN		225	W
	DFN8080-4		250	W
	TOLL-8A/TOLL-8B			
Junction Temperature		T_J	+150	$^{\circ}\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^{\circ}\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 100\text{mH}$, $I_{AS} = 7.1\text{A}$, $V_{DD} = 90\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^{\circ}\text{C}$

4. $I_{SD} \leq 30\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^{\circ}\text{C}$

■ **THERMAL DATA**

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220WF	θ_{JA}	62.5	$^{\circ}\text{C}/\text{W}$
	TO-247		40	$^{\circ}\text{C}/\text{W}$
	TO-3P/TO-3PN		30	$^{\circ}\text{C}/\text{W}$
	DFN8080-4		35 (Note)	$^{\circ}\text{C}/\text{W}$
	TOLL-8A/TOLL-8B			
Junction to Case	TO-220WF	θ_{JC}	2.84	$^{\circ}\text{C}/\text{W}$
	TO-247		0.6	$^{\circ}\text{C}/\text{W}$
	TO-3P/TO-3PN		0.55	$^{\circ}\text{C}/\text{W}$
	DFN8080-4		0.43 (Note)	$^{\circ}\text{C}/\text{W}$
	TOLL-8A/TOLL-8B			

Note: Device mounted on FR-4 substrate P_c board, 2oz copper, with 1inch square copper plate.

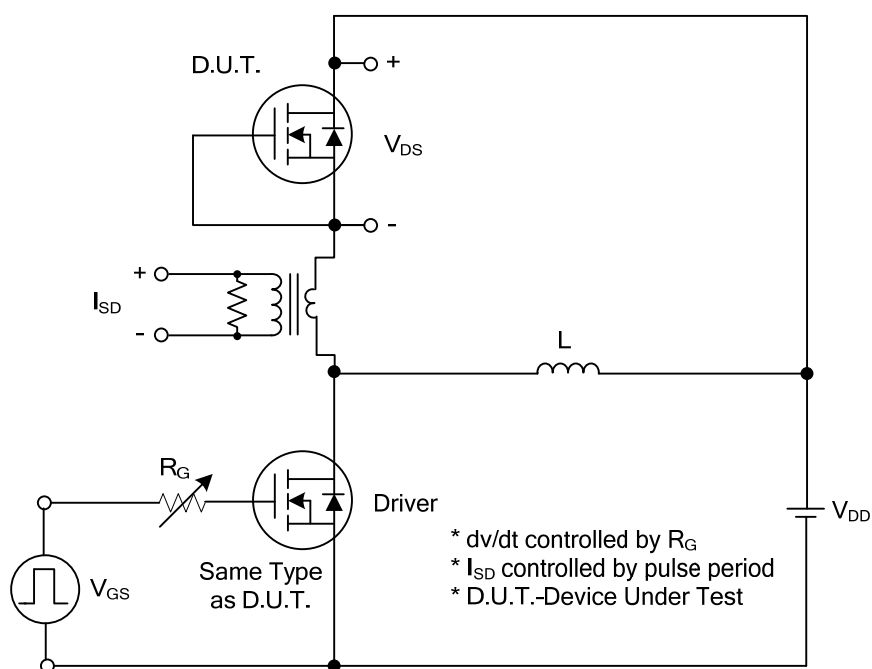
■ **ELECTRICAL CHARACTERISTICS** ($T_J=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	650			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =650V, V _{GS} =0V			10	μA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±30V, V _{DS} =0V			±100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	2.5		4.5	V
Static Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =12A			66	mΩ
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{ISS}	V _{GS} =0V, V _{DS} =50V, f=1MHz		3221		pF
Output Capacitance	C _{OSS}			1304		pF
Reverse Transfer Capacitance	C _{RSS}			79		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge	Q _G	V _{DS} =520V, V _{GS} =10V, I _D =48A (Note 1, 2)		143		nC
Gate-Source Charge	Q _{GS}			25		nC
Gate-Drain Charge	Q _{DD}			89		nC
Turn-On Delay Time	t _{D(ON)}	V _{DD} =100V, V _{GS} =10V, I _D =48A, R _G =3Ω (Note 1, 2)		19		ns
Turn-On Rise Time	t _r			28		ns
Turn-Off Delay Time	t _{D(OFF)}			83		ns
Turn-Off Fall Time	t _f			28		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Continuous Drain-Source Diode Forward Current	I _S				48	A
Maximum Pulsed Drain-Source Diode Forward Current	I _{SM}				144	A
Drain-Source Diode Forward Voltage	V _{SD}	I _S =48A, V _{GS} =0V			1.4	V
Body Diode Reverse Recovery Time	t _{rr}	I _S =30A, V _{GS} =0V,		656		nS
Body Diode Reverse Recovery Charge	Q _{rr}	dI _F /dt=100A/μs		14.3		μC

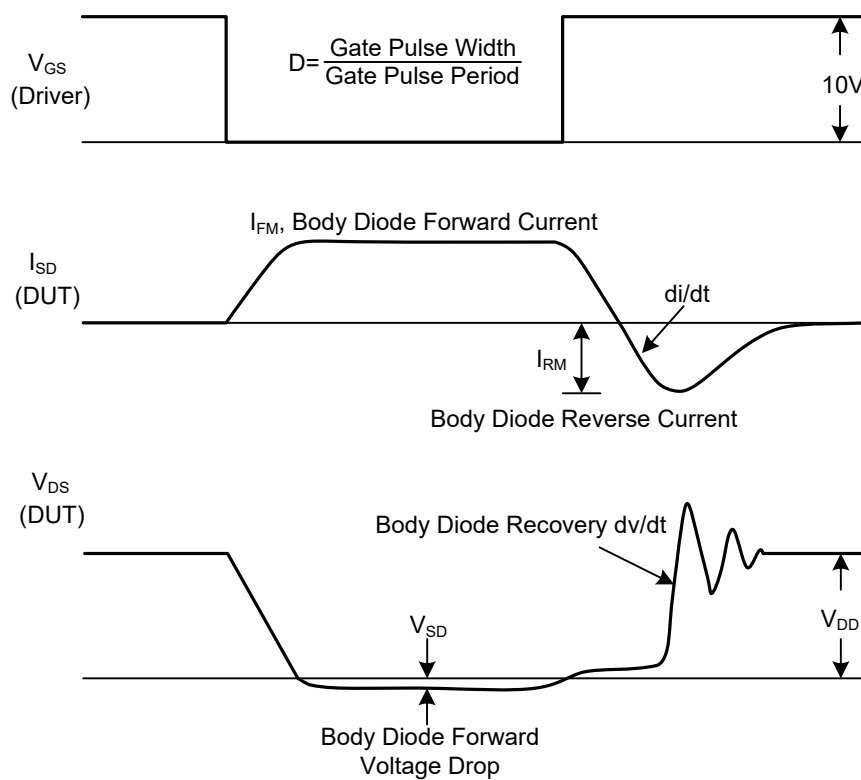
Notes: 1. Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

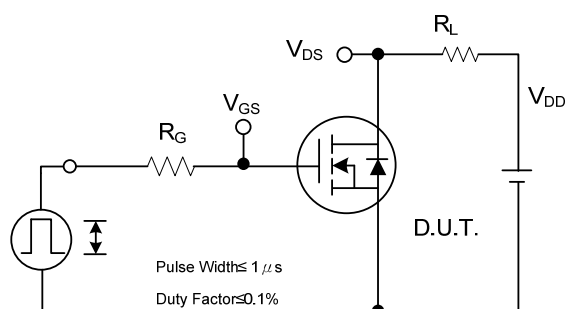


Peak Diode Recovery dv/dt Test Circuit

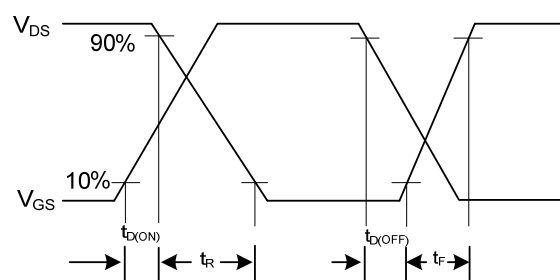


Peak Diode Recovery dv/dt Waveforms

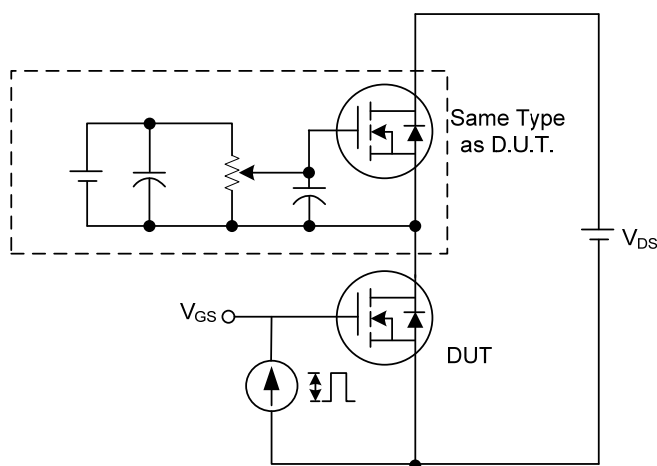
■ TEST CIRCUITS AND WAVEFORMS



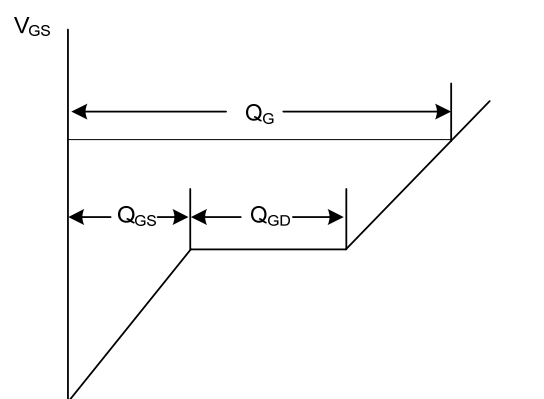
Switching Test Circuit



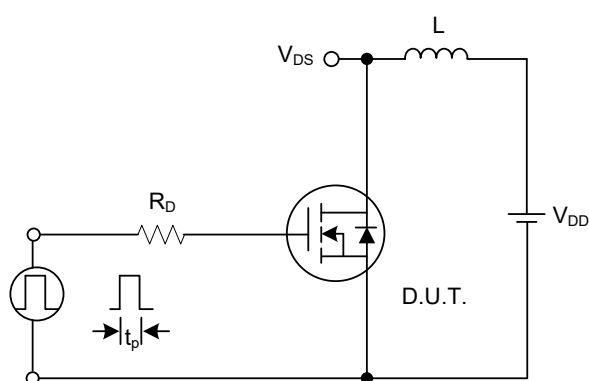
Switching Waveforms



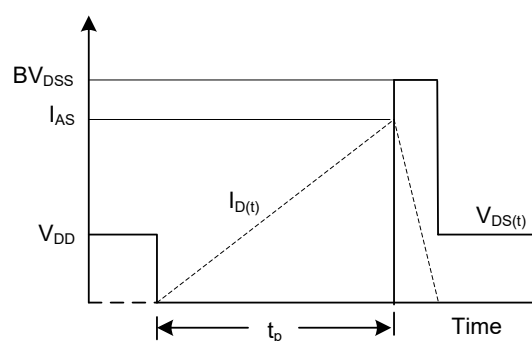
Gate Charge Test Circuit



Charge
Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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