

UTC UNISONIC TECHNOLOGIES CO., LTD

UT03N03LZ **Preliminary Power MOSFET**

0.3A, 30V N-CHANNEL LOGIC LEVEL ENHANCEMENT MODE

DESCRIPTION

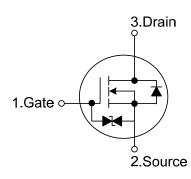
The UT03N03LZ employs advanced MOSFET technology and features low gate charge while maintaining low on-resistance.

Optimized for switching applications, this device improves the overall efficiency of DC/DC converters and allows operation to higher switching frequencies.

FEATURES

- * $R_{DS(ON)} \le 0.8 \Omega$ @ V_{GS} =4.5V, I_D =0.3A $R_{DS(ON)} \le 1.2 \Omega @ V_{GS} = 2.5 V, I_D = 0.15 A$ $R_{DS(ON)} \le 1.5 \Omega @ V_{GS} = 1.8 V, I_D = 0.15 A$
- * Low Capacitance
- * Low Gate Charge
- * Fast Switching Capability
- * Avalanche Energy Specified
- * With ESD protection

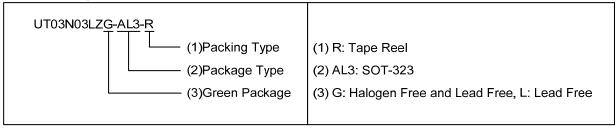
SYMBOL

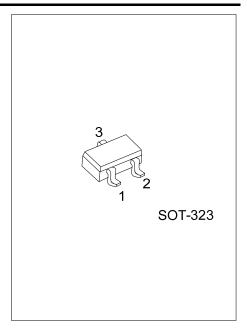


ORDERING INFORMATION

Ordering Number		Desterns	Pin Assignment			Daakina	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UT03N03LZL-AL3-R	UT03N03LZG-AL3-R	SOT-323	G	S	D	Tape Reel	

Note: Pin Assignment: G: Gate S: Source D: Drain





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■ ABSOLUTE MAXIMUM RATINGS (T_A = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	30	V
Gate-Source Voltage		V_{GSS}	±8	V
Oti	DC		0.3	Α
Continuous Drain Current	Pulse	I _D	0.6	Α
Peak Diode Recovery dv/dt(Note 2)		dv/dt	3.7	V/ns
Power Dissipation		P _D	0.2	W
Junction Temperature		TJ	+150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Note: 1.Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. $I_{SD} \le 0.3 A$, di/dt $\le 200 A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting T_J = 25°C

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θја	312	°C/W	

Note: Device mounted on FR-4 substrate Pc board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS (T_A = 25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	30			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V			1	μΑ
Gate–Body Leakage, Forward	Igss	V _{DS} =0V, V _{GS} =±8V			±10	uA
ON CHARACTERISTICS (Note)						
Gate-Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_D=250\mu A$	0.3		1.2	V
	R _{DS(ON)}	V _{GS} =4.5V, I _D =0.3A			8.0	Ω
Static Drain–Source On–Resistance		V _{GS} =2.5V, I _D =0.15A			1.2	Ω
		V _{GS} =1.8V, I _D =0.15A			1.5	Ω
DYNAMIC PARAMETERS						
Input Capacitance	Cıss			18		pF
Output Capacitance	Coss	V _{DS} =15V, V _{GS} =0V, f=1MHz		10		pF
Reverse Transfer Capacitance	Crss			6		pF
SWITCHING PARAMETERS (Note)						
Total Gate Charge	Q_G			8		nC
Gate Source Charge	Q_GS	V _{DS} =24V, V _{GS} =10V, I _D =0.3A		0.8		nC
Gate Drain Charge	Q_GD			0.7		nC
Turn-ON Delay Time	t _{D(ON)}			3		ns
Turn-ON Rise Time	t _R	V _{DD} =15V, I _D =0.3A,V _{GS} =10V,		17		ns
Turn-OFF Delay Time	t _{D(OFF)}	R _G =3.3Ω		57		ns
Turn-OFF Fall-Time	t _F			33		ns
SOURCE- DRAIN DIODE RATINGS AND	CHARACTI	ERISTICS				
Max. Diode Forward Current	Is				0.3	Α
Drain-Source Diode Forward Voltage	V_{SD}	V _{GS} =0V, I _S =0.3A			1.2	V
Reverse Recovery Time	t _{rr}	V _{GS} =0V, I _S =0.3A,		131		ns
Reverse Recovery Charge	Qrr	d _I /d _t =100A/μs		125		nC

Notes: 1. Pulse Test: Pulse width \leq 300µs, Duty cycle \leq 2%.

^{2.} Essentially independent of operating temperature.

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