UNISONIC TECHNOLOGIES CO., LTD

ULV3542

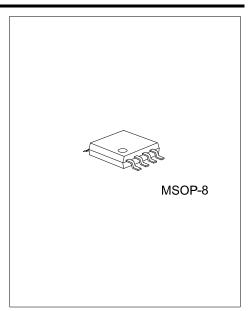
LINEAR INTEGRATED CIRCUIT

160V/µs, RAIL-TO-RAIL I/O, **CMOS OPERATIONAL AMPLIFIER**

DESCRIPTION

The UTC ULV3542 is high-speed, voltage-feedback CMOS operational amplifiers. It is optimized for operation on single or dual supplies as low as 2.7V (±1.35V) and up to 5.5V (±2.75V). Input common mode range extends beyond the supplies. Quiescent current is only 4.5mA/Amplifier.

The UTC ULV3542 is suitable for applications requiring high continuous output current. It is designed for video and other applications which require wide bandwidth. It is unity-gain stable and can provide large output current.



FEATURES

* Supply Voltage: 2.7V ~ 5.5V

* Supply Current/Amplifier: 7 mA (Max.)

* Input Offset Voltage: 10mV (Max.)

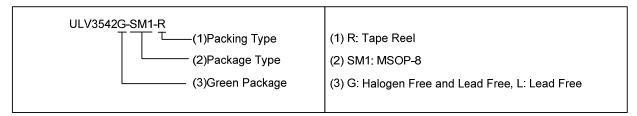
* Rail-to-Rail Input and Output

* Slew Rate: 160V/µs (Typ.)

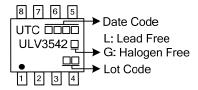
* Thermal Shutdown

ORDERING INFORMATION

Ordering	Number	Daalaana	Packing	
Lead Free	Halogen Free	Package		
ULV3542L-SM1-R	ULV3542G-SM1-R	MSOP-8	Tape Reel	

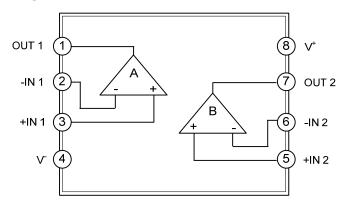


MARKING



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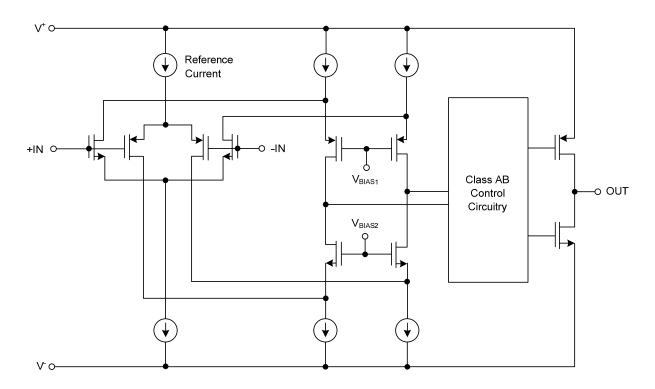
■ PIN CONFIGURATION



■ PIN DESCRIPTION

PIN NO.	PIN NAME	DESCRIPTION		
1	OUT 1	Output of 1 AMP		
2	-IN 1	Inverting input of 1 AMP		
3	+IN 1	Non-inverting input of 1 AMP		
4	V-	Negative power supply		
5	+IN 2	Non-inverting input of 2 AMP		
6	-IN 2	Inverting input of 2 AMP		
7	OUT 2	Output of 2 AMP		
8	V ⁺	Positive power supply		

■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATING

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	(V ⁺ - V ⁻)	6	V
Input Common Mode Voltage Range	V _{CM}	V ⁻ -0.1 ~ V ⁺ +0.1	V
Junction Temperature (Note 3)	TJ	+150	°C
Storage Temperature	T _{STG}	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ RECOMMENDED OPERATING CONDITIONS

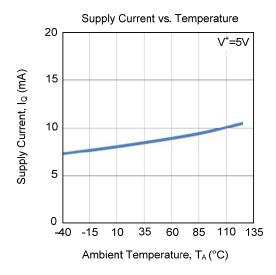
PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage Range	V+ - V-	2.7 ~ 5.5	V
Operating Temperature Range	T _{OPR}	-40 ~ +125	°C

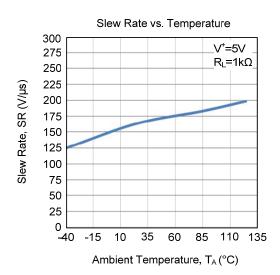
■ ELECTRICAL CHARACTERISTICS

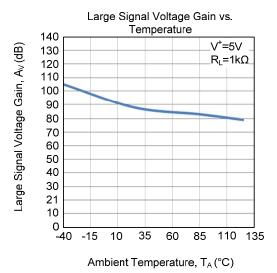
 $(T_A=25^{\circ}C.\ V^{+}=2.7\sim5.5V,\ V^{-}=0V,\ V_{IC}=\ V^{+}/2V,\ V_{O}=\ V^{+}/2V\ and\ R_L\ =1k\Omega\ connected\ to\ GND.)$

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Supply Current/Amplifier	ΙQ				4.3	7	mA
Power Supply Rejection Ratio	PSRR	$2.7V \le V^{+} \le 5.5V, V_{iC}=0V$		70	100		dB
Input Offset Voltage	Vos				-2	±10	mV
Input Bias Current	lΒ				5		pА
Input Offset Current	los				8.0		pА
Common-Mode Voltage Range	V _{CM}			V ⁻ -0.1		V++0.1	V
Common Mode Rejection Ratio	CMRR	$V^{-}-0.1 \le V_{lc} \le V^{+}+0.1$		60	90		dB
Large Signal Voltage Gain	A_V	R _L =1k Ω , V _O = V ⁺ +0.3V ~ V	/+-0.3V	70	85		dB
0.44)/-14	Vo	R _L =1kΩ	Vон	V+-0.09	V+-0.07		V
Output Voltage			V_{OL}		0.005	0.06	V
Short-Circuit Current	Isc	Sourcing, V ⁺ =3V, V _O =0V			90		mA
		Sinking, V ⁺ =3V V _O = V ⁺			90		mA
Slew Rate	SR	V ⁺ =5V, V ₀ =2V _{PP}			160		V/µs
Gain-Bandwidth Product	GBW	$G = +10, V_O = 100 \text{mV}_{PP}$			100		MHz
Input-Referred Voltage Noise	en	f=1MHz			10		nV/ √Hz
Input-Referred Current Noise	i _n	f=1MHz			20		fA/ √Hz
Thermal Shutdown					150		°C

■ TYPICAL CHARACTERISTICS







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