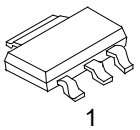
USSP4041

Preliminary ***PNP EPITAXIAL SILICON TRANSISTOR***

-60V, -5.7A PNP LOW $V_{CE(SAT)}$ TRANSISTOR



SOT-223

■ FEATURES

* Low collector-emitter saturation voltage $V_{CE(SAT)}$
* High collector current capability: I_C and I_{CM}
* Higher efficiency leading to less heat generation
* Reduced printed-circuit board requirements
* Complement: USSN4041

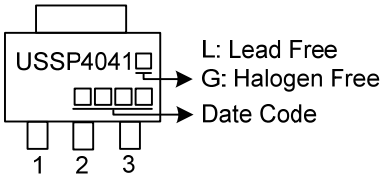
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
USSP4041L-AA3-R	USSP4041G-AA3-R	SOT-223	B	C	E	Tape Reel

Note: Pin Assignment: B: Base C: Collector E: Emitter

USSP4041G-AA3-R (1)Packing Type (2)Package Type (3)Green Package	(1) R: Tape Reel (2) AA3: SOT-223 (3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING



■ ABSOLUTE MAXIMUM RATING (T_A=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Base Voltage		V_{CBO}	-60	V
Collector-Emitter Voltage		V_{CEO}	-60	V
Emitter-Base Voltage		V_{EBO}	-5	V
Collector Current	DC	I_C	-5.7	A
	Peak	I_{CM}	-15	A
Power Dissipation ($T_A \le 25°C$)		P_D	1	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-65 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Device mounted on a FR4 printed-circuit board, single-sided copper; tin-plated, mounting pad for collector 1 cm^2.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	125	°C/W
Junction to Case	θ_{JC}	10	°C/W

Note: Device mounted on a FR4 printed-circuit board, single-sided copper; tin-plated, mounting pad for collector 1 cm^2.

■ ELECTRICAL CHARACTERISTICS (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector to Base Breakdown Voltage	BV_{CBO}	I_C=-100μA, I_E=0	-60			V
Collector to Emitter Breakdown Voltage	BV_{CEO}	I_C=-1mA, I_B=0	-60			V
Emitter to Base Breakdown Voltage	BV_{EBO}	I_E=-100μA, I_C=0	-5			V
Collector Cut-Off Current	I_{CBO}	V_{CB}=-20V, I_E=0			-100	nA
		V_{CB}=-20V, I_E=0, T_J=150°C			-50	μA
Collector Cut-Off Current	I_{CES}	V_{CE}=-10V, V_{BE}=0			-100	nA
Emitter Cut-Off Current	I_{EBO}	V_{EB}=-5V, I_C=0			-100	nA
Collector-Emitter Saturation Voltage (Note)	$V_{CE(SAT)}$	I_C=-1A, I_B=-50mA		-38	-63	mV
		I_C=-1A, I_B=-10mA		-80	-125	mV
		I_C=-2A, I_B=-40mA		-90	-150	mV
		I_C=-4A, I_B=-200mA		-110	-195	mV
		I_C=-4A, I_B=-400mA		-95	-175	mV
		I_C=-6A, I_B=-300mA		-165	-285	mV
Base-Emitter Saturation Voltage (Note)	$V_{BE(SAT)}$	I_C=-1A, I_B=-100mA		-0.8	-0.9	V
		I_C=-4A, I_B=-400mA		-1.08	-1.2	V
Base-Emitter Turn-On Voltage (Note)	$V_{BE(ON)}$	V_{CE}=-2V, I_C=-2A		-0.73	-0.85	V
Dc Current Gain (Note)	h_{FE}	V_{CE}=-2V, I_C=-500mA	200	340		
		V_{CE}=-2V, I_C=-1.0A	200	330		
		V_{CE}=-2V, I_C=-2.0A	200	310		
		V_{CE}=-2V, I_C=-4.0A	150	270		
		V_{CE}=-2V, I_C=-6.0A	120	240		

Note: Pulse test: $t_P \le$ 300μs, Duty cycle ≤ 2%.