



**UTG10N135LX1**

Preliminary

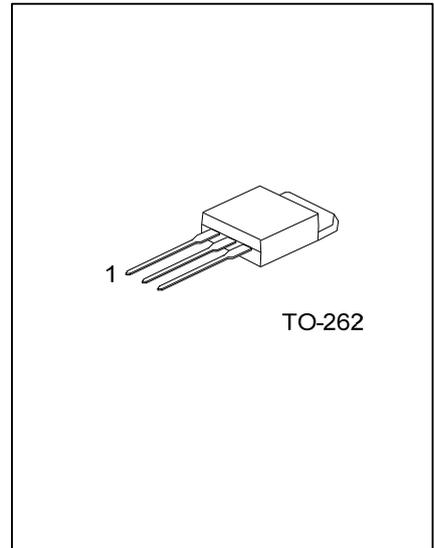
*Insulated Gate Bipolar Transistor*

**1350V TRENCH GATE  
FIELD-STOP IGBT**

■ DESCRIPTION

The UTC **UTG10N135LX1** is an Trench Field-Stop Insulated Gate Bipolar Transistor. it uses UTC's advanced technology to provide customers with high switching speed, low saturation voltage and low switching loss, etc.

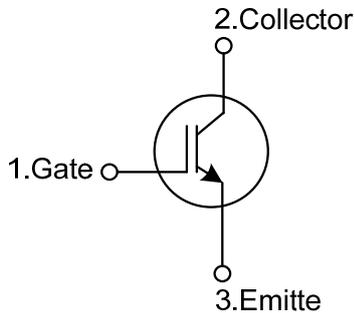
The UTC **UTG10N135LX1** is suitable for the resonant or soft switching applications.



■ FEATURES

- \* High switching speed
- \* High avalanche ruggedness
- \* Low saturation voltage:  $V_{CE(SAT),Typ.} = 1.6V @ I_C=10A, V_{GE}=15V$   
( $T_C = 25^\circ C$ )

■ SYMBOL



■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UTG10N135LX1L-T2Q-T	UTG10N135LX1G-T2Q-T	TO-262	G	C	E	Tube

Note: Pin Assignment: G: Gate C: Collector E: Emitter

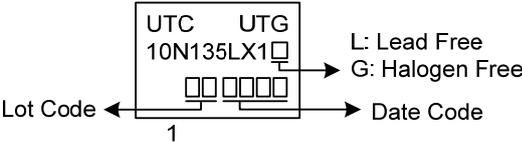
<p>UTG10N135LX1G-T2Q-T</p>	<p>(1) Packing Type (1) T: Tube</p> <p>(2) Package Type (2) T2Q: TO-262</p> <p>(3) Green Package (3) G: Halogen Free and Lead Free, L: Lead Free</p>
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector-Emitter Voltage	V <sub>CES</sub>	1350	V
Gate-Emitter Voltage	V <sub>GES</sub>	±20	V
Transient Gate-emitter voltage (t <sub>p</sub> < 5 ms)		±25	V
Continuous Collector Current	I <sub>C</sub>	T <sub>C</sub> =25°C	20
		T <sub>C</sub> =100°C	10
Collector Current Pulsed (Note 1)	I <sub>CM</sub>	40	A
Short Circuit Withstand Time V <sub>GE</sub> = 15V, V <sub>CC</sub> ≤ 200V Allowed number of short circuits < 1000 Time between short circuits: ≥ 1.0s T <sub>VJ</sub> = 25°C	t <sub>SC</sub>	8	μs
Power Dissipation (T <sub>C</sub> =25°C)	P <sub>D</sub>	100	W
Operating Junction Temperature	T <sub>J</sub>	-40 ~ +150	°C
Storage Temperature Range	T <sub>STG</sub>	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

Absolute maximum ratings are those values beyond which the device could be permanently damaged.

2. Pulse width limited by maximum junction temperature.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Case	θ <sub>JC</sub>	1.25	°C/W

■ ELECTRICAL CHARACTERISTICS (T<sub>C</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>Off Characteristics</b>						
Collector-Emitter Breakdown Voltage	BV <sub>CES</sub>		1350			V
Collector Cut-Off Current	I <sub>CES</sub>	V <sub>CE</sub> =1350V, V <sub>GE</sub> =0V			5	μA
G-E Leakage Current	I <sub>GES</sub>	V <sub>CE</sub> =0V, V <sub>GE</sub> =±20V			±100	nA
<b>On Characteristics</b>						
Gate to Emitter Threshold Voltage	V <sub>GE(TH)</sub>	I <sub>C</sub> =250μA, V <sub>CE</sub> =V <sub>GE</sub>	4.5		7.5	V
Collector to Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	I <sub>C</sub> =10A, V <sub>GE</sub> =15V	T <sub>C</sub> =25°C	1.6	2.0	V
			T <sub>C</sub> =125°C	2		V
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>IES</sub>	V <sub>CE</sub> =25V, V <sub>GE</sub> =0V, f=1MHz		910		pF
Output Capacitance	C <sub>OES</sub>			85		pF
Reverse Transfer Capacitance	C <sub>RES</sub>			52		pF
<b>Switching Characteristics</b>						
Total Gate Charge	Q <sub>G</sub>	V <sub>CE</sub> =600V, I <sub>C</sub> =10A, V <sub>GE</sub> =15V		68		nC
Gate-Emitter Charge	Q <sub>GE</sub>			16		nC
Gate-Collector Charge	Q <sub>GC</sub>			43		nC
Turn-On Delay Time	t <sub>DON</sub>	V <sub>CC</sub> =600V, I <sub>C</sub> =10A, R <sub>G</sub> =5Ω, V <sub>GE</sub> =0~15V, L=500μH		8.3		ns
Rise Time	t <sub>R</sub>			16		ns
Turn-Off Delay Time	t <sub>DOFF</sub>			68		ns
Fall Time	t <sub>F</sub>			311		ns
Turn-On Switching Loss	E <sub>ON</sub>			0.7		mJ
Turn-Off Switching Loss	E <sub>OFF</sub>			0.9		mJ

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