



## F11NM65

Power MOSFET

### 11A, 650V N-CHANNEL SUPER-JUNCTION MOSFET

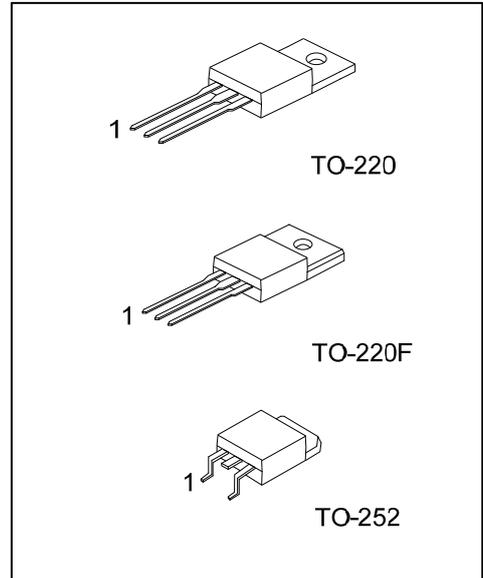
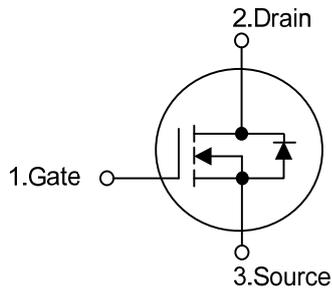
#### DESCRIPTION

The **UTC F11NM65** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

#### FEATURES

- \*  $R_{DS(ON)} \leq 0.42 \Omega @ V_{GS}=10V, I_D=2.75A$
- \* High switching Speed
- \* 100% avalanche tested
- \* Improved dv/dt capability

#### SYMBOL



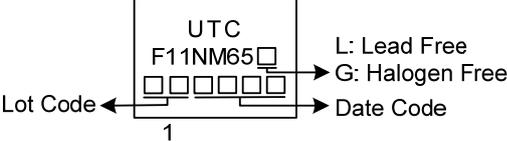
#### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
F11NM65L-TA3-T	F11NM65G-TA3-T	TO-220	G	D	S	Tube
F11NM65L-TF3-T	F11NM65G-TF3-T	TO-220F	G	D	S	Tube
F11NM65L-TN3-R	F11NM65G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>F11NM65G-TA3-T</p>	<p>(1) T: Tube, R: Tape Reel</p> <p>(2) TA3: TO-220, TF3: TO-220F, TN3: TO-252</p> <p>(3) G: Halogen Free and Lead Free L: Lead Free</p>
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MARKING



■ ABSOLUTE MAXIMUM RATINGS (T<sub>c</sub>=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V <sub>DSS</sub>	650	V
Gate-Source Voltage		V <sub>GSS</sub>	±30	V
Drain Current (T <sub>c</sub> =25°C)	Continuous	I <sub>D</sub>	11	A
	Pulsed (Note 2)	I <sub>DM</sub>	22	A
Avalanche Energy	Single Pulsed (Note 3)	E <sub>AS</sub>	190	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	15	V/ns
Power Dissipation	TO-220	P <sub>D</sub>	92	W
	TO-220F		30	W
	TO-252		60	W
Junction Temperature		T <sub>J</sub>	+150	°C
Storage Temperature		T <sub>STG</sub>	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L = 100mH, I<sub>AS</sub> = 1.95A, V<sub>DD</sub> = 50V, R<sub>G</sub> = 25Ω, Starting T<sub>J</sub> = 25°C.

4. I<sub>SD</sub> ≤ 11A, di/dt ≤ 200A/μs, V<sub>DD</sub> ≤ BV<sub>DSS</sub>, Starting T<sub>J</sub> = 25°C.

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F	θ <sub>JA</sub>	62.5	°C/W
	TO-252		110	°C/W
Junction to Case	TO-220	θ <sub>JC</sub>	1.35	°C/W
	TO-220F		4.17	°C/W
	TO-252		2.08 (Note)	°C/W

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

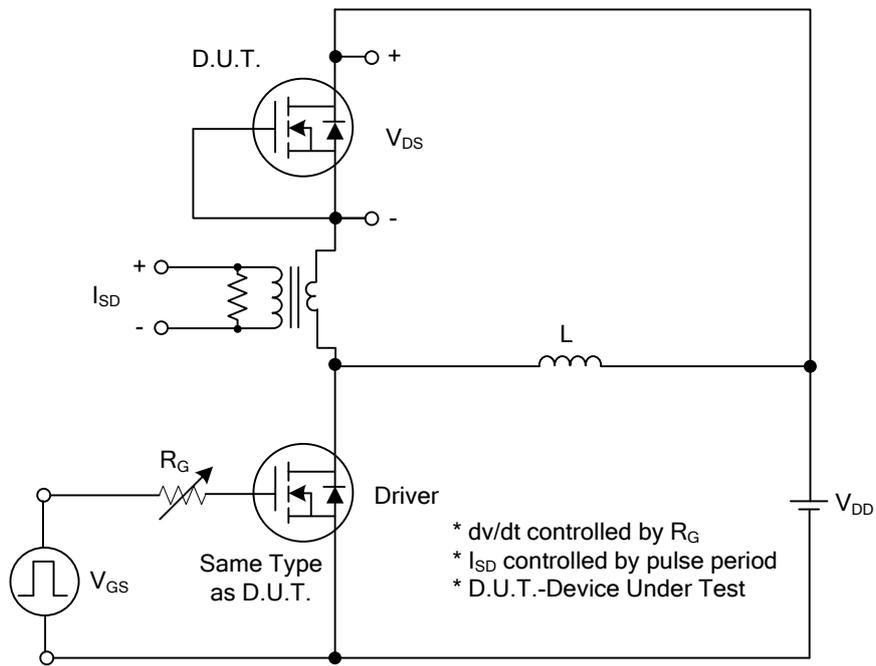
■ ELECTRICAL CHARACTERISTICS ( $T_J=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$I_D=250\mu\text{A}$ , $V_{GS}=0\text{V}$	650			V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS}=650\text{V}$ , $V_{GS}=0\text{V}$			10	$\mu\text{A}$
Gate- Source Leakage Current	Forward	$V_{GS}=+30\text{V}$			+100	nA
	Reverse	$V_{GS}=-30\text{V}$			-100	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}$ , $I_D=250\mu\text{A}$	2.5		4.5	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10\text{V}$ , $I_D=2.75\text{A}$			0.42	$\Omega$
<b>DYNAMIC PARAMETERS</b>						
Input Capacitance	$C_{ISS}$	$V_{GS}=0\text{V}$ , $V_{DS}=50\text{V}$ , $f=1.0\text{MHz}$		730		pF
Output Capacitance	$C_{OSS}$			140		pF
Reverse Transfer Capacitance	$C_{RSS}$			8		pF
<b>SWITCHING PARAMETERS</b>						
Total Gate Charge (Note 2)	$Q_G$	$V_{DS}=520\text{V}$ , $V_{GS}=10\text{V}$ , $I_D=11\text{A}$ (Note 1, 2)		36		nC
Gate to Source Charge	$Q_{GS}$			9		nC
Gate to Drain Charge	$Q_{GD}$			15		nC
Turn-ON Delay Time (Note 2)	$t_{D(ON)}$	$V_{DD}=100\text{V}$ , $V_{GS}=10\text{V}$ , $I_D=11\text{A}$ , $R_G=25\Omega$ (Note 1, 2)		11		ns
Rise Time	$t_R$			28		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			95		ns
Fall-Time	$t_F$			52		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_S$				11	A
Maximum Pulsed Drain-Source Diode Forward Current (Note 1)	$I_{SM}$				22	A
Drain-Source Diode Forward Voltage (Note 2)	$V_{SD}$	$I_S=11\text{A}$ , $V_{GS}=0\text{V}$			1.4	V
Reverse Recovery Time	$t_{rr}$	$I_S=11\text{A}$ , $V_{GS}=0\text{V}$ ,		175		ns
Reverse Recovery Charge (Note 1)	$Q_{rr}$	$di/dt = 100\text{A}/\mu\text{s}$		1180		nC

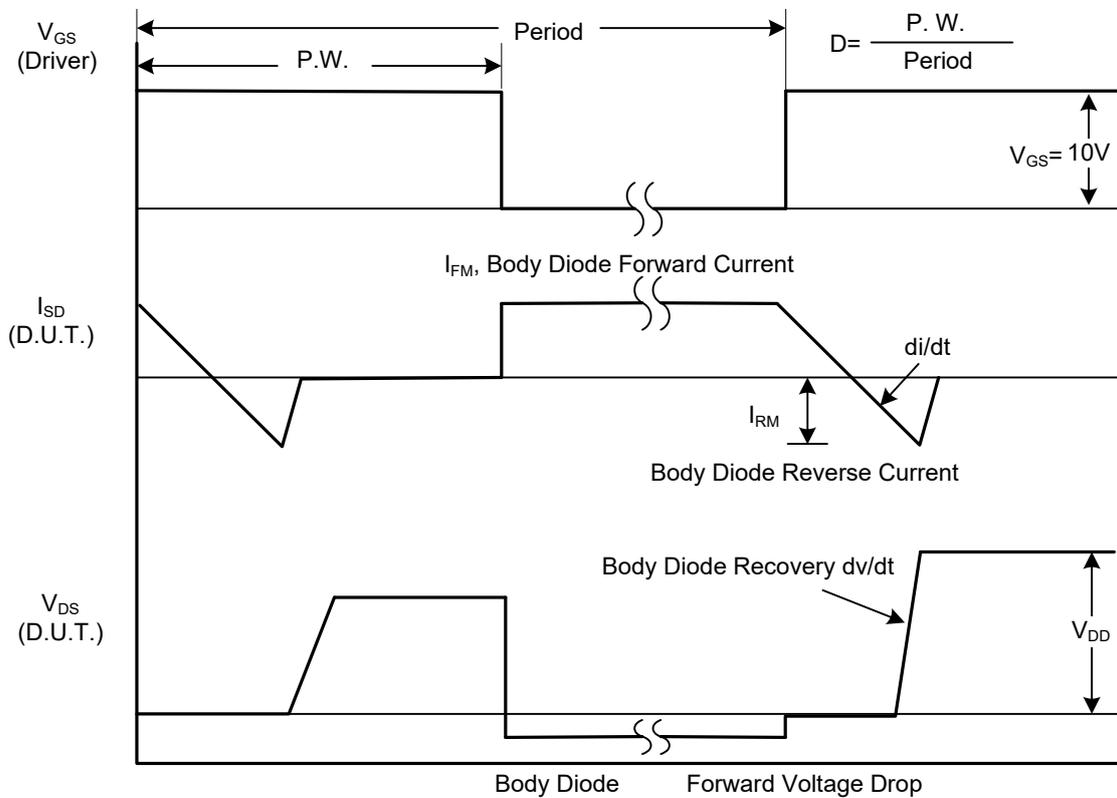
Notes: 1. Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$

2. Essentially independent of operating temperature

## TEST CIRCUITS AND WAVEFORMS

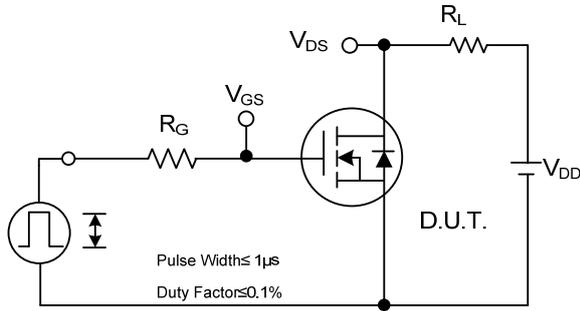


Peak Diode Recovery dv/dt Test Circuit

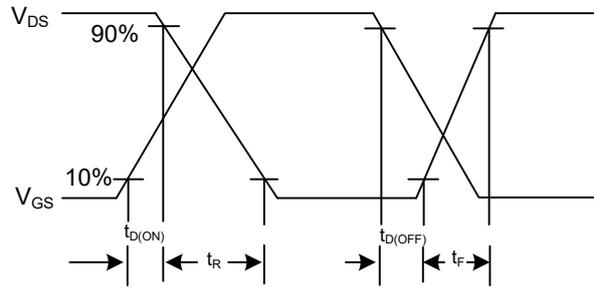


Peak Diode Recovery dv/dt Waveforms

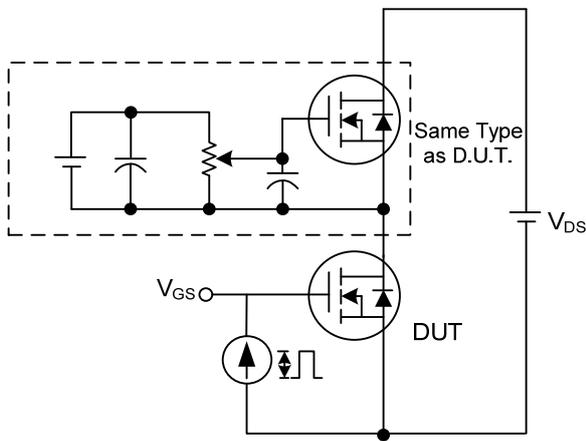
## TEST CIRCUITS AND WAVEFORMS



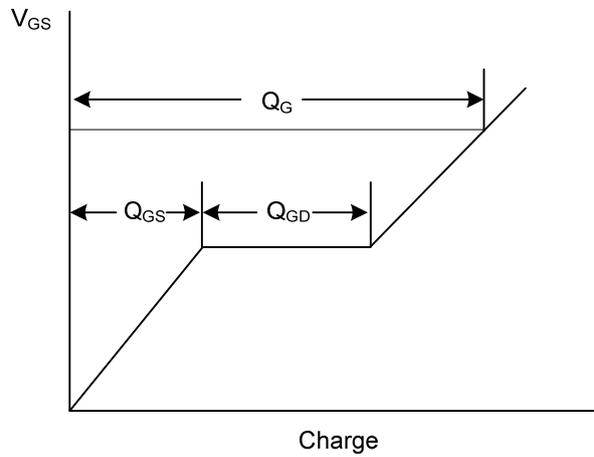
**Switching Test Circuit**



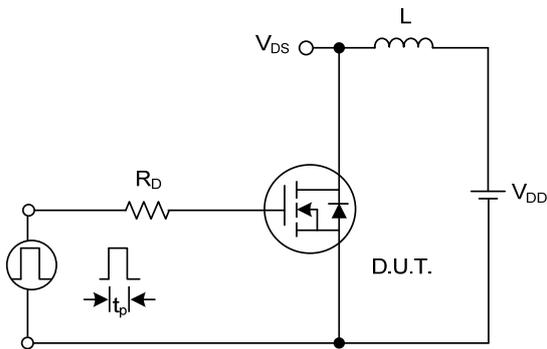
**Switching Waveforms**



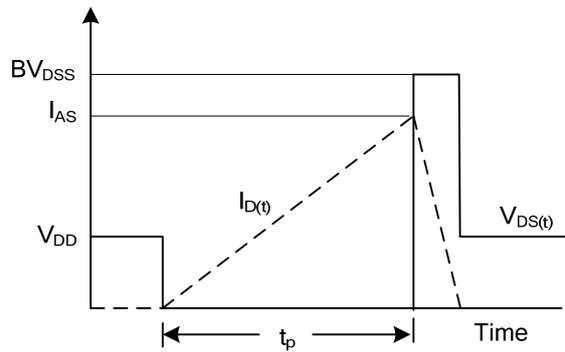
**Gate Charge Test Circuit**



**Gate Charge Waveform**

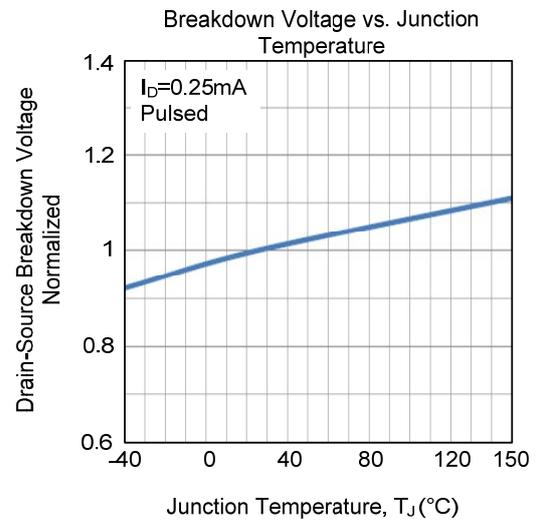
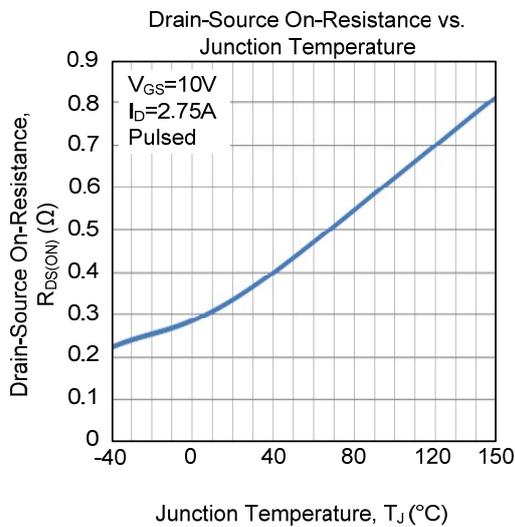
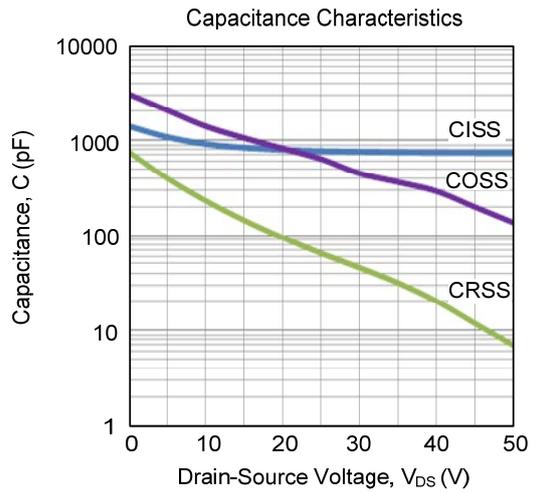
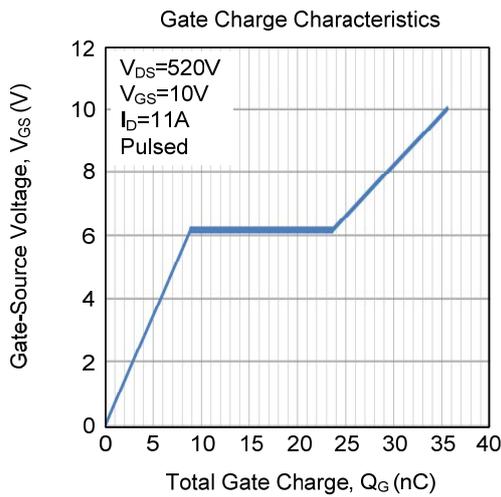
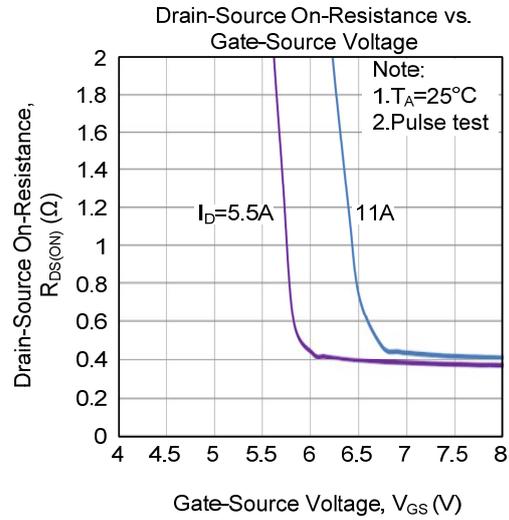
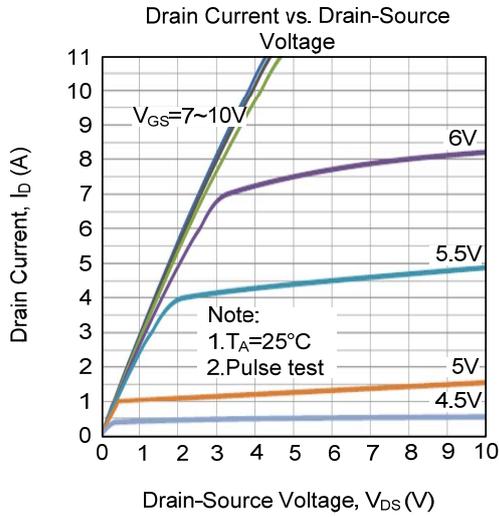


**Unclamped Inductive Switching Test Circuit**

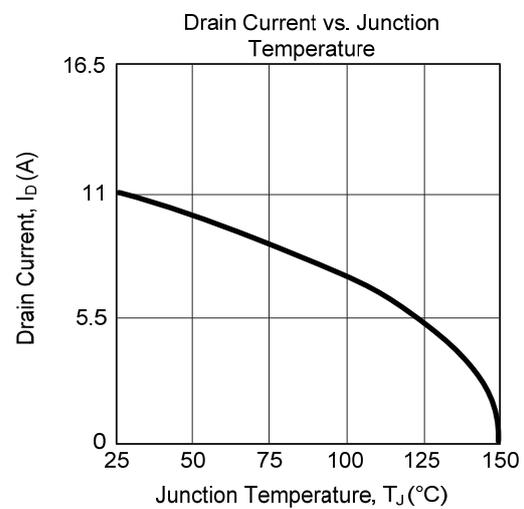
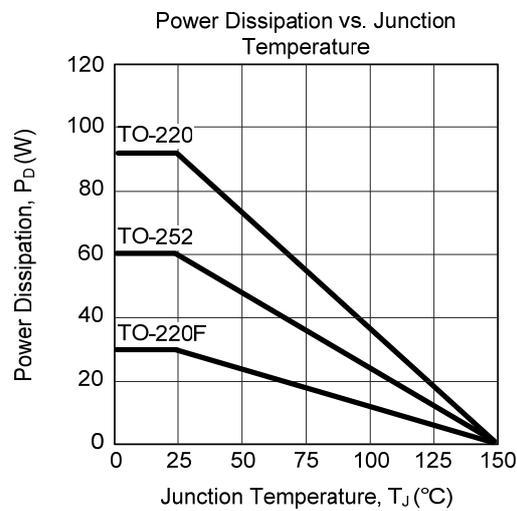
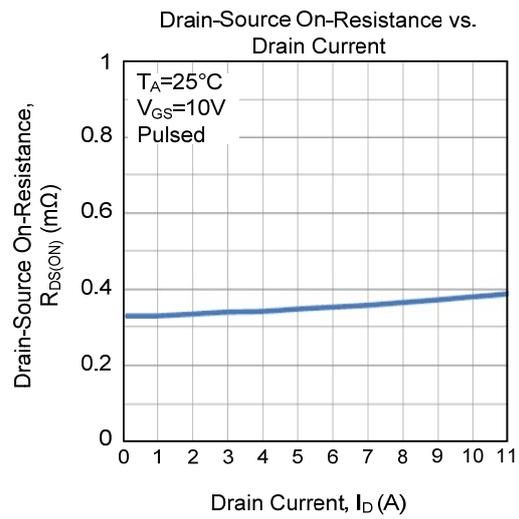
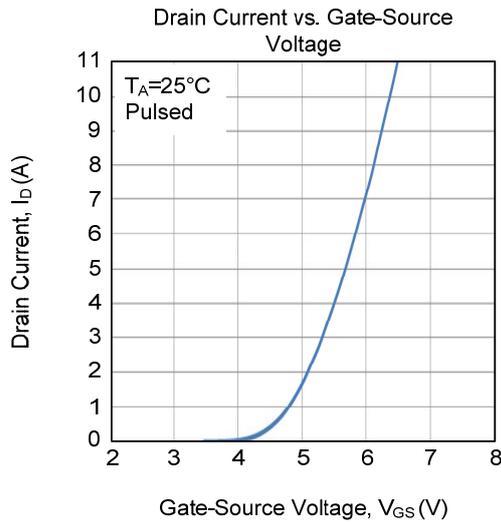
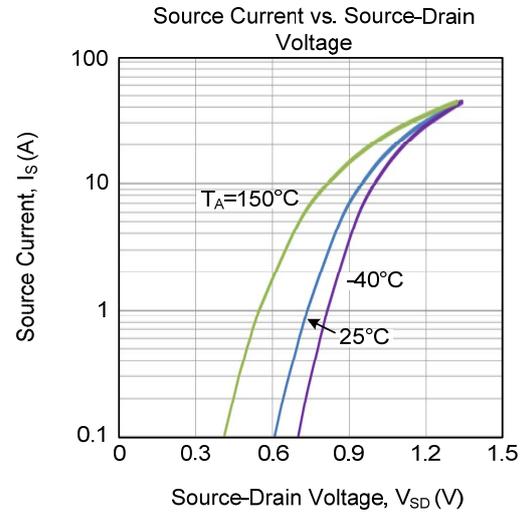
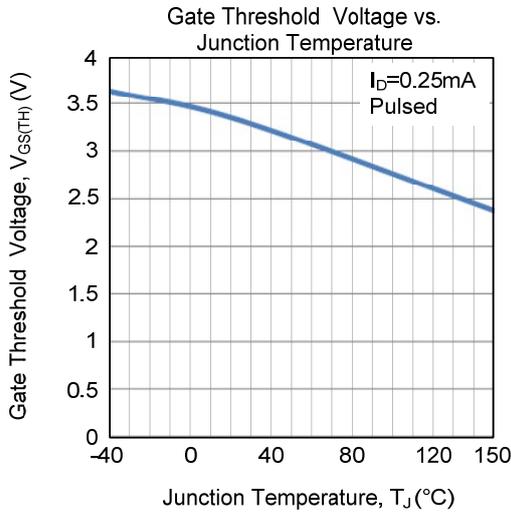


**Unclamped Inductive Switching Waveforms**

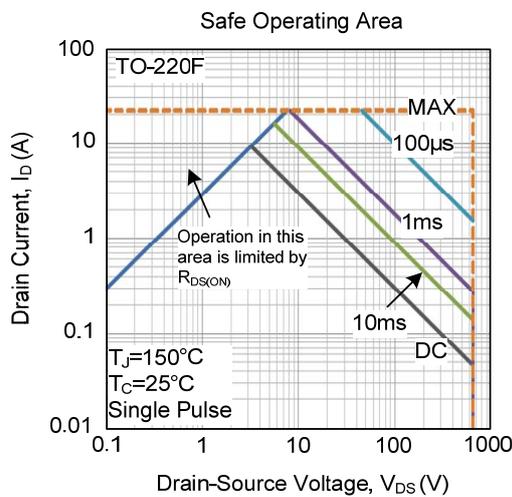
## TYPICAL CHARACTERISTICS



### ■ TYPICAL CHARACTERISTICS (Cont.)



### ■ TYPICAL CHARACTERISTICS (Cont.)



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